

DISK PROCESSING UNIT

Model: 2280

Customer Engineering Product Maintenance Manual

PREFACE

The purpose of this manual is to provide the Wang-trained Customer Engineer (CE) with instructions to operate, troubleshoot and repair the Disk Processing Unit Model 2280.

Second Edition (August 1984)

This edition is the converted number for and obsoletes document number 729-0971. It also incorporates Publication Update Bulletin (PUB), 729-0971-1. This edition of the manual may be used only for the purpose stated in the Preface. Updates and /or changes to this document will be published as Publication Update Bulletins (PUB's) or subsequent editions.

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REASON FOR CHANGE:

This PUB corrects a previous error by reversing the photos in figures 2-8, and 2-9 for the proper sequence, enhancing figures 2-12 and 2-13, and adding figures 2-12A and 2-13A.

INSTRUCTIONS:

Remove pages and insert attached pages as follows:

	REMOVE	INSERT	_
1.	2-11, 2-12	2-11, 2-12	-
2.	2-15, 2-16	2-15, 2-16	
3.	2-17, 2-18	2-17, 2-17A	-
4.	1	2-17B	i
5.	1	2-17D, 2-18	-
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This page is to be used as a permanent record of revisions; place it directly following the title page.



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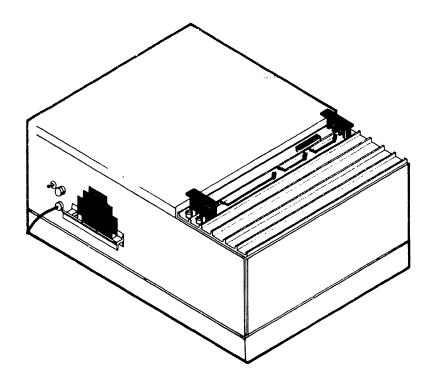
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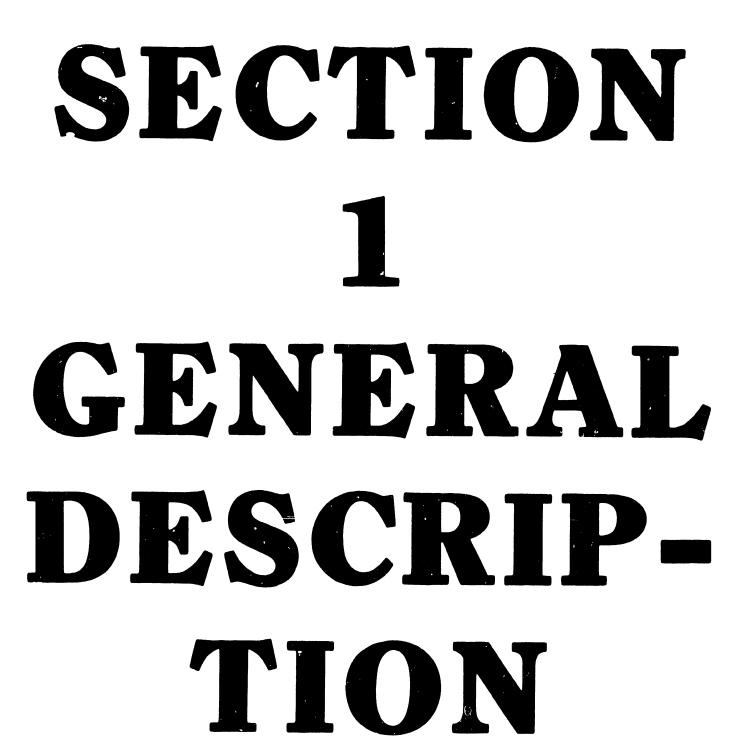
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SECTION 1

INTRODUCTION

1.1 PURPOSE AND SCOPE

The purpose of this Product Maintenance Manual (PMM) is to provide sufficient information for the Wang CE to install and maintain the Model 2280 Disk Processor. The information is presented as follows:

<u>Section</u>	<u>Title</u>
1 2 3 4 5	INTRODUCTION INSTALLATION OPERATION MAINTENANCE THEORY OF OPERATION
Appendix A Appendix B Appendix C Appendix D	DISK SECTOR LAYOUT BILL OF MATERIAL

1.2 RELATED PUBLICATIONS

Following is a list of documentation categories referenced by this PMM. Documentation from these other categories is required for the performance of certain installation/maintenance tasks.

Phoenix Disk Drive -- III.A.7 Disk Diagnostics -- IV.C.1 2280 DPU-to-2280 DPU/MUX Conversion -- I.B.2 22C03, 22C11, 22C32 Disk I/O Controllers -- IV.B.1

1.3 FUNCTIONAL DESCRIPTION

The 2280 DPU (Disk Processing Unit) controls all disk drive operations (such as reading, writing, and head positioning) for the CDC Model 9448 Cartridge Module Disk Drive (CMD)—commonly called the "Phoenix"—when the drive is a component in a 2200VP/LVF/MVP/SVP Computer System. The DPU permits two Phoenix drives to be daisy-chained, providing up to 162 megabytes (approximate) of on-line storage. The 2280 processor (WL No. 187-2200-80) consists of nine printed circuit boards and a motherboard contained in a 2200S chassis. This chassis is housed (located) in the bottom of the Phoenix cabinet/stand. All printed circuit boards are defined in section 2.5.2.

The 2280 DPU connects to a 22C03, 22C11, or 22C32 disk I/O controller in the CPU via a 12-ft (3.6-m) twisted pair cable. The processor connects to the disk drive via two 15-ft (4.5-m) ribbon cables. Figure 1-1 shows a dual disk drive 2200/2280 system configuration.

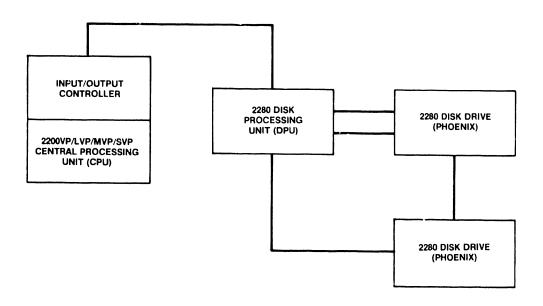


Figure 1-1. 2200/2280 System Configuration

1.4 SPECIFICATIONS

The specifications for the 2280 DPU system are listed below as follows:

Physical Dimensions

```
Height - 9 in. (23 cm)
Width - 16 in. (41 cm)
Depth - 21 in. (53 cm)
```

Power Requirements

```
115 or 230 Vac \pm 10\%
50 or 60 Hz \pm 1.0 Hz
230 Watts
```

Fuses

```
3.0 A (SB) for 115 Vac
1.5 A (SB) for 230 Vac
```

Operating Environment

```
Temperature - 60^{\circ}F to 80^{\circ}F (15°C to 27°C)
Relative Humidity - 40% to 60\% (noncondensing)
```

Heat Output

```
787 BTU/hr (197 Kcal/hr)
```

Cables

```
Power - 10 ft (3.0 m)
Data - 15 ft (4.5 m)
```

SECTION INSTAL-LATION

SECTION 2

INSTALLATION

2.1 SITE PREPARATION

For information concerning preinstallation site planning and preparations, refer to the corporate "Customer Site Planning Guide" WL No. 700-5978, its updates, and CE documentation category I.A.7.

2.2 PRE-UNPACKING INSPECTION

Before unpacking the 2280 DPU, check the packing slip to ensure that the proper equipment has been delivered. After checking the slip, visually inspect the container carefully for any indications of possible shipping damage (crushed edges or corners, puncture holes, tears, etc.). If any damage is noted, file an appropriate claim promptly with the carrier involved and notify the WLI Distribution Center (Department 90), Quality Assurance Department, of the nature and extent of the damage, making arrangements for equipment replacement, if necessary.

2.3 UNPACKING INSTRUCTIONS

- a. Using an x-acto knife, cut the tape securing the shipping carton cover.
- b. Open the box and remove the instapack covering the top of the unit.
- c. Remove the unit from the carton.
- d. Save the shipping carton and protective padding for use when reshipping the unit.

2.4 PRE-INSTALLATION INSPECTION

- a. Remove the top cover from the unit (see section 4.5).
- b. Inspect the DPU chassis for damaged or loosened assemblies. Also check for loose hardware or debris. If any damage is noted, notify the WLI Distribution Center (Department 90), Quality Assurance Department, of the nature and extent of the damage, making arrangements for equipment replacement, if necessary.
- c. Thoroughly clean the unit. Use a soft bristle brush and a vacuum cleaner to remove dust from the inside of the unit. Use a mild detergent and a soft cloth or sponge to remove dirt and grime from the chassis. Do not use abrasive or corrosive materials.

2.5 INITIAL SETUP

This section consists of:

- a. ac input voltage selection information
- b. photographs of the circuit boards giving all pertinent information concerning each board (if applicable) along with motherboard loading
- c. an explanation of system interconnection cabling.
- d. device address selection information.

Section 2.6 (Installation Procedure) helps link together the various information items contained in this section.

2.5.1 Motherboard AC Input Voltage Selection Jumpers

Jumper wires are provided on the motherboard for ac input voltage selection (115V or 230V). Two jumpers are installed for 115Vac and one jumper for 230Vac. Figure 2-1 shows the positions of these jumpers. Be certain the jumper configuration is correct for the supplied ac voltage (see table 2-1).

TABLE 2-1 Voltage Selection Jumpers

JUMPER	115 VA C	230VAC
A	IN	OUT
В	OUT	IN
С	IN	OUT

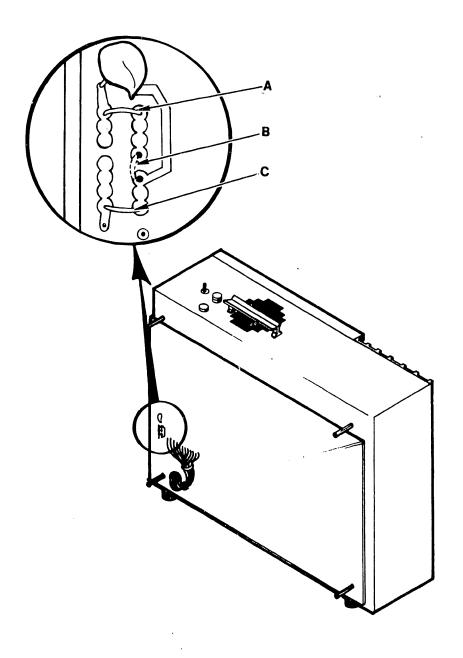


Figure 2-1. AC Input Voltage Selection Jumpers

2.5.2 Circuit Boards

Figures 2-2 through 2-9, 2-11, and 4-1 illustrate the individual printed circuit boards that comprise the 2280 DPU. The specific reference to each figure is as follows:

WL Number	Nomenclature	Figure
210-7415	Prime Circuit Board	2-2
210-7421-A	ALU/MUX Interface Board	2-3
210-7422	ECC/Device Interface Board	2-4
210-7423-A	RAM/PROM Control Board	2 - 5
210-7424	I/O Controller Board	2-6
210-7715	2280 MUX Disk Conntoller	2 - 7
210-7716	Motherboard	2-11
210-7717	2280 MUX Master	2 - 8
210-7718	2280 MUX Slave	2-9
210-L567	Regulator Board	4-1

2.5.3 Motherboard/PCB Layout

The locations of the 2280 DPU circuit boards in relation to the motherboard/chassis are shown in figures 2-10 and 2-11. Ensure that all fingerboard connectors are clean prior to installing the boards in the DPU. (An ink eraser should be used to clean the pins if necessary.)

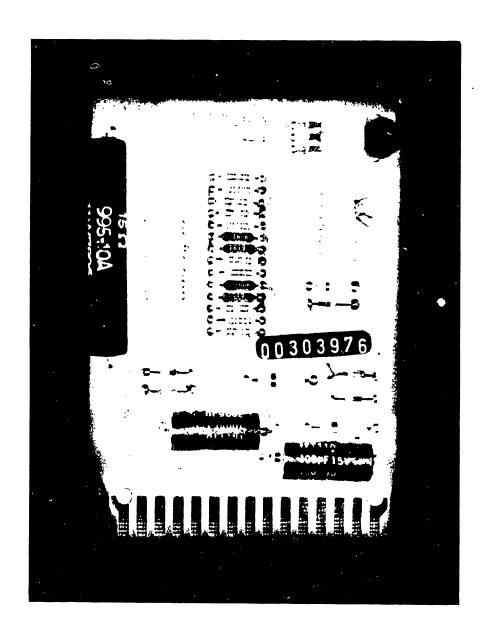
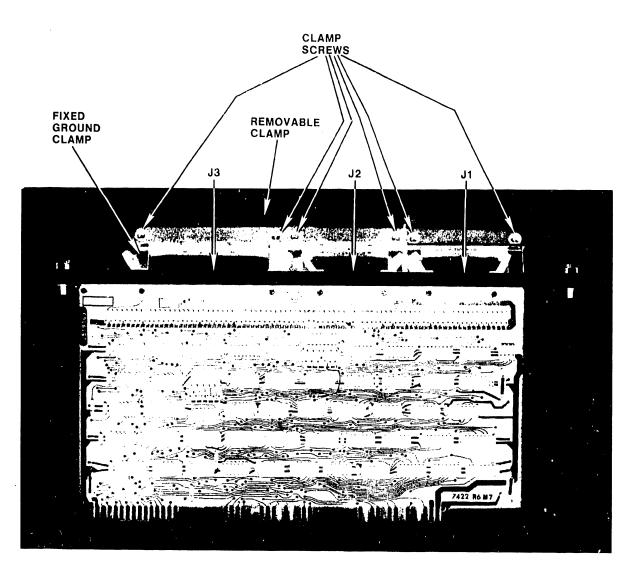


Figure 2-2. WL No. 210-7415 Prime Circuit Board

Sijijiyalibiini 2

J1 - CONNECTS TO I/O CONTROLLER IN CPU VIA 220-0105-2 CABLE

Figure 2-3. WL No. 210-7421-A ALU/MUX Interface Board



J1 AND J2 CONNECT TO PHOENIX EM2 BOARDS VIA 220-3033-36 CABLE

J3 CONNECT TO PHOENIX EM1 BOARD VIA 220-3041-22 CABLE

Figure 2-4. WL No. 210-7422 ECC/Device Interface Board

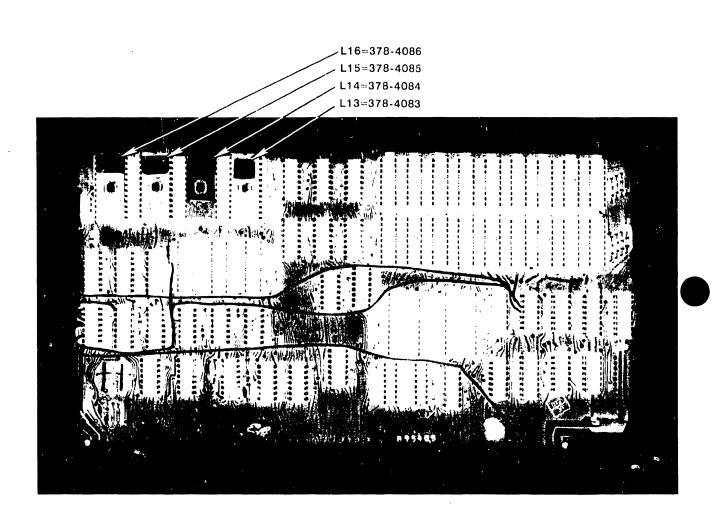


Figure 2-5. WL No. 210-7423-A RAM/PROM Control Board

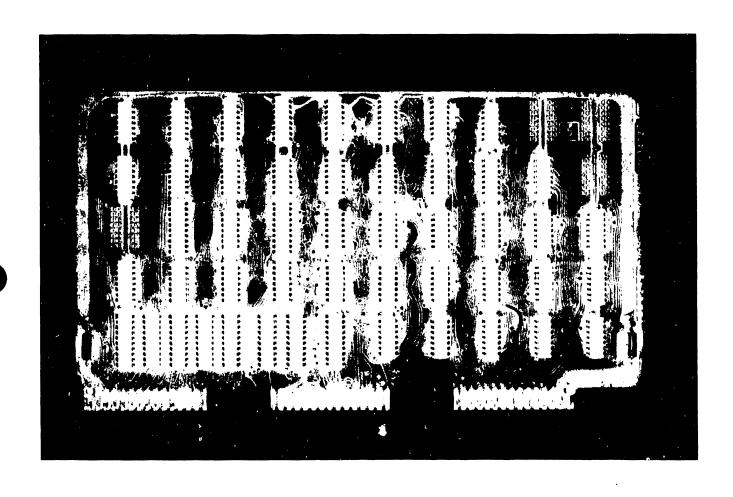


Figure 2-6. WL No. 210-7424 I/O Controller Board

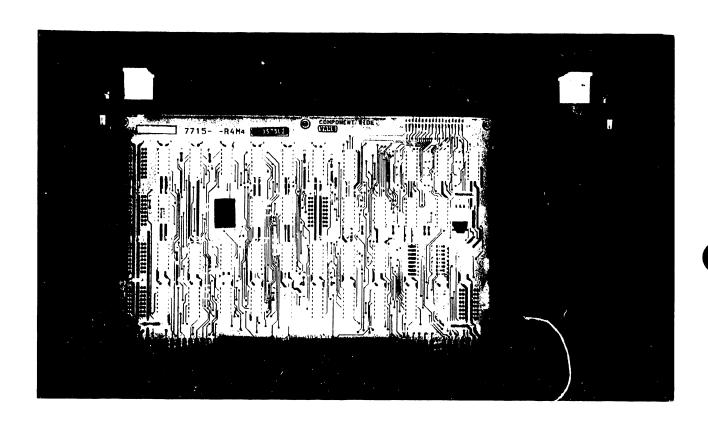
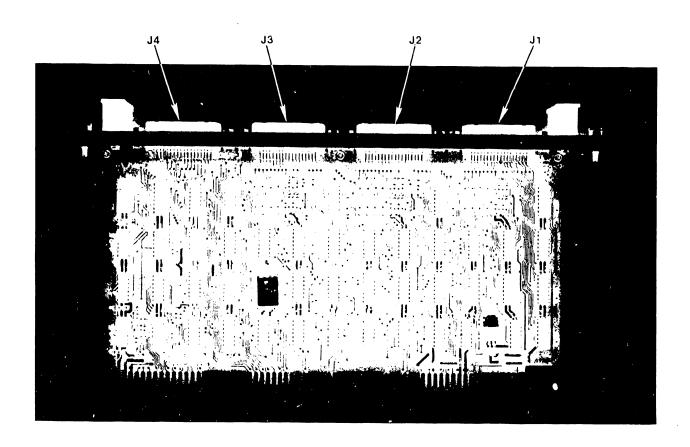
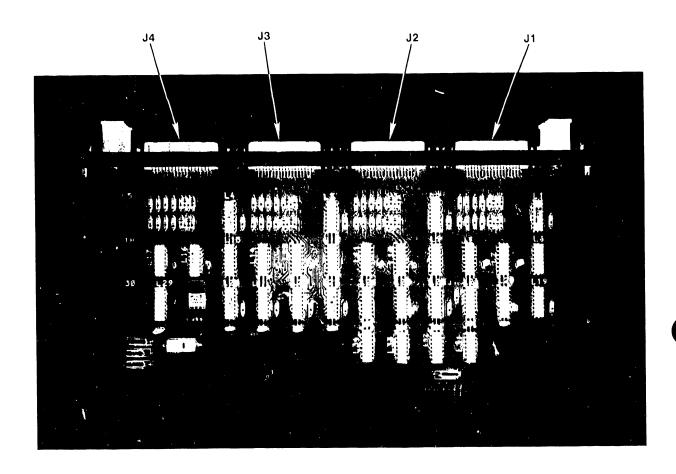


Figure 2-7. WL No. 210-7715 2280 MUX Disk Controller Board



J1 THROUGH J4 ARE EACH CONNECTED TO THE 7715 PCB IN THEIR RESPECTIVE CPU VIA 220-0365 CABLE

Figure 2-8. WL No. 210-7717 2280 MUX Master Board



- 1. J1,J2,AND J3 ARE EACH CONNECTED TO THE 7715 PCB IN THEIR RESPECTIVE CPU VIA 220-0365 CABLE
- 2. J4 IS CONNECTED TO THE 7421 PCB IN ITS RESPECTIVE CPU VIA 220-0360 CABLE

Figure 2-9. WL No. 210-7718 MUX Slave Board

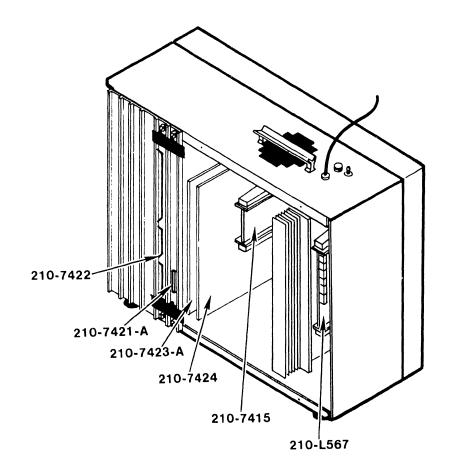


Figure 2-10. Circuit Board Loading

210-7718 210-7717 210-7422 210-7421A 210-7423A 210-7424 210-7415 210-L567

Figure 2-11. WL No. 210-7716 Motherboard

2.5.4 Input/Output Cable Connections (see figure 2-12)

Refer to documentation category 3105 for Phoenix disk drive connection locations.

CAUTION:

Since the "A" the "B" cables are not keyed, carefully check the routing of the cables (following steps) to ensure that pin 1 on the drive connector receiving the cable connects to pin 1 on the appropriate DPU interface connector. Pin 1 of the cables is designated on the connectors of the cable by an embossed triangle. Pin 1 is also recognized by the black strand of the ribbon cable that is attached to that pin.

A 36-pin I/O cable (WL No. 220-0365) connects J1 on the 210-7421-A board in the DPU (see figure 2-3) to the appropriate jack on the I/O controller in the CPU.

A 60-pin "A" cable (WL No. 220-3041-22) connects J3 on the 210-7422 board in the DPU (see figure 2-4) to J1 on I/O logic board EM1 in the Phoenix drive.

A 26-pin "B" cable (WL No. 220-3033-36) connects J2 (Device No. 1 connector) on the 210-7422 board in the DPU (see figure 2-4) to J3 on Control/Mux logic board E.2 in the Phoenix drive.

If only one disk drive is connected to the DPU, a terminator board (WL No. 726-5790) is installed in J2 on I/O logic board EMI in the Phoenix drive; otherwise, the terminator is installed in the same location in the second drive.

If two disk drives are connected to the DPU, the following cable connections are also required.

A 60-pin "A" cable (WL No. 220-3041-7) connects J2 on the I/O logic board EM1 in the first Phoenix drive to J1 on I/O logic board EM1 in the second Phoenix drive.

A 26-pin "B" cable (WL No. 220-3033-21) connects J1 (Device No. 2 connector) on the 210-7422 board in the DPU (see figure 2-4) to J3 on Control/Mux logic board EM2 in the second Phoenix drive.

2.5.5 Device Address Plug (on Phoenix disk drive)

A device address plug must be inserted into the socket at the left of the START/STOP indicator switch on the front panel of each disk drive. The address plug must corresspond with the device number assigned to the 210-7422 connector receiving the "B" cable from the given drive. That is, install a "1" device address plug in the disk drive whose "B" cable is attached to connector J2(Device No. 1) on the 210-7422 board in the DPU, and install a "2" plug in the drive whose "B" cable is attached to connector J1 (Device No. 2) on the 210-7422 board.

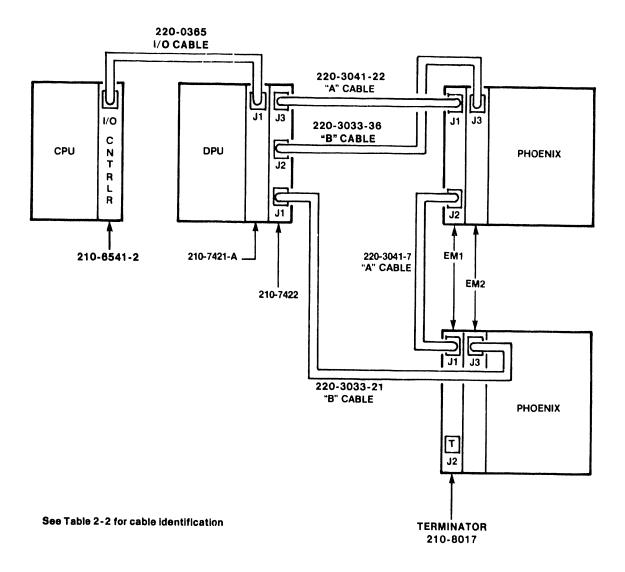
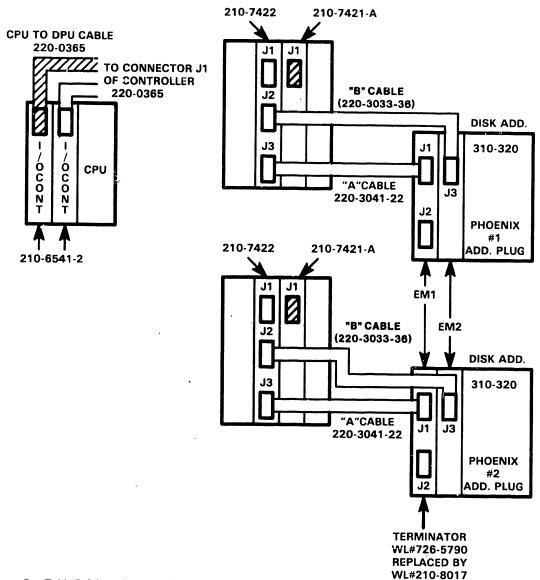


FIGURE 2-12 SYSTEM INTERCONNECTION DIAGRAM WITH ONE DPU



1

See Table 2-2 for cable identification

FIGURE 2-12A SYSTEM INTERCONNECTION DIAGRAM WITH TWO DPU'S

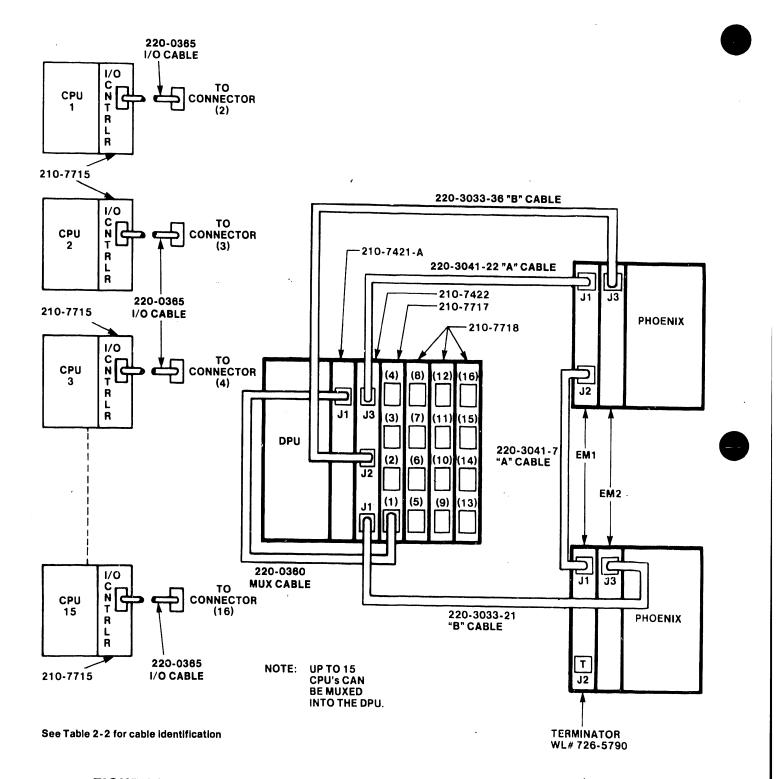


FIGURE 2-13 MUX SYSTEM INTERCONNECTION DIAGRAM WITH ONE DPU

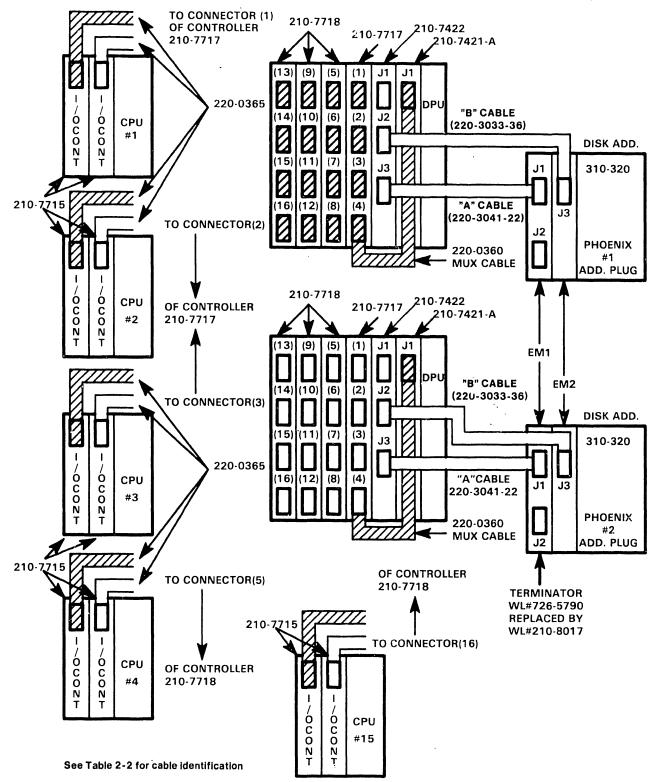


FIGURE 2-13A SYSTEM INTERCONNECTION DIAGRAM WITH TWO DPU'S

TABLE 2-2. CABLES AND TERMINATOR PART NUMBERS

FIGURES 2-12 & 2-12A

CONNECTS	S TANDAR D	SPECIAL
CPU I/O Controller to J1 of 210-7421-A	220-0365 9 ft	N/A
J3 of DPU's 210- 7422 to J1 of Phoenix #1	220-3041-22	N/A
J2 of Phoenix #1 to J1 of Phoenix #2	N/A	220-3041-7
J1 of 210-7422 to J3 of EM2 Phoenix #2	220-3308-21	220-3033-36*
J2 of DPU's 210- 7422 to J3 of Phoenix #1	220-3033-36	N/A
J2 EM1 Phoenix #2	726-5790	210-8017
FIGURES 2-13 & 2-	13A	
210-7715 to either 210-7717 or 210-7718	220-0365 9 ft	
DPU's J3 to Phoenix's J1	220-3041-22	N/A
Phoenix #1's J2 to Phoenix #2's J1	n/A	220-3041-7
J1 of 210-7422 to J3 EM2 Phoenix #2	220-3308-21	220-3033-36*
J2 of 210-7422 to Phoenix #1's J3	220-3033-36	N/A
J2 EM1 Phoenix #2	726-5790	210-8017
	CPU I/O Controller to J1 of 210-7421-A J3 of DPU's 210- 7422 to J1 of Phoenix #1 J2 of Phoenix #1 to J1 of Phoenix #2 J1 of 210-7422 to J3 of EM2 Phoenix #2 J2 of DPU's 210- 7422 to J3 of Phoenix #1 J2 EM1 Phoenix #2 FIGURES 2-13 & 2- 210-7715 to either 210-7717 or 210-7718 DPU's J3 to Phoenix's J1 Phoenix #1's J2 to Phoenix #2's J1 J1 of 210-7422 to J3 EM2 Phoenix #2 J2 of 210-7422 to Phoenix #1's J3	CPU I/O Controller to J1 of 210-7421-A J3 of DPU's 210- 7422 to J1 of Phoenix #1 J2 of Phoenix #1 to J1 of Phoenix #2 J1 of 210-7422 to J3 of EM2 Phoenix #2 J2 of DPU's 210- 7422 to J3 of Phoenix #2 J2 EM1 Phoenix #2 726-5790 FIGURES 2-13 & 2-13A 210-7715 to either 210-7717 or 210-7718 Phoenix #1's J2 to Phoenix #2's J1 Phoenix #1's J2 to Phoenix #2's J1 J1 of 210-7422 to J3 EM2 Phoenix #2 J2 of 210-7422 to J3 EM2 Phoenix #2 J2 of 210-7422 to Phoenix #1's J3 Phoenix #1's J3

^{*}For Daisy Chain Configuration Only.

2.5.6 Shielded Cable Connections

The ribbon cable connecting the 210-7422 PCB to the disk drive is copper clad for good sheilding. To ensure proper contact, install the DPU cable clamp at the 210-7422 PCB as follows:

- a. Loosen and remove the clamp screws that secure the two halves of the DPU cable clamp to the 210-7422 PCB (see figure 2-4).
- b. Remove the removable half of the cable clamp from the 210-7422 board.
- c. Connect the ribbon cable to the fixed half of the DPU cable clamp by inserting ther grounding clip (only) between the copper shield and the outer cable jacket of the ribbon cable (see figure 2-14, A).
- d. Push the ribbon cable onto the clamp; fully inserting the grounding clip (see figure 2-14, B).
- e. Install the removable clamp in the same manner (see figure 2-14, $^{\circ}$ C).
- f. Assemble and secure the two halves of the clamp: tighten the two clamp screws evenly, ensuring that the copper shield remains in good contact with both grounding clips (see figure 2-14, D).

CAUTION

Do not overtighten the clamp screws which secure the two halves of the clamp; otherwise, damage to the cable may result.

g. All three ribbon cables are secure in the same fashion (refer to figure 2-14, E).

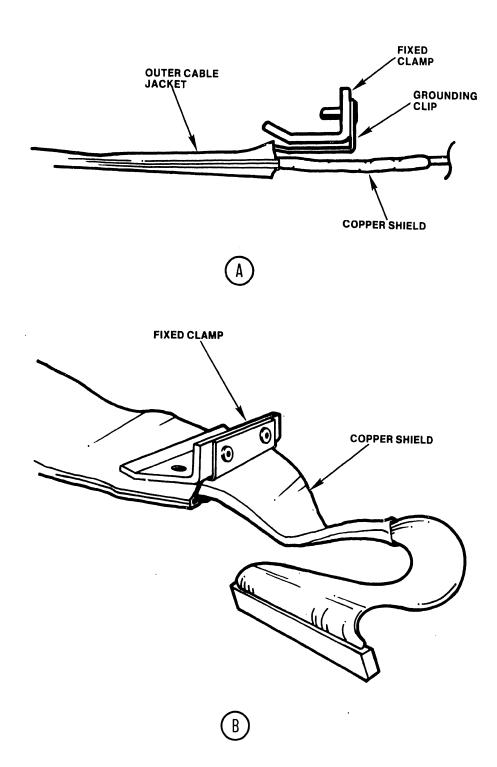


Figure 2-14. Shielded Cable Installation (Sheet 1 of 3)

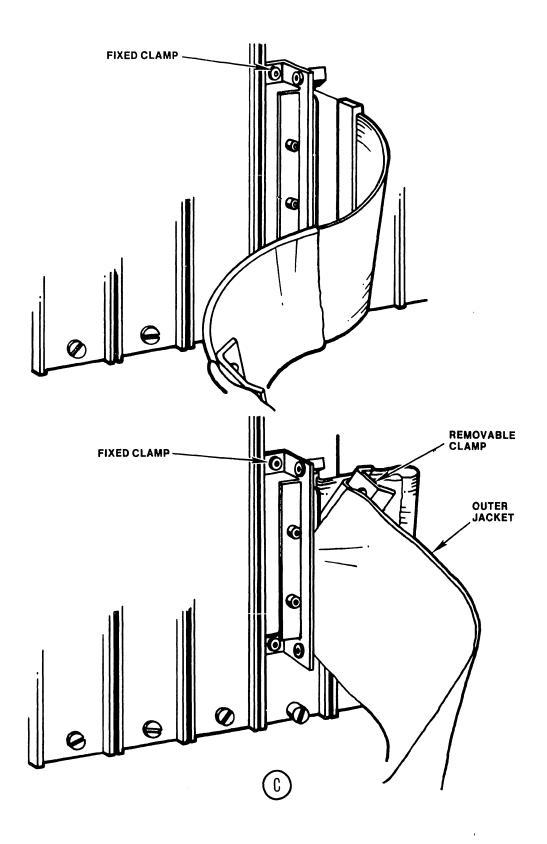


Figure 2-14. Shielded Cable Installation (Sheet 2 of 3)

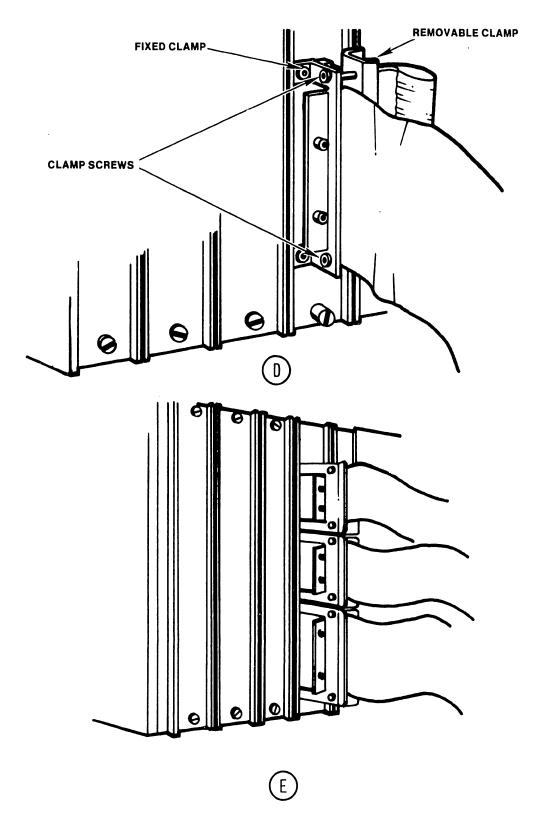


Figure 2-14. Shielded Cable Installation (Sheet 3 of 3)

2.6 INSTALLATION PROCEDURE

- a. Ensure that the ac input voltage selection jumpers on the motherboard are positioned correctly for the supplied voltage (see section 2.5.1).
- b. Check to see that all circuit boards are properly seated in the appropriate locations (see section 2.5.3).
- c. Attach all system interconnection cables (see section 2.5.4) ensuring that the cable shields are properly connected (see section 2.5.6).
- d. Check to see that the appropriate device address plug is in the disk drive (see section 2.5.5).
- e. Be certain the ac power switch is OFF, and then plug the ac power cord in.
- f. Set the ac power switch ON, then check and adjust, if necessary, all power supply voltages (see section 4.4).
- g. Run all appropriate 2280 disk diagnostics to confirm proper operation of the system (see section 4.2).
- h. Replace the unit cover(s) and place the DPU in the disk drive stand as shown in figure 2-15.
- i. The system is now ready for customer use.

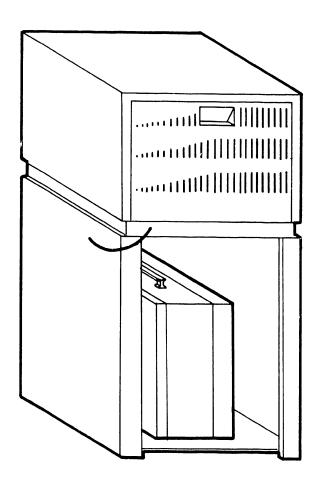


Figure 2-15. DPU Mounted In Bottom Of Stand

SECTION OPERA-TION

SECTION 3

OPERATION

For information concerning operation/programming of the 2280 Disk System (Phoenix drive and DPU) refer to Wang BASIC-2 Disk Reference Manual, WL No. 700-4081F (III.A.0), and Model 2280 Disk Drive User Manual, WL No. 700-5216 (III.A.10).

SECTION MAINT-ENANCE

SECTION 4

MAINTENANCE

4.1 RECOMMENDED TEST EQUIPMENT/TOOL LIST

- a. Digital voltmeter -- (acceptable type/equivalent: Fluke No. 8000A).
- b. Oscilloscope--(acceptable type/equivalent: Tektronix No. 465).
- c. Heavy duty screw driver with insulated handle (WL No. 726-9411).
- d. Small slot screwdriver with insulated handle (WL No. 726-9406).

4.2 DIAGNOSTICS

Refer to documentation category IV.C.1 for information concerning disk diagnostics.

4.3 PREVENTIVE MAINTENANCE

To ensure trouble-free operation, the 2280 DPU must have periodic preventive maintenance, consisting of inspection, cleaning, and adjustments. The following preventive maintenance routine should be performed once every six months. This maintenance schedule assumes a clean operating environment and a normal operating time during the standard five-day, 40-hour weeks. A dusty environment or any substantial increase in system operating time will require that the preventive maintenance be scheduled at closer intervals. In addition, this preventive maintenance routine should be performed during each unscheduled service call.

- a. Check the unit cooling fan for proper operation.
- b. Set the DPU ac power switch OFF.
- c. Remove the screws securing the top cover and remove the cover (see Section 4.5).
- d. Remove each circuit board from the DPU and clean the finger connectors with an ink eraser.
- e. Check to see that all circuit boards are at the latest electronic revision (E-REV). Refer to Mandatory Update Bulletin in documentation category I.B.O for ECO implementation procedures.

- f. Use a soft-bristle brush and a vacuum cleaner (WL No. 726-9518) to remove dust from the inside of the DPU.
- g. Reinstall all circuit boards in the appropriate locations (see section 2.5.3).
- h. Set the ac power switch ON.

NOTES:

- 1. Before making any adjustment, be certain the measuring instrument is properly calibrated.
- Electrical adjustments should be performed only when the parameter measured is out of tolerance.
 Do not make electrical adjustments indiscriminately.
- i. Check and adjust, if necessary, the DPU power supply voltages according to the procedure given in section 4.4.
- j. Run the 2280 disk diagnostics (see section 4.2) to confirm proper operation of the DPU and then replace the top cover.
- k. Use a mild detergent and a soft cloth or sponge to remove dirt and grime from the DPU chassis. Do not use abrasive or corrosive chemicals.

4.4 POWER SUPPLY VOLTAGE ADJUSTMENT

- a. Set the ac power switch OFF.
- b. Remove the top cover of the unit. (see section 4.5).
- c. Set the ac power switch ON.
- d. Check the dc voltages with a digital voltmeter for the values listed in table 4-1. (The test points for monitoring the voltages are also given in table 4-1.) Adjust the trimpots where indicated in figure 4-1 to obtain correct voltage levels where necessary.

NOTE:

Be sure to connect the common lead of the voltmeter to a ± 0 V connection, NOT the chassis or I/O controller rail. Erroneous readings will result if chassis ground is used as the voltmeter reference. The oscilloscope ground clip should also be connected to ± 0 V, NOT chassis ground.

e. Using an oscilloscope with the vertical sensitivity set at 1V/cm and a X1 probe, measure the ripple at the points indicated in table 4-1. AC ripple must not exceed the limits specified.

25 mv p-p

LIMITS L567 L567 VOLTAGE TEST POINT ADJUST VOLTAGE RIPPLE +5VRM Pin 1_1 R17 +4.90 to +5.10 15 mv p-p +4.90 to +5.10 +5VRL Pin 2₁ R2 15 mv p-p +8VR +8.50 to +8.80 Pin 12₁ R13 20 mv p-p Pin 15₁ +11.80 to +12.20+12VR R30 15 mv p-p -12VR Pin 5₂ R34 -11.80 to -12.20 15 mv p-p

-14.80 to -15.20

TABLE 4-1 DC VOLTAGE SPECIFICATIONS

4.5 REMOVAL/REPLACEMENT PROCEDURES

Pin 62

4.5.1 Top Cover

-15 VR

Remove the four screws that secure the top cover (see figure 4-2) and remove the cover from the unit.

R40

4.5.2 Bottom Cover

Remove the four screws that secure the bottom cover (see figure 4-3) and remove the cover from the unit.

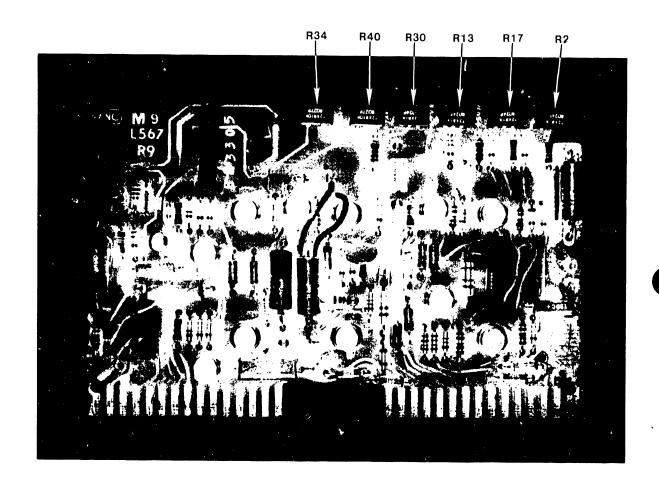


Figure 4-1. WL No. 210-L567 Regulator Board Potentiometers

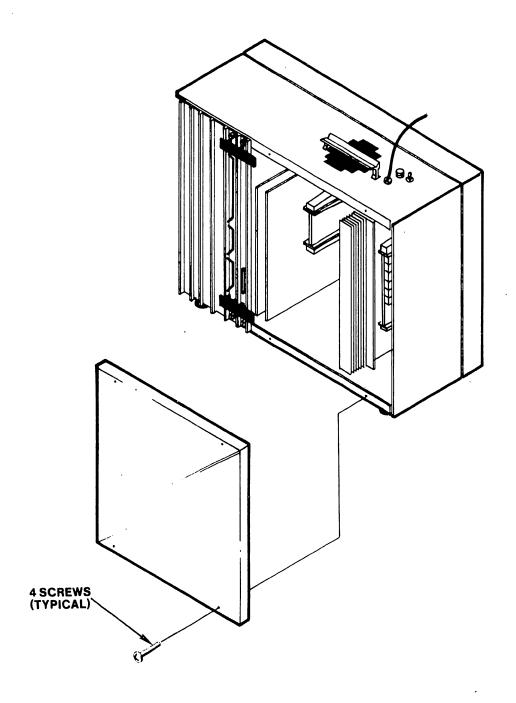


Figure 4-2. Top Cover Removal

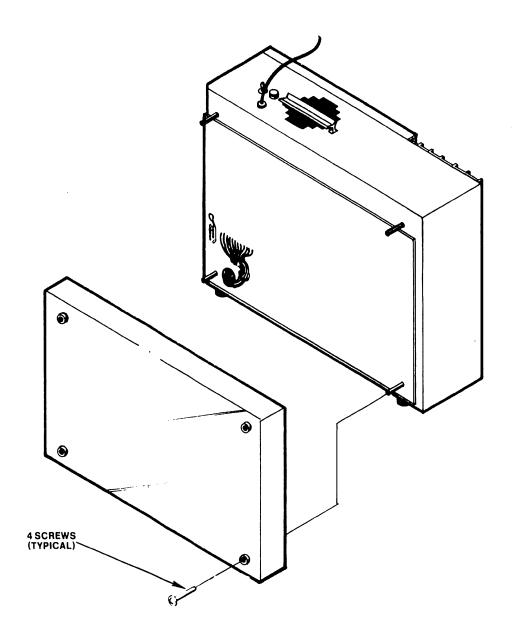


Figure 4-3. Bottom Cover Removal

4.6 DISK ERROR CODES

ERR 190 -- Disk Hardware Error

The disk did not properly respond to the system at the beginning of a read or write operation.

ERR 191 -- Disk Hardware Error

The disk is not in a "ready" condition.

ERR 192 -- Timeout Error

The disk did not respond to the system in the proper amount of time during a read or write operation.

ERR 193 -- Format Error

The disk media is not properly formatted.

ERR 194 -- Format Key Engaged

Not applicable for the 2280 DPU.

ERR 195 -- Seek Error

The specified sector could not be found on the disk.

ERR 196 -- Cyclic Redundancy Check Error

The data in a sector was read (or written) incorrectly.

ERR 197 -- Longitudinal Redundancy Error

The data was not transmitted to the CPU correctly during a read operation.

ERR 198 -- Illegal Sector Address

The sector address is greater than that allowed for the specific disk capacity.

ERR 199 -- Read After Write Error

The data read did not compare to that that was written during a read after write operation.

SECTION 5 THEORY OF OPERA-TION

CHAPTER 5

THEORY OF OPERATION

5.1 BLOCK LEVEL

This section consists of a brief explanation of the circuitry that comprises the four major logic boards in the DPU. The section is divided into five subsections—one for each logic board.

- 5.1.1 WL No. 210-7423-A RAM/PROM Control Board (see figure 5-1)
- 5.1.1.1 Clock (see timing chart, figure 5-2) -- The clock circuit produces 16 clock cycles, each 100 nsec in duration. This results in a machine-cycle speed of 1.6 usec (1600 nsec). The clock cycles are applied to a bcd-to-decimal (4-to-10) decoder which generates 8 clock pulses that are used in the DPU. These clock pulses and the events that occur at that specific time are listed below.
 - TO Clocks instruction through Rom Bit Latch as R_{0-15} . Clocks Status Register 0.
 - T2 Clocks data into the A and K Registers.
 - T3 Clocks data into Memory Register.
 - T4 Clocks Instruction Counter.
 Loads/clears Memory Address Register.
 Generates A and K Register Strobes.
 Combines with CNTRL-1 to generate CNTRL14 which-Clocks Read/Write/Format Latch.
 Fires Strobe One-Shot.
 Sets/resets Busy F/F.
 Clocks ECC Shift Register.
 Combines with CNTRL-3 to generate CNTRL34 which-Clocks Tag Latches.
 Clocks Select Latches.
 - T5 Increments/decrements Memory Address Register.
 - T8 Clocks Carry F/F. Clocks Equal F/F.
 - T9 Strobes data from RAM through Memory Register.
 - T2-5 Generates RAM write pulse.
- 5.1.1.2 Programmable Read Only Memory (PROM) -- The PROM contains the microprogram that controls the operations of the DPU. The 2280 DPU utilizes four INTEL 2716 PROM chips. Each PROM contains a 2K x 8-bit matrix. Since the DPU instruction set requires 16-bit words, two PROM's are selected at the same time to provide the instruction. With this requirement, the total read only memory capacity is 4096 words or instructions.

The PROM output bits are clocked through D-type latches at time TO as ROM bits $\rm R_{O-15}$.

210-7423-A RAM/PROM Control

Block Diagram

Figure

5-1.

£

No.



400 nSEC.

T2-5

The PROM is addressed by an Instruction Counter composed of three AM2911 microprogram sequencer chips. Twelve bits (${\rm IC}_{0-11}$) are applied to the PROM by the instruction counter; ${\rm IC}_{0-10}$ provide the addressing and ${\rm IC}_{11}$ produces the appropriate chip select (${\rm CS}_{1-2}$) signal. ${\rm CS}_1$ selects the lower 2K of PROM and ${\rm CS}_2$ selects the higher 2K. The PROM space is allocated as follows.

OK-2K = main body of microprogram

2K-3K = alternate sector routine, copy write error routine, error

correction routine, and diagnostics

3K-4K = diagnostics

Normally the instruction counter increments by one at time T4. However, during execution of the microprogram, it is necessary to jump (branch) to a location other than that immediately following. Three signals are provided to accomplish the jump (branch): $UB--\underline{U}$ nconditional \underline{B} ranch, $SB--\underline{S}$ ubroutine \underline{B} ranch, and $256--\underline{256}$ step branch.

5.1.1.3 Random Access Memory (RAM) -- The RAM is used to store flags, pointers, and status information required for execution of the microprogram as well as the data that is to be exchanged between disk and CPU. The RAM consists of eighteen 2114-L integrated circuits with a capacity of 1024×4 bits resulting in a total storage capacity of 9,216 bytes.

The Memory Address Register (MAR) provides 16 bits (AD_{0-15}) for the addressing of the RAM. AD_{0-9} supply the actual RAM address while AD_{10-15} generate the appropriate chip select (1K-8K) signal. The MAR increments or decrements at time T5. It can also be loaded (preset to a certain address) or cleared (reset to an address of zero) at time T4. The address to which the MAR can be loaded is provided through the Memory Address Multiplexer (MA $_{0-7}$). The inputs to the multiplexer that can be selected to provide the address are either ROM bits 0-7 (R_{0-7}) or the A and K Register bus (AK_{0-7}).

Data is transferred to/from RAM through the Memory Register (MB $_{0-7}$). The 2280 DPU utilizes two AM2905 Bus Transceiver chips for the MAR. Data that is to be written into RAM is input to the Memory Register from the A. bus or the Memory Register bus (M $_{0-7}$). Data that is read from RAM is applied to the Memory Register bus (M $_{0-7}$). RAM space is allocated as follows.

1st 1/4K (256 bytes) = flags and pointers
Next 2K = cache for read
Next 4K = cache for multi-sector write
Next 8K = read/write buffer for COPY
2nd page of last 'K' = alternate sector RAM

READ -- 256-1K-1K

WRITE -- 256-1K-1K-1K-1K

COPY -- 256-1K-1K-1K-1K-1K-1K-1K

The Error Check Circuitry (ECC) in the RAM functions as: 35-shift polynomial in a similar manner as that found in the OIS/VS systems.

5.1.1.4 <u>Control Circuit</u> -- Refer to section 5.3 (Instruction Set) for information concerning the control circuitry on the 210-742;—A board.

- 5.1.2 WL No. 210-7421-A ALU/MUX Interface Board (see figure 5-2)
- 5.1.2.1 <u>Arithmetic/Logic Unit (ALU)</u> -- Two 74181 integrated circuits, designed to perform specific arithmetic or logical operations as directed by the microprogram, comprise the ALU.

The ALU data inputs are referred to as the A-bus and the B-bus. The A-bus inputs are from the A or K Registers (AK $_{0-7}$). The B-bus is the output of a multiplexer, incorporating memory (M $_{0-7}$) and ROM bits (R $_{0-7}$) as selectable inputs.

The ALU output is the C-bus (C_{0-7}) . Data on this bus can be input to the A Register of K Register.

- 5.1.2.2 <u>A Register</u> -- The general purpose A-Register stores data from: 1) the CPU, $\frac{1}{2}$ Status Register 0, 3) the ECC Shift Register, or 4) the ALU. The contents of the A-Register can be processed by the ALU, stored in memory, or input to the Track, Head, or Sector Registers (THS₀₋₇).
- 5.1.2.3 <u>K-Register</u> -- The general purpose K-Register stores data from the ALU or acts as Status Register 1. The contents of the K-Register can be processed by the ALU or stored in memory.
- 5.1.2.4 <u>Status Register 0 (STO)</u> -- STO stores seven CPU/DPU conditions which can be input to the A-Register, via the BO_{0-7} bus, for monitoring or testing. The conditions are as follows:

*REQ (Bit 0)		When active, indicates the Output Bus Buffer has received a byte of data from the CPU.
*CAB (Bit 1)		When active, indicates a carry resulted from a preceding ALU arithmetic operation or from the execution of a "Set Carry" instruction.
*GKBD (Bit 2)		When active, indicates the CPU is ready to receive input from the DPU.
REINIT (Bit 3)		When active, indicates the CPU is sending address information next. When inactive, indicates the CPU is sending data next.
DN #3 (Bit 4)	au na	When active, indicates that selection of drive 2 in a daisy-chain configuration is desired.
A=B (Bit 5)		When active, indicates the data on the ALU A-bus is equal to that on the B-bus.
AD7 (Bit 6)		The eigth $(2_8 = 128)$ RAM address bit.

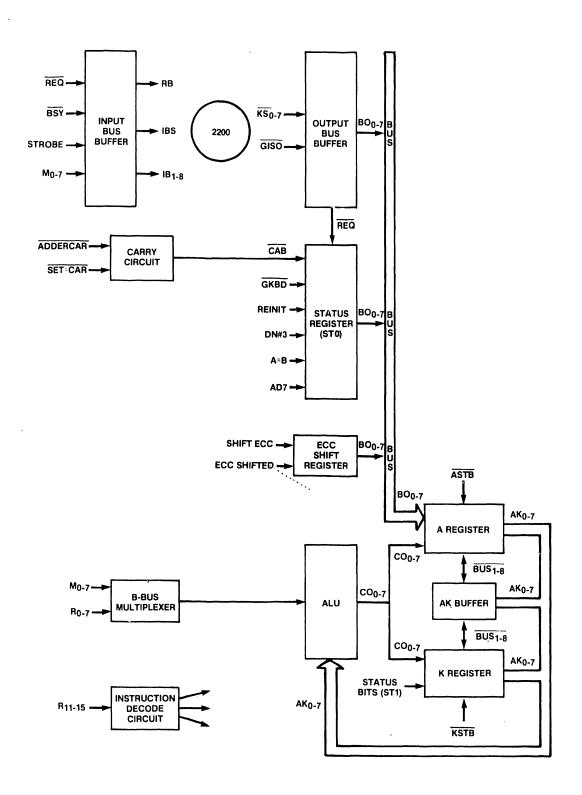


Figure 5-3. WL No. 210-7421-A ALU/MUX Interface Block Diagram

5.1.2.5 Status Register 1 (ST1) -- The K-Register acts as ST1. Seven bits representing DPU/disk drive conditions can be selected as inputs to the K-Register. Once in the register, the bits can be tested for the various conditions. The bit representation is as follows:

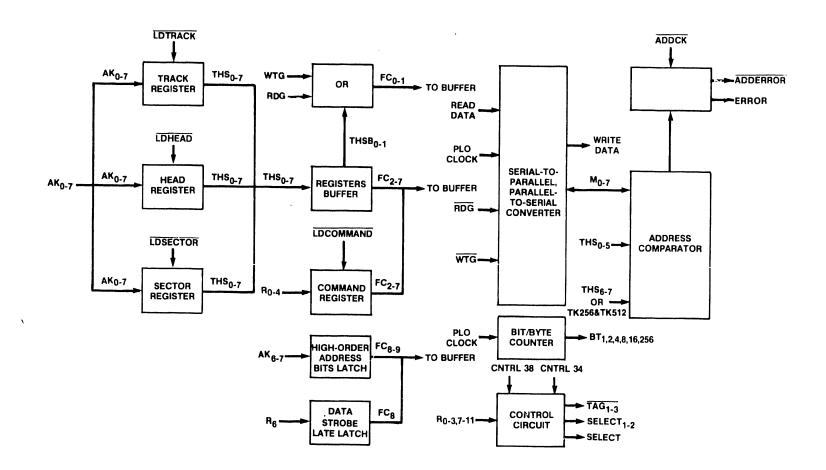
W/P (Bit 1)	 When active, indicates the disk drive is write protected.
ERROR (Bit 2)	 When active, indicates an address error was detected.
DERROR (Bit 3)	 When active, indicates a disk drive fault occurred or a seek error was detected by the disk drive.
*DONE (Bit 4)	 When active, indicates 256 bytes of data have been transferred to/from the disk.
*NULL (Bit 5)	 (Not Used)
READY (Bit 6)	 When active, indicates the disk drive is "on cylinder" and "ready".
SECTOR (Bit 7)	 When active, indicates the sector counter is equal to the desired sector, which is present on the THS (\underline{T} rack/ \underline{H} ead/ \underline{S} ector) bus.

- 5.1.2.6 <u>Input Bus Buffer</u> -- The Input Bus Buffer provides the interface for data being sent to the CPU from the DPU. Data is input to the buffer from the Memory Register (M_{0-7}) .
- 5.1.2.7 Output Bus Buffer -- The Output Bus Buffer provides the interface for data being sent to the DPU from the CPU. Data is output from the buffer to the A-Register via BO_{0-7} . When the buffer receives a byte of data, it generates a request (REQ) signal. REQ is monitored by the DPU, and when the signal is active, the microprogram causes the hardware to read in the data.
- 5.1.2.8 <u>Instruction Decode Circuit</u> -- ROM bits R_{11-15} are applied to a 74154 one-of-sixteen decoder which produces the control signals necessary for execution of the microinstructions by the hardware.

- 5.1.3 WL No. 210-7424 I/O Controller Board (see figure 5-4)
- 5.1.3.1 <u>Track Register</u> -- The Track Register holds the desired track (cylinder) address as input from the AK bus. The address is outut to a buffer, via the THS bus, from where it is sent to the disk drive.
- 5.1.3.2 <u>Head Register</u> -- The Head Register holds the desired head number and volume select bit as input from the AK bus. The head/volume select information is output to a buffer, via the THS bus, from where it is sent to the disk drive.
- 5.1.3.3 <u>Sector Register</u> -- The Sector Register holds the desired sector number as input from the AK bus. The number is output to the Sector Comparator, via the THS bus, where is checked against the output of the Sector Counter.
- 5.1.3.4 <u>Command Register</u> -- The Command Register holds the disk drive control select bits (for example, write gate, read gate, and return to zero). The control information is output to a differential buffer to be sent to the disk drive.
- 5.1.3.5 <u>Bit/Byte Counter</u> -- The Bit/Byte Counter keeps track of the number of bytes of data transferred between DPU and disk. The counter generates certain control signals depending on the number of bits/bytes that have been counted.
- 5.1.3.6 Serial-to-Parallel, Parallel-to-Serial (SP/PS) Converter -- The SP/PS accepts eight parallel bits of data from the Memory Register (M_{0-7}) and converts the information to serial and adds clock pulses between data bits. The serial data is then transmitted to the disk drive.

The converter also accepts serial Read Data and changes it to eight parallel bits of data. If the data that is read is address information, it is input to the Address Comparator; otherwise, the data is sent to memory.

- 5.1.3.7 Address Comparator -- The Address Comparator checks the address read from the disk with the desired address that is present on the THS bus. If the two are not the same, the comparator flags an error.
- 5.1.3.8 <u>Control Circuit</u> -- Refer to section 5.3 (Instruction Set) for information concerning the control circuitry on the WL No. 210-7424 board.



- 5.1.4 WL No. 210-7422 ECC/Device Interface Board (see figure 5-5)
- 5.1.4.1 <u>Differential Buffers</u> -- The Differential Buffers provide the interface between DPU and disk drive. The buffers convert TTL voltage levels to differential voltage levels and vice versa.
- 5.1.4.2 <u>Sector Counter</u> -- The Sector Counter increments every time a sector mark pulse is received from the disk drive. It is reset (cleared) when the index pulse is received.
- 5.1.4.3 <u>Sector Comparator</u> -- The Sector Comparator checks the desired sector number that is present on the THS bus against the count from the sector counter. When the two are equal, indicating the correct disk sector is under the read/write head, an EQUAL signal is generated to inform the microprogram.
- 5.1.4.4 <u>ECC</u> -- The Error Correction Circuit along with an ECC routine in the microcode is responsible for detecting and correcting any single-bit errors that occur during a read operation. Figure 5-6 is a detailed flowchart illustrating the ECC code handling routine.

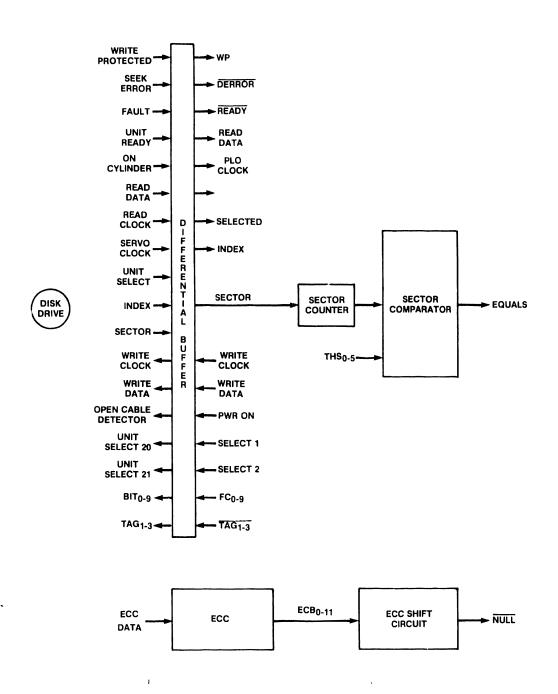


Figure 5-5. WL No. 210-7422 ECC/Device Interface Block Diagram

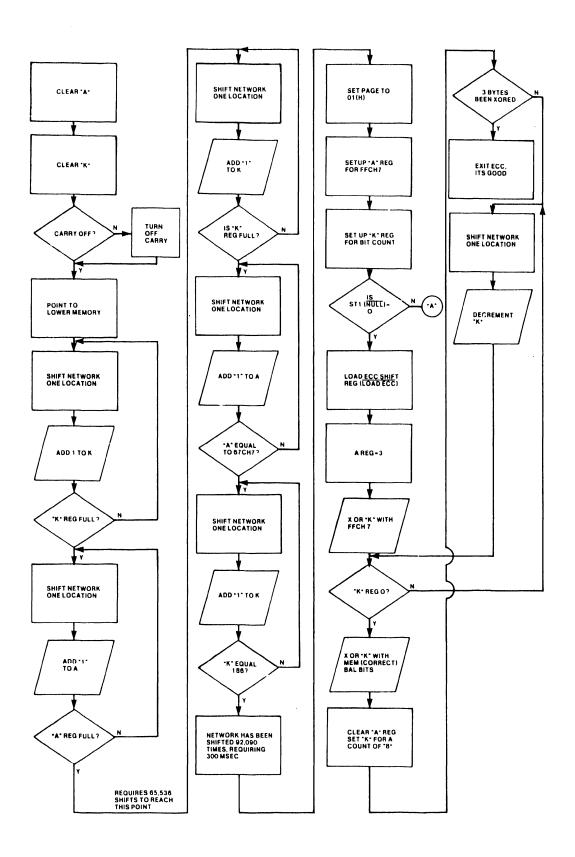


Figure 5-6. ECC Code Handling Flowchart

5.1.5 WL No. 210-L567 Regulator Board (see schematic)

There are six regulated voltages in the DPU power supply, each independently variable. All six supplies are of the series pass variety, each controlled by an IC voltage regulator. There are four transformer generated, full wave unregulated supplies which provide the necessary voltages for the regulators.

The six regulated supplies are +5 VRL for the TTL logic and +5 VRM, +8 VR, +12 VR, -12 VR, and -15 VR for the PROM's and RAM's. As all regulators operate in the same manner, only the +5 VRL supply is discussed here.

The heart of the regulated supply is the voltage regulator (L4). Unregulated +14V is applied to L4 pin 8 to provide the operating voltage for the IC. Pin 4 of the regulator is the output from a reference amplifier in the IC. The output of this amplifier is applied to the voltage divider network R1, R2, and R3. The reference amplifier contains a current source and temperature compensator to prevent drifting.

The voltage present at the wiper of potentiometer R2 is applied to the non-inverting input of the error amplifier (regulator pin 3). By varying the voltage at the non-inverting input, the output voltage changes.

Since the IC regulator cannot supply large output currents, external circuitry must be provided for this purpose. Pin 7 of the regulator is the collector output of the internal series pass transistor which provides the necessary drive for transistor Q5. Q5 provides the necessary current for driver X8, which in turn controls the series pass transistors X6 and X7.

The error amplifier constantly monitors the regulated output voltage by sampling the output voltage at the inverting input (regulator pin 2). Voltage regulation is performed as follows.

If the output voltage tries to go more positive, the inverting input (pin 2) also follows positive. This results in a more negative input to the internal series pass transistor, causing it to conduct less thus increasing the positive voltage at pin 7. (The voltage drop across R4 is less due to the decrease in current; hence Q5 base tends toward +14V.) Q5 conducts less, driver Q8 controlled by Q5 conducts less, and finally the series pass transistors conduct less, decreasing the output voltage. In a similar manner, the output voltage is increased when a negative output change is detected. Note that no regulation can take place unless there is a change in the output voltage to initiate a correction; therefore, the regulation is less than perfect.

The regulators also employ foldback current limiting as follows.

Resistors R5, R7, and R8 form the external current sensing network. As the current in the external circuit increases, the voltage drop across the sensing network changes, until the internal current limiter transistor is turned off. The output voltage drops to zero and the output current remains at a safe value when the output current exceeds the predetermined value.

Capacitor C8 compensates the internal error amplifier to avoid instability. As mentioned previously, all other regulator circuits operate in the same manner.

Diode D2 connected between L5 pin 1 and L1 pin 1 prevents the +5VRM supply from ever becoming more positive than the +8VR supply. This is necessary to prevent damage to the memory. Also diode D1 prevents the +8VR supply from rising above +14.2V, again to prevent damage to the memory.

5.2 READ/WRITE DATA FLOW

5.2.1 Write Data Flow

Figure 5-5 is a write data flow diagram illustrating the hardware involved when data is read from the 2200, processed through the 2280 DPU, and written onto the disk. The following theory references schematic diagrams located in Appendix D at the end of this document.

Parallel write data enters the 2280 DPU at 7421-2 (KS $_{0-7}$ to 80_{0-7}) where it goes through the A register as AK $_{0-7}$. The data is sent via 7423-2 to L40 and L41 (M $_{0-7}$) and to the RAM. From the RAM the data goes through L40 and L41 (M $_{0-7}$) to 7424-1 and to L47 (serializer/de-serializer). The data goes out as WDATA to 7424-2 through L32 and L42 and emerges as WRITE DATA (serial) and to the drive.

5.2.2 Read Data Flow

Figure 5-6 is a read data flow diagram illustrating the hardware involved when data is read from the disk, processed through the 2280 DPU, and written into the 2200. The theory discription presented below references schematic diagrams located in Appendix D at the end of this document.

Serial read data from the disk drive enters the 2280 DPU at 7424-1 where is is applied to L47 (serializer/de-serialaizer). The parallel data output from the serializer/de-serializer is sent via 7423-2 to L40 and L41 and to the RAM. From the RAM, it goes to L40 and L41 and to 7421-1. It is now sent via L4 and L11 to the 2200 as IB_{0-7} .

5.3 MICROPROGRAM FLOWCHARTS

Figures 5-9 thru 5-15 are flowcharts of each individual microprogram sequence for the 2280 DPU functional modes of operation.

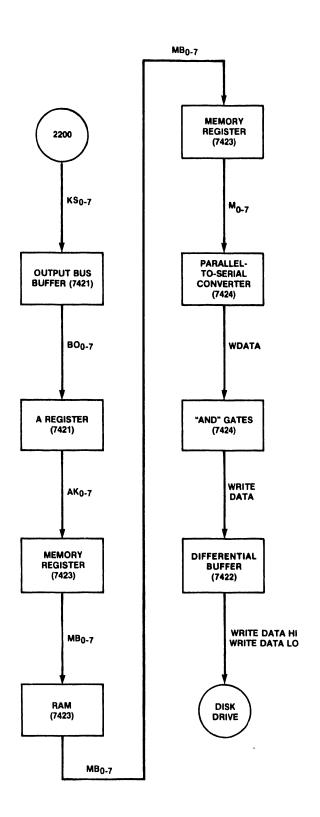


Figure 5-7. Write Data Flow

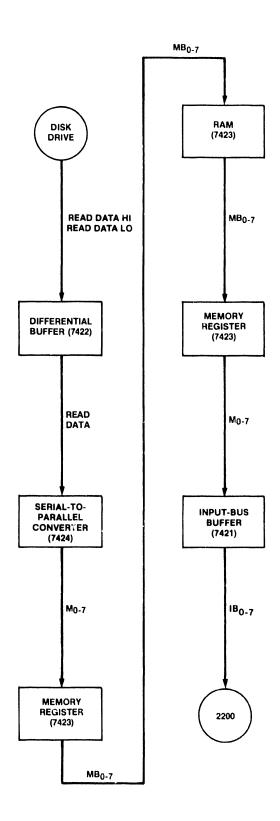


Figure 5-8. Read Data Flow

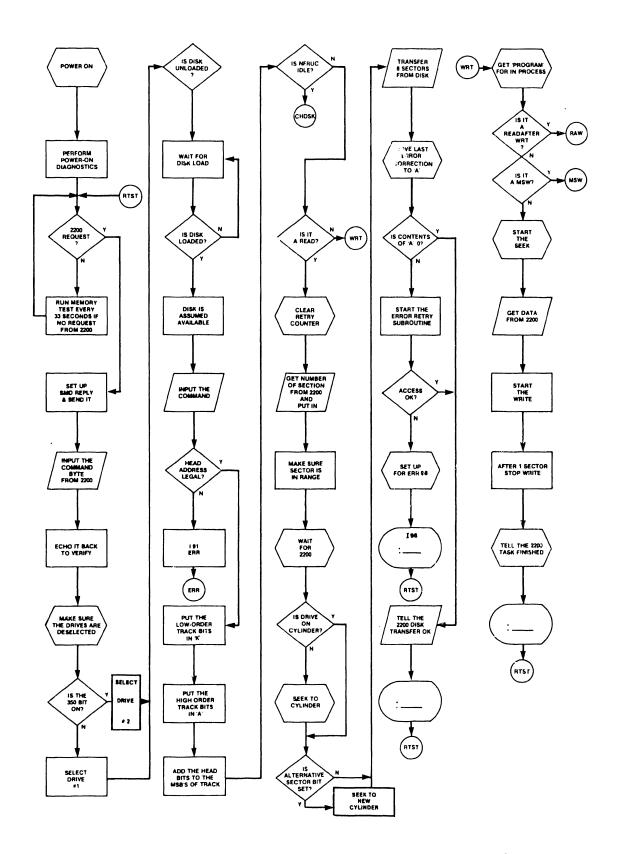


Figure 5-9. 2280 DPU Microprogram Flowchart (Sheet 1 of 2)

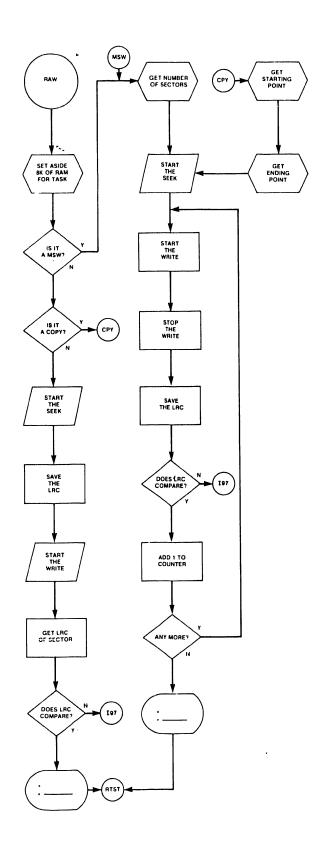


Figure 5-9. 2280 DPU Microporgram Flowchart (Sheet 2 of 2)

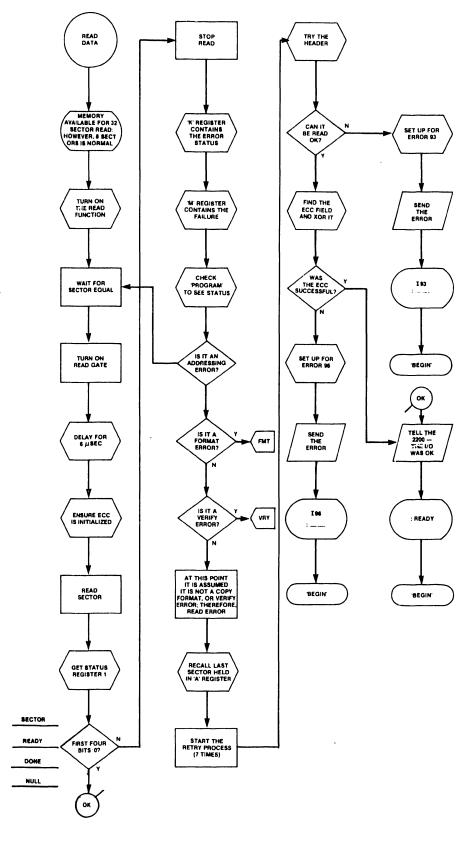


Figure 5-10. 2280 DPU Microprogram Flowchart - Read Data

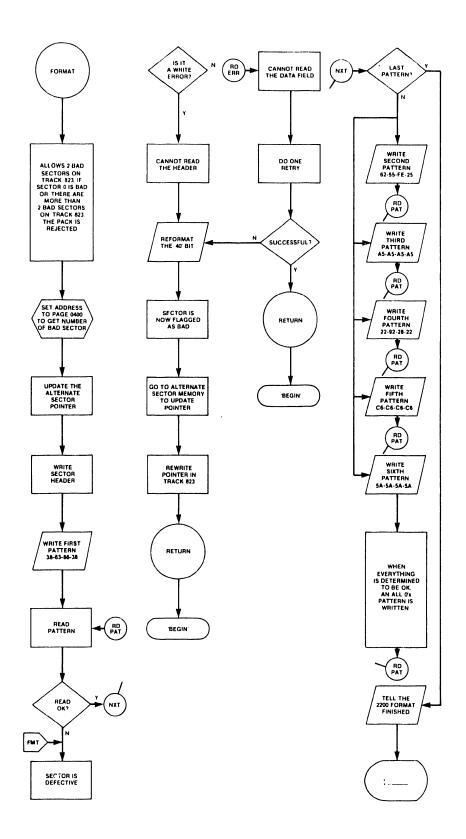


Figure 5-11. 2280 DPU Microprogram Flowchart - Format

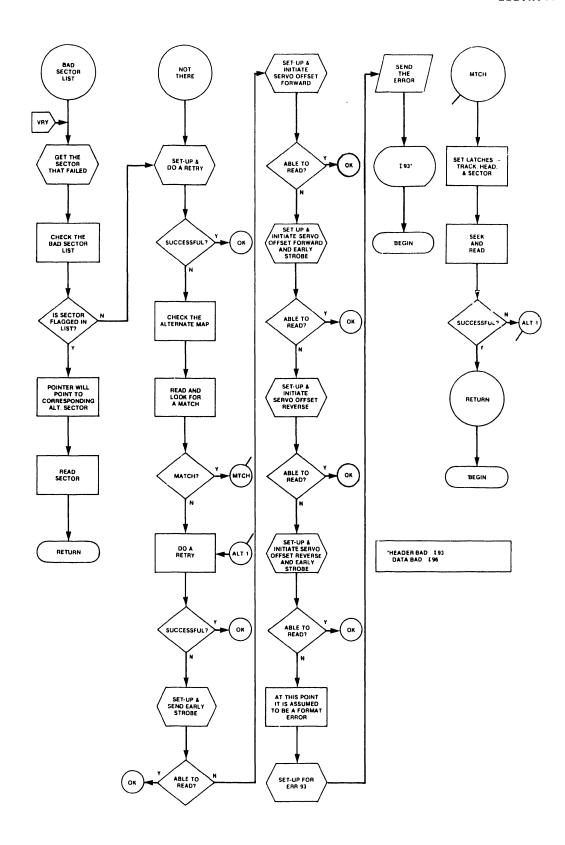


Figure 5-12. 2280 DPU Microprogram Flowchart - Verify

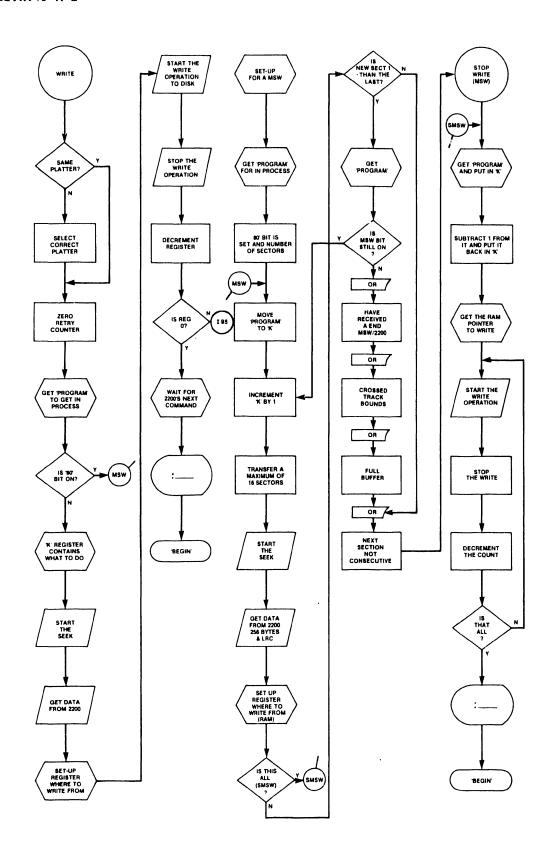


Figure 5-13. 2280 DPU Microprogram Flowchart - Welte/Start MSW/Stop MSW

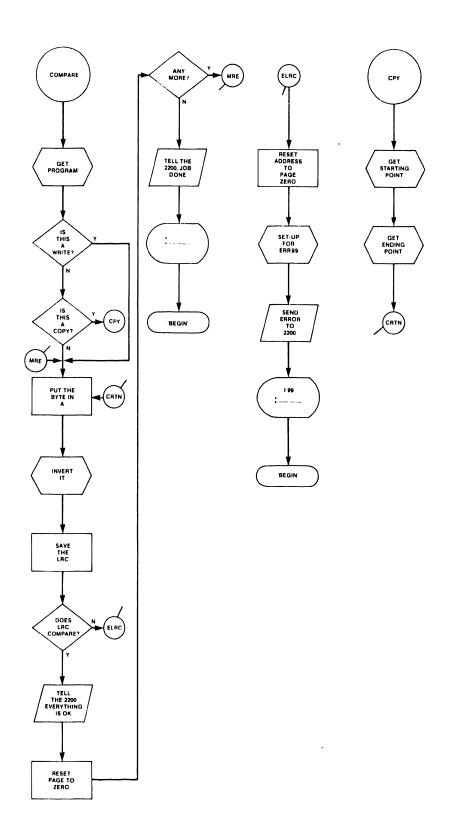


Figure 5-14. 2280 DPU Microprogram Flowchart - Compare

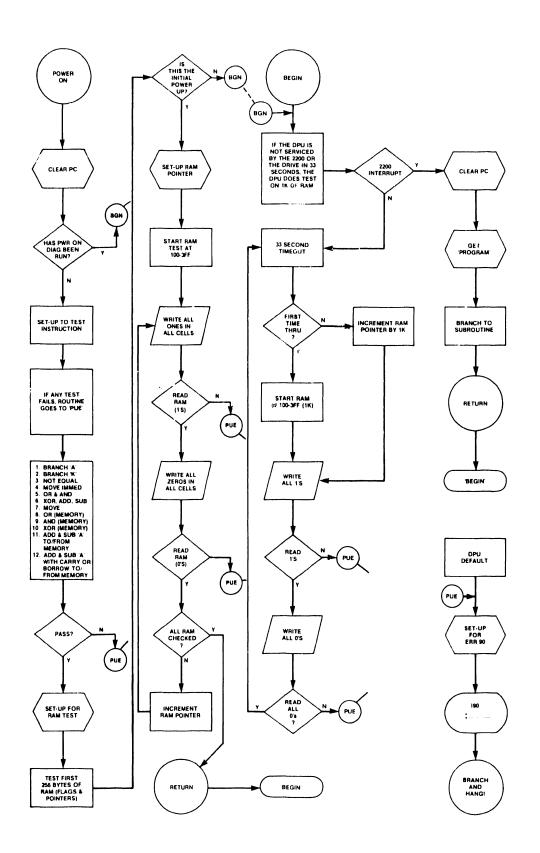


Figure 5-15. 2280 DPU Microprogram Flowchart - Power-On Diagram/Begin

APPENDIX DISK ERROR CODES

APPENDIX A

DISK ERROR CODES

ERR D80

Error:

File Not Open

Cause:

The file was not opened.

Recovery:

Open the file before attempting to read from it or

write to it.

ERR D81

Error:

File Full

Cause:

The file is full; no more information may be written

into the file.

Recovery:

Correct the program, or use MOVE to move the file to another platter and reserve additional space for it.

ERR D82

Error:

File not in Catalog

Cause:

A non-existing file name was specified, or an attempt was made to load a data file as a program

Recovery:

Make sure the correct file name is being used, the proper disk is mounted, and that the proper disk

drive is being accessed.

ERR D83

Error:

File Already Cataloged

Cause:

An attempt was made to catalog a file with a name

that already exists in the Catalog Index.

Recovery:

Use a different name, or catalog the file or a

different platter.

ERR D84

Error:

File Not Scratched

Cause:

An attempt was made to rename, or write over a file

that has not been scratching.

Recovery:

Scratch the file before renaming it.

III.A.10 M-2

ERR D85

Error:

Catalog Index Full

Cause:

There is no more room in the Catalog Index for a new

name.

Recovery:

Scratch any unwanted files and compress the catalog using a MOVE statement, or mount a new disk platter and create a new catalog.

ERR D86

Error:

Catalog End Error

Cause:

The end of the catalog area is defined to fall within the catalog index, or an attempt has been made to move the end of the catalog area to fall within the area already occupied by cataloged files (with MOVE END), or there is not room left in the Catalog Area to store more information.

Recovery:

Correct the SCRATCH DISK or MOVE END statement, or increase the size of the Catalog Area with MOVE END, or scratch unwanted files and compress the catalog with MOVE, or open a new catalog on a separate

platter.

ERR D87

Error:

No End of File

Cause:

No end-of-file record was recorded in the file (with DATASAVE DC END or DATASAVE DA END), and therefore none could be found by the DSKIP END statement.

Recovery:

Correct the file by writing an end-of-file trailer

after the last data record.

ERR D88

Error:

Wrong Record Type

Cause:

A program record was encountered when a data record was expected, or vice versa.

Recovery:

Correct program. Be sure the proper platter is mounted and be sure the proper drive is being

accessed.

ERR D89

Error:

Sector Address Beyond End of File

Cause:

The sector address being accessed by the DATALOAD DC or DATASAVE DC operation is beyond the end-of-file. This error can be caused by a bad disk platter.

Recovery:

Run the program again. If error persists, use a different platter or reformat the platter. If error still exists, contact your Wang Service Representative.

ERR 190

Error:

Disk Hardware Error

Cause:

The disk did not recognize or properly respond to the System at the beginning of a read or write operation (the read or write has not been performed).

Recovery:

Run the program again. If error persists, contact your Wang Service Representative.

ERR TO1

Error:

Disk Hardware Error

Cause:

A disk hardware error occurred, e.g., the disk is not in file-ready position. This could occur, for example, if the disk is in LOAD mode or power is not turned on.

Recovery:

Ensure disk is turned on and properly set up for operation. Set the disk into LOAD mode and then back into RUN mode with the RUN/LOAD selection switch. The check light should then go out. If error persists, call your Wang Service Representative. (Note: disk must never be left in

LOAD mode when turned on.)

ERR 192

Error:

Time-out Error

Cause:

The disk did not respond to the system during a read or write operation in the proper amount of time (time-out).

Recovery:

Run program again. If error persists, reinitialize disk - if error still occurs, contact your Wang Service Representative.

III.A.10 M-2

ERR 193

Error:

Disk Format Error

Cause:

A disk format error was detected during a disk read or write. The disk is not properly formatted. The error can be either in the disk platter or the disk

hardware.

Recovery:

Format the disk again; if error persists, call your

Wang Service Representative.

ERR 194

Error:

Format Key Engaged

Cause:

The disk format key is engaged (the key should be

engaged only when formatiing a disk).

Recovery:

Turn off the format key.

ERR 195

Error:

Seek Error, or Platter Protected

Cause:

A disk-seek error occurred; the specified sector could not be found on the disk. This error may indicate a bad format, or it may result from an attempt to write to a protected platter.

Recovery:

Run program again. If error persists, reinitialize (reformat) the disk. If error still occurs, call

your Wang Service Representative.

ERR 196

Error:

Cyclic Read Error

Cause:

A cyclic redundancy check (CRC) error occurred during a disk read operation; the sector being addressed has never been written to or was

incorrectly written.

Recovery:

If not formatted, format the disk. If the disk was formatted, rewrite the bad sector. If error persists, use a different disk platter. If error persists on a fixed platter, call your Wang Service

Representative.

ERR 197

Error:

LRC Error

Cause:

A disk longitudinal redundancy check (LRC) error occured when reading a sector. This usually

indicates a data transmission error occured when the

sector was read or written.

Recovery:

If error persists, rewrite the sector. If the error

still persists, call your Wang Service

Representative.

ERR 198

Error:

Illegal Sector Address or Platter Not Mounted

Cause:

The disk sector being addressed is not on the disk or the disk platter is not mounted. (Maximum Legal sector address depends upon the model of disk used.)

Recovery:

Correct the program statement in error, or mount a

platter in the specified drive.

ERR 199

Error:

Read After Write Error

Cause:

The comparison of read after write to a disk sector failed, indicating that the information was not written properly. This error usually indicates a

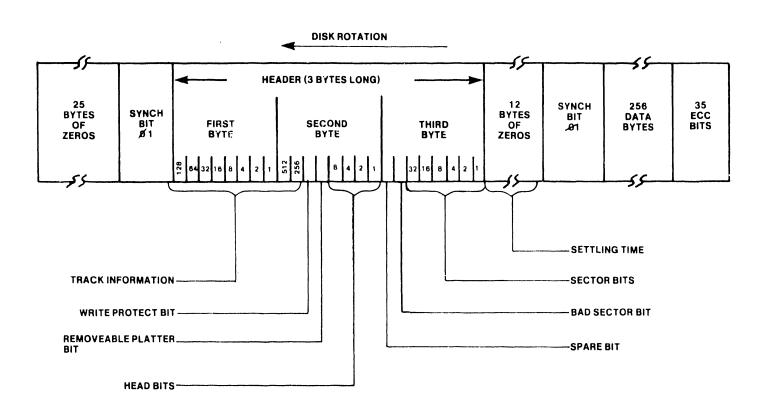
bad disk platter.

Recovery:

Write the information again. If error persists, try a new platter; if error still persists, call

your Wang Service Representative.

APPENDIX R DISK SECTOR LAYOUT



APPENDIX B

DISK SECTOR LAYOUT

APPENDIX BILL OF MATER-IALS

APPENDIX C

BILL OF MATERIALS

A Bill of Materials for the 2280 DPU starts on the following page.

ACSEMBLY PART NUMBER 177-2220-80- - LEGEND
ASSEMBLY DESCRIPTION 2250 DISK PROCESSOR UNIT 1: PEPHANICM; 2: ITEM MASTER DELY CODE; 3: *=TAGGED OUT OF KITCPROD STR)

POSITICN IN STRUCTURE	LEGEND 1 2 3	CCMECNENT PART NUMBE		DESCRIPTION	F C N	QUANTITY PER ASSY	L/ M	L/T
1	ΙN	187-2200-80-	-	MET 2280 PISK TO VP/MVP INTREC LABOR PRODUCTION SYSTEMS LABOR QUALITY CONTROL FCA 2200A/B/C/S/T PS REGULATOR LABOR CALCULATING SYSTEMS LABOR QUALITY CONTROL OTHER DIRECT COST CAP 82 PF 10% 500 V CERAMIC DISC CAP 100 PF 10% 500 V CERAMIC DISC CAP - FOI UF 10% 500 V CERAMIC DISC		1.0000	EACH	00010
?	١٨	000-0005	-	LABOR PRODUCTION SYSTEMS		2.4610		00000
2	IN	0 20 - 0 0 1 1	-	LABOR QUALITY CONTROL		• 4920		00000
2	IN	210-65	-	FCA 2200A/B/C/S/T PS REGULATOR		1.0000	EACH	00023
3	ΙV	000-0003	-	LABOR CALCULATING SYSTEMS		1.0000		00000
3	1 N	000-0011	-	LABOR GUALITY CONTROL		.2000		00000
3	IN	0 50 - cadd	-	OTHER DIRECT COST		14.3880	EACH	00000
3	IV	300-1082	-	CAP 82 PF 10% 500 V CERAMIC DISC CAP 100 PF 10% 500 V CERAMIC DISC CAP .001 UF 10% 500 V CERAMIC DISC		1.0000	EACH	
3	1 /	300-1100	-	CAP 100 PF 10% FOO V CERAMIC DISC		1.0000	EACH	
3	1 N	300-1906					EACH	
t	FS	300-4000-R -		CAF 1.0 UF 35V 10% TANT AXIAL T&R			EACH	
3	F S	307-43(1-R -		CAP .47 UF 35V 10% TANT AXIAL TER	F1 = 76 =	2 • 0000	EACH	
3	F.S	300-4002-R -		CAP •1 UF 35V 10% TANT AXIAL T&R		1.7000	EACH	
3	FS	300-4013-R -		CAP 1.2 UF 35V 10% TANT AXIAL T&R		1.0000	EACH	
3	FS	300-4014-R -		CAP 2.2 UF 20V 10% TANT AXIAL TRP		1.0000	EACH	
3	FS	300-4018-R -		CAP 18.0 UF 15V 10% TANT AXIAL TRR	E15765	1.0000	EACH	
3	IN	300-4019		CAP 33.0 UF 15 V 10% TANT AXIAL		1.0000	EACH	
3	I N	300-4020		CAP 47.0 UF 15 V 10% TANT AXIAL		1.0000	EACH	
3	11	100-4032		CAP 10.0 UF 35 V 10% TANT AXIAL		4.0000	EACH	
3	FS FS	330-1027		RES 27 OFM 1/4W 10% FIXED CCMP	E15765	2.0000	EACH	
: 3		330-2010 330-2022		RES 100 OHM 1/4W 10% FIXED CCMP	E15765	1.0000	EACH	
3	FS T	330-2022		RFS 220 OFM 1/4W 10% FIXED CCMP	515775	4 • 0000	EACH	
	F S			RES 330 OHM 1/4W 10% FIXED CCMP	F15765	1.0000	EACH	
3	FS	330-2047		RES 470 OHM 1/4W 10% FIXED CCMP RES 560 OHM 1/4W 10% FIXED CCMP	E15765	2.0000	EACH	
3	FS	330-2068		RES 680 OHM 1/4W 10% FIXED CCMP	F15765	2.0000	EACH	
3		330-3010		RES 1K OHM 1/4W 10% FIXED COMP	E 15765	2.0000 8.0000	EACH	
3		330-3015		RES 1.5K OHM 1/4W 10% FIXED COMP	C15185	7.0000	EACH	
3		330-3022		RES 2.2K OHM 1/4W 10% FIXED COMP	E 16778	2.0000	EACH	
3	FS	330-3033		RES 3.3K CHM 1/4W 10% FIXED CCMF	E16778	2.0000	EACH	
3	F.S	330-3047		RES 4.7K OHM 1/4W 10% FIXED CCMF	F15765	2.0000	EACH	
3	FS	330-3068		RES 6.8K CHM 1/4W 10% FIXED CCMP	E15765	1.0000	EACH	
3	FS	330-3083		RES 8.2K CHM 1/4W 5% FIXED CCMP	E 15765	2.0000	EACH	
ì	F۶	330-4012		RES 12K CHM 1/4W 10% FIXED CCMP	E15765	1.0707	EACH	
į	FS	30-4015	-	RES 15K OHM 1/4W 10% FIXED COMP	E 15765	1.0000	EACH	
3	FS	330-4040	-	RES 39K CHM 1/4W 5% FIXED COMP	E15765	1.0000	EACH	
3	F.S	31-0010-R -	-	RES 1 OHM 1/2W 10% FIXD CCMP TER		1.0000	EACH	
3	FS	331-2015-R -	-	RES 150 OHM 1/2W 10% FIXD CCMP T&R	E 15765	1.0000	EACH	
3	FS	333-0093-R -	-	RES 2.37K OHM 1/8W 1% FIX FILM T+R	F15765	2.0000	EACH	
3	IN	336-1014	-	RES 1K OHM VAR TRIM SIDE ACL SQ		4.0000	EACH	
3	IN	336-1015	-	RES 10K OHM VAR TRIM SIDE ADJ SQ		2.0000	EACH	
3	1 V	175-0017	-	TSTR 2N3014 560MW 40V SH NPN S 52 TSTR 2N4037 1.0W 40V S PNP S		1.0000	EACH	
3	17	375-0016	-	TSTR 2N4037 1.0W 40V S PNP S		5.0000	EACH	
3	IN	175-90Cj		TRANSIPAD 8977887-1 LARGE TRANSIPAD TO-18 (SMALL) IC 723 VOLTAGE REGULATOR IC LM304 NEG VOLTAGE REGULATOR D035 SIL DIODE 30% 100MA AT 1V TRR		5.0000	EACH	
3	ΙN	375-9004		TRANSIPAD TO-18 (SMALL)		1.0000	EACH	
3	14	376-0066		IC 723 VOLTAGE REGULATOR		4.0000	EACH	
3	. N	376-0134		IC LM304 NEG VOLTAGE REGULATOR		2.0000	EACH	
3	FS	360-1001-R -			F15765	2.0000	EACH	
3		380-10(4-R -		DC35 SIL DIODE 47V, 250MA AT IV -P		3.0000	EACH	
3	FS	380-2033-R -		DID ZEN 1N746A 3.3V 400MW SD07 T+R	E15765	1.0000	EACH	
3	IN	380-3005		1N5823 / MRD5300		1.0000	EACH	
3	ΙN	510-L567	-	PCB 2200A/B/C/S/T PS REGULATOR		1.0000	EACH	

MBOOR-A MULTI-LEVEL BILL OF MATERIAL AS CF RUN DATE: CH/26/81 PAGE 2

ASSEMBLY FART NUMBER 177-2200-80- -

LEGEND

ASSEMBLY DESCRIPTION 2280 DISK PROCESSOR UNIT 1: P=PHANTOM; 2: ITEM MASTER DELY CODE; 3: *=TAGGED OUT OF KIT(PROD STR)

POSITION IN STRUCTURE	LEGEND 1 2 3	CCMFCNENT PART NUMPFR	DESCRIPTION	E C N	QUANTITY PER ASSY	U / M	L/T
3	P FS	600-1002	WIRE 22 GA RED	E16826	.3333	FEET	
· 4	FS	600-1009	WIRE 22 GA RED WIRE 22 GA WHITE		1.0000	FEET	
3	P FS	600-7000	16 GA BLACK STRANDED WIRE	E16826	• 0833	FEET	
4	FŞ	600-7009	16 GA BLACK STRANDED WIRE 16 GA WHITE STRANDED WIRE		1.0000	FEET	
_	• • •	0.0 7.15				EACH	0.0007
2	IN	210-7415	PCA 2200SMD PRIME CIRCUIT LABOR CIRCUIT SYSTEMS		1 • 0 0 C 0 • 6 8 9 0	EALH	00000
3 3	IN	000-0061			1.6680		00000
3	IN	00-0011	LABOR PRODUCTION SYSTEMS LABOR QUALITY CONTROL 1 UF CERAMIC CAPACITOR (HIGH FREG) CAP 100.0 UF 15 V 10% TANT AXIAL RES 2.2K OHM 1/4W 10% FIXED CCMP		•4150		00000
3	IN	300-1931	1 UE CERAMIC CAPACITOR (HIGH EREG)		2.0000	EACH	40000
3	ĬN	300-4021	CAP 100.0 UF 15 V 10% TANT AXIAL		2.0000	EACH	
3	FS	330-3022	RES 2.2K OHM 1/4W 10% FIXED CCMP		1.0000	EACH	
3	FS	331 - 2010 - R	RES 100 OHM 1/2W 10% FIXD CCMP TRR	F15690		EACH	
3	FS	333-0060-R	RFS 4.02K OHM 1/8W 1% FIX FL T+R	E15690	4.0000	EACH	
3	IN	333-0061	RES 9.09K OHM 1/8W 1% FIXED FILM		2.0000	EACH	
3	IN	333-0069	RES 9.09K OHM 1/8W 1% FIXED FILM RES 6.19K OHM 1/8W 1% FIXED FILM		2.0000	EACH	
3.	FS	333-0090-R	RES 10 K OFF 1/PW 1% FIX FILM T+R	515690	6.0000	EACH	
1	IN	334-0002	15 OHM 11 W FIXED RESISTOR		1.0000	EACH	
3	IN	374-0002	15 OHM 11W FIXED RESISTOR IC REG UA 7905 -5V TO-220 IC LM330 A COMPARATOR		1.0000	EACH	
3	IN	376-0240	IC LM339 4 COMPARATOR		1.0000	EACH	
3	FS	380-1001	DO35 SIL DIODE 30V 100 MA AT 1V TRR		3.0000	EACH	
3	FS	380-2062-R	DID ZEN 1N753A 6.2V 400MW SD(7 T+R		2.0000	EACH	
3	IN	510-7415	PCB 2200SMD PRIME CIRCUIT		1.0000	EACH	
3	FS	659-3087	SCR 6-32 1/4 PAN SLOT MS NYL	E12099	1.0000	EACH	
3	FS	652-3002	NUT 6-32UNC HEX REG PAT NYLON	E12099	1.0000	EACH	
2	IN	210-7421-A	PCA 2200SMD ALU/MUX INTERFACE		1.0000	EACH	00023
3	IN	000-0005			.2740		00000
3	I٨	000-0011			.0550		00000
3	PIN	209-7421	LABOR QUALITY CONTROL PCA 2200SMD ALU/MUX INTERFACE LABOR CIRCUIT SYSTEMS LAFOR QUALITY CONTROL		1.0000	EACH	
4	IN	000-0001	LABOR CIRCUIT SYSTEMS		2.1260		00000
4	TN	000-0011	LAPOR QUALITY CONTROL		• 4250		00000
4 .	IN	300-1100	CAP 100 PF 10% 500 V CERAMIC CISC	E10439	1.0000	EACH	
4	IN	300-1150	CAP 150 PF 10% 500 V CERAMIC DISC	19091	1.0000	EACH	
4	FS	*00-1966	CAP .047 UF 50V+80-20% CERAMIC MLD	E13726	20.0000	EACH	
4	IN	300-3011	CAP 100 UF 16V -10+75% ELECT AXTAL		1.0900	EACH	
4	FS	300-4J22-R	CAP 15.0 UF 20V 10% TANT AXIAL TER	E15690	2.0000	EACH	
4	F٥	330-2025	RES 240 OHM 1/4W 5% FIXED COMP	F15690	1.9000	EACH	
4	FS	330-2033	PES 330 OHM 1/4W 10% FIXED COMP	E12553	5.0000	EACH	
4	FS	330-2039	RES 390 OHM 1/4W 10% FIXED CCMP	F15690	8.0000	EACH	
4	FS	125-2047	RES 470 OHM 1/4W 10% FIXED COMP	10091	1.0900	EACH	
4	FS	330-3110	RES 1K CHM 1/4W 10° FIXED CCMP	1 4 9 9 1	9.0000	EACH	
4	F٤	330-5010	RES 100K CHM 1/4W 10% FIXED CCMP	E15690	1.0000	EACH	
4	FS	333-0057-R	RES 16.9K OHM 1/8W 1% FIX FILM T+R	E15690	1.0000	EACH	
4	IN	160-50c6	CONN RECEPT.36 SLD CUP W/O FLOAT	E12101	1.0000	EACH	
4	1 N	376-0302	IC 7400N 4 2 IN POS NAND GATE		1.0000	EACH	
4	IN	176-Dug3	IC 7412N 3 3 IN POS NAND GATE		1.0000	EACH	
4	14	116-03(6	IC 7474K 2 C FDGF TRIG FLIP-FLOP		1 • 0 0 0 0	EVCH	
4	1 🗸	176-0017	IC 7476N 2 UK MA-SLV F/F PRST CLEAR		1.0000	EACH	
4	1 V	76-0010	IC 7404N HEX INVERTER		1.0000	EACH	
4	17	176-0:12	IC 74515 EXP 2 W 2 IN AND OR INV GT		1.0000	EACH	

ACCEMBLY DART NEMBER (177-1770-17-) USON DITCH ERCLESS OF DALT SECONDARY OF DEPHANIOM: ST. LIFM MACTER DELY CODE: 4: *STAGGED OUT OF RITGEROUS STR.)

FOSTITION IN STRUCTURE	LESEND 1 D 3	COMPONENT PART NUMPER	DESCRIPTION	FCN	QUANTITY PER ASSY	UZF	1/1
4	17	176-0:16	TO THERE 4 2 IN MOS NOR CATE		1.0000	EACH	
4	IV	174-054km	IC 7415 2 4-1 LINE DATA SEL MX	F12101	2.0000	EACH	
4	I۸	*76-00-1	TO TARE 4 2 IN FOS AND GATE	E 12101	3.0000	EACH	
4	It.	376-0182	1C 74157 4 2 IN MX		2.0000	EACH	
4	14	176-0751	10 7432 4 2 IN OR GATE		3.0000	EACH	
4	IN	176-0756	IC 9371 2 1 OF 4 DECODER		1.0000	EACH	
4	1.5	3/6-0104	TIC 9602 2 RETRIG RESET MONOSIBL MVP	E10439	1.0000	EACH	
4	IA	376-0139	IC 7414 HEX SCHMITT TRIGGER	E 150 18	5.0000	EACH	
4	ΙV	376-0153	IC 74LS36P HEX BUS DRIVER 3 STATE		2.0000	EACH	
4	1 🗸	* 76 - 91 54	IC 7411 3 3 IN POS AND GATE	PATREL	1.0000	EACH	
4	IN	376-0286	IC 7415374 A LATCHES WITR ST OUTP		1.0000	EVCH	
4	17	376-0257	IC 74LS241 OCTAL RUF/LINE DR/LN REC	PATREL	1.0000	EACH	
4	1 /	376-0303	IC 14LS299 SCHOTTKY H BIT UNIV RES		1.0000	FACH	
4	IV	376-90C3	IC 24 PIN SOCKET BURNDY	E 12101	8.0000	EACH	
4	1 /	376-9005	IC 16 PIN SOCKET CAMBION	F12101	1.0000	EACH	
4	IN	:76-90[8	IC 16 PIN TEKNA #4330	E 12101	1.0000	EACH	
4	FS	* 83-1061-R	DIRS SIL DIODE ROV. 100MA AT 1V TER	£15690	1.0000	FACH	
4	ΙV	449-0247	HANDLE FACEPLATE 86815-123		2.0000	EACH	
4	IN	452-2095-36	PC FACE PLATE (16 PIN) C6422-354	E11039	1.0000	EACH	
4	I۸	461-3140	SCREW CAP 8-32 B6422-231	5 1 1 0 3 c	2 • 0 0 0 0	EACH	
4	IN	461-3141	SCREW CAP HOUSING R6422-233	E11039	2 • 0000	EACH	
4	IN	462-0251	STANDOFF.MALE/FEMALE PA815-56	E1103º	2.0000	EACH	
4	1 N	519-7421	PCB 2200SMD ALU/MUX INTERFACE		1.0000	EACH	
4	FS	650-2120	4-40 X 3/P PAN HD PHL MS SS SEMS		3.1000	EACH	
4	FS	€ £ 1 - C O ₃ Ú	SCRFW.SELF TAP T-R #4X1/2"L FAHD PH		4.0090	EACH	
4	FS	653-2000	NO. 4 FLAT WASHER	E 12191	3.0000	EACH	
4	FS	653-3002	WASH 6 .14110 .25000 .062 FL NYL	E 12553	3.9000	EACH	
3	IN	376-0090	IC 74154 1 OF 16 DECODER DEMX E7758	£12101	1.0000	EACH	
3	IN	376-0059	IC 74181 ARITHMETIC LOGIC UNITS	E 12191	1.0000	EACH	
3	. IN	₹76-C320	IC 74 S412 MULTI-MODE BUFFRD LATCH		1.0000	E AC!	
3	IN	377-0353	AM2905 LOW PWR SCHOTTKY BUS TRNSCVR		4.0000	EACH	
2	IN	210-7422	PCA 2200SMD ECC/DEVICE INTERFACE		1.3000	EACH	00023
3	IN	000-0001	LABOR CIRCUIT SYSTEMS		1.9460		00000
3	I V	000-0005	LABOR PRODUCTION SYSTEMS		2.2680		00000
3	IV	000-0011	LABOR QUALITY CONTROL		·8430		00000
3	FS	300-1966	CAP .047 UF 50V+80-20% CERAMIC MLD		18.0000	EACH	
3	F S	300-4022-R	CAP 15.0 UF 20V 10% TANT AXIAL TER		3.0000	EACH	
3	FS	330-1056	RES 56 OFM 1/4W 10% FIXED CCMP	E 1264P	96.0000	EACH	
3	FS	330-2069	RES 680 OHM 1/4W 10% FIXED CCMP	E15690	2 • 0000	EACH	
3	FS	330-3010	RES 1K OHM 1/4W 10% FIXED COMP	F15690	2.0000	EACH	
3	F S	330-3022	RES 2.2K CHM 1/4W 10% FIXED CCMP	1 476	5.0000	EACH	
3	FS	330-4021	RES 20K CHM 1/4W 5% FIXED COMP	E15690	2.0000	EACH	
1	IV	350-0057	60P 90 DEG HDR ASSY PCB LK & EJECT		1.0000	EACH	
3	IN	350-0058	26P 9C DEG HDR CONN ASSY FOR LK EJT		2.0000	EACH	
3	IV	376-0002	IC 7400N 4 2 IN POS NAND GATE	18992	3.0000	EACH	
3	IN		IC 7410M 3 3 IN POS NAND GATE	F12648	1.0000	EACH	
3	IN	₹76-0104	IC 7429N 2 4 IN POS NAND GATE	E12648	1.0000 2.0000	EACH	
3	IN	376-0010	IC 7404N HEX INVERTER IC 7402N 4 2 IN POS NOR GATE	E10865	1.0000	EACH	
3 3	I N I N	376-0316 376-0331	IC 7412N 4 2 IN POS NON GATE	E10865	3.0000	EACH	
S	I IV	J 10 - 0 J J I	TO 1400 O T 100 HAND DATE	CIUDO:	3 4 0 0 0 0	CACP	

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ASSEMELY FART NUMBER 177-2200-40- -

LEGEND

ASSEMBLY DESCRIFTION 1280 CISK PROCESSOR UNIT 1: P=PHANTOM; 2: ITEM MASTER DELY CODE; 3: +=TAGGED OUT OF KITOPROD STR)

POSITION IN STRUCTURE	LFGEND 1 2 4		DESCRIPTION	E C N	QUANTITY PER ASSY	U/M	L/T
3	IN	376-0036	IC 7486N 4 2 IN EXCLUSIVE OR GATE		4.0000	EACH	
3	IN	376-0061	IC 74C8 4 2 IN POS AND GATE		2.0000	EACH	
3	IN	376-00F2	IC 74157 4 2 IN MX		1.0000	EACH	
3	IN	376-0094	IC 74161 SYNCHRONOUS 4 BIT CCUNTER		4.0000	FACH	
3	IN	₹76-009µ	IC 74174 PEX D TYPE FLIP FLOP	FATREL	2.0000	EACH	
3	I۸	376-0119	IC 74175 4 D TYPF EDGE TRIG F/F		6.0000	EACH	
3	IN	176-0202	IC 74874 2 D TYPE F F W PRESET CLER		1.0000	EACH	
3	IN	₹76-02C5	IC 74532 4 2 IN POS OR GATE		1.0990	EACH	
3	IN	176-0206	IC 745260 2 5 IN POS NOR GATE		1.0000	FACH	
3	1.//	376-0238	IC 74810 3 3 IN POS NAND GATE		1.0000	EACH	
3	IN	376-0255	IC 75:110 2 LINE DRIVER	PATREL	2.0000	EACH	
3	I٨	376-7274	IC MC3453L4 LINE DRIVER		5.0000	EACH	
3	IN	176-0275	IC MC3450F 4 LINE RECFIVER		6.0000	EACH	
3	IN	376-0363	IC 74LS299 SCHOTTKY A BIT UNIV RES IC 25LS2521 9-BIT FQUAL TO CMPRTR		2.0000	EACH	
3	IN	376-0317			2.0000	EACH	
3	IV	449-0247	HANDLE FACEPLATE B6815-123		2.0000	EACH	
3	IN	452-2095-35	FACE PLATE 2200 PHOENIX D6422-348		1.0000	EACH	
3	IN	461-3140	SCREW CAP 8-32 86422-231	E10865	2.0000	EACH	
3	IV	461-3141	SCREW CAP HOUSING B6422-233	E11865	5.0000	EACH	
3	IN	510-7422	PCB 2210SMD ECC/DEVICE INTERFACE		1.0000	EACH	
3	F.S	650-1120	SCR 3-48 3/8 SLOT PH MS SS	E10865	1.0000	EACH	
3	FS	650-1160	SCR 3-48 1/2 SLOT PH MS SS	F10865	6.0000	EACH	
3	FS	651-0030	SCREW SELF TAP T-B #4X1/2 TL FNHD PH	E12648	4.0000	EACH	
2	IN	210-7423-4	PCA 2200SMD RAM/PROM CNTL		1.0000	EACH	00023
3	IN	000-0005	LABOR PRODUCTION SYSTEMS LABOR QUALITY CONTROL PCA 22005MD RAM/PROM CNTL		2.5730		00000
3	IN	000-0011	LABOR QUALITY CONTROL		•5150		00000
3	PIN	209-7423	PCA 2200SMD RAM/PROM CNTL			EACH	
4	IN	000-0001	FARIN CINCUIT SASIEMS		1.2760		00000
4		.000-0011	LABOR QUALITY CONTROL		.2550		00000
4	IN	300-1010	CAP 10 PF 10% 500 V CERAMIC DISC		1.0000	EACH	
4	IN	300-1220	CAP 220 PF 17% 500 V CERAMIC DISC	E11204	1.0000	EACH	
4	IN	300-1906	CAP . DC1 UF 10% 500 V CERAMIC DISC		1.0000	EACH	
4	FS	700-1966	CAP . 247 UF 50V+80-20% CERAMIC MLD		24.0000	EACH	
4	FS	300-4022-R	CAP 15.0 UF 20V 10% TANT AXIAL TRR	£15690	4.0000	EACH	
4	11	321-0008	CRYSTAL 10.0 5 % QUARTZ FC-18/U		1.0000	EACH	
4	F S	130-2022	RES 220 OHM 1/4W 10% FIXED CCMP	2,500	1.0000	EACH	
4	FS FS	330-2033	RES 330 OPM 1/4W 10% FIXED COMP RES 470 OPM 1/4W 10% FIXED COMP	E 1569 C 2 C 2 D D	1.0000 8.0000	EACH	
4	F S	330-3018	RES 1.8K OHM 1/4W 10% FIXED COMP	E 15690	1.0000	EACH	
4	FS	*30-3022	RES 2.2K OHM 1/4W 10% FIXED COMP	E 12634	6.0000	EACH	
4	FS	333-0181-R	RFS 16.2K OHM 1/4W 1% FIX FL T+R	F 15690	1.0000	EACH	
4	IN	376-0302	IC 74 CON 4 2 IN POS NAND GATE	· 1.6 · C	1.0020	EACH	
ú	IN	376-0006	IC 7474N 2 D EDGE TRIG FLIP-FLOP	E10675	2.0000	EACH	
4	1 N	376-0008	IC 7442N 4 LINE-10 LINE DECODER	E 12634	1.0000	EACH	
4	IN	376-0010	10 7404N HEX INVERTER	. 12054	3.2022	EACH	
4	ĪN	376-0012	IC 7451N FXP 2 W 2 IN AND OR INV GT		1.0000	EACH	
4	IN	375-0016	IC 7402N 4 2 IN POS NOR GATE	F10679	1.0000	EACH	
4	1 /	376-3044	IC 74153 2 4-1 LINE DATA SEL MX		1.0000	EACH	
4	IN	176-00-3	IC 74193 SYN 4 BIT UP DOWN CCUNTER	F11679	4.0000	EACH	
4	I٨	376-9981	IC 7408 4 2 IN POS AND GATE		2.0000	E AC F	
4	1 /	376-0092	IC 74157 4 2 IN MX	E10679	2.0000	EACH	

ASSEMBLY FART NUMBER 177-2270-40- -ASSEMBLY PART NUMBER 177-2200-F0- - LIGEND
ASSEMBLY DESCRIPTION 0240 DISK PROCESSOR UNIT 1: PEPHANTOM; 2: ITEM MASTER DELY CODE; 3: *=TAGGED OUT OF KIT(PROD STR)

POSITION IN STRUCTURE	LEGENT 1 2 T	COMPONENT Part Number	DESCHIPTION	FCN	QUANTITY PFR ASSY	U/F L/T	
4	IN	376-0003	IC 7432 4 2 IN OR GATE	E10679	5.0000	EACH	
4	IN	376-3096	IC 9321 2 1 OF 4 DECODER		1.0000	EACH	
4	ĪŇ	376-0657	IC 74195 4 BIT PAR ACCESS SHIFT REG	E 12634	1.0000	EACH	
4	I۸	376-9098	IC 74174 HEX D TYPE FLIP FLOF		3.0000	EACH	
4	IN	376-0104	IC 9602 2 RETRIG RESET MONOSTPL MVR		1.0000	EACH	
4	IN	376-0125	IC 74?7 3 3 IN NOR GATE		1.0000	EACH	
4	IN	176-0139	IC 7414 HEX SCHMITT TRIGGER	E10679	1.0000	EACH	
4	IN	376-0191	IC 74160 SYN 4 BIT CTR	E10679	1.0000	EACH	
4	IN	376-0294	IC 74LS138 3-8 LINE DECODER/MPX	E10679	1.0000	EACH	
4	IN	*76-9002	IC 16 PIN SOCKET BURNDY	E10679	2.1000	EACH	
4	IN	376-9003	IC 24 PIN SOCKET BURNDY	F10679	6.0000	E A C H	
4	IN	376-9014	IC 18 PIN SOCKET	E10679	18.0000	EACH	
4	1 N	376-9020	IC 20PIN SOCKET DIL LOW PROFILE	E10679	3.0000	EACH	
4	IN	510-7423	PCB 2200SMD RAM/PROM CNTL		1.0000	EACH	
3	I٨	377-9341-L	2114L 1KX4 STATIC RAM 450NS L PWR	E11118	18.0000	EACH	
3	17	377-0347	2911 PIPOLAR MICROPRGRM SEQUENCES		3.0000	EACH	
3	IN	377-0353	AM2905 LOW PWR SCHOTTKY BUS TRNSCVR		2.0000	EACH	
3	FS	378-4083-R7	2280 CPU MICROCODE CHIP #4 L13	16416	1.0000	EACH DCDO3	
4	IN	377-0348	TMS2716 2KX8 EPROM 12V		1.0000	EACH	
3	FS	378-40P4-R7	2280 DPU MICROCODE CHIP #3 L14	18418	1.0000	EACH 00003	
4	IN	377-0348	TMS2716 2KX8 EPROM 12V		1.0000	EACH	
3	FS	378-4085-R7	2280 DPU MICROCODE CHIP #1 L15	18418	1.0000	EACH 00003	
4	IN	377-0348	TMS2716 2KX8 EPROM 12V		1.0000	EACH	
3	FS	378-4086-R7	2280 CPU MICROCODE CHIP #2 L16	10418	1.0000	EACH 00003	
4	IN	377-0348	TMS2716 2KX8 EPROM 12V	• • • • •	1.0000	EACH	
2	IN	210-7424	PCA 2200SMP I/O CONTROLLER		1.0000	EACH 00023	
3	IN	000-0001	LABOR CIRCUIT SYSTEMS		.9510	00000	
3	IN	^00-0005	LABOR PRODUCTION SYSTEMS		1.4020	20000	
3	ΙN	000-0011	LABOR QUALITY CONTROL		.6710	00000	
3	IV	300-1220	CAP 220 PF 10% 500 V CERAMIC DISC	E 12563	1.0000	EACH	
3	I٨	300-1470	CAP 470 PF 10% 500 V CERAMIC CISC	E 12231	3.0000	EACH	
3	FS	300-1966	CAP .047 UF 50V+80-20% CERAMIC MLD		17.0000	EACH	
3	FS	300-4022-R	CAP 15.0 UF 20V 10% TANT AXIAL TRR		2.0000	EACH	
3	FS	330-3022	RES 2.2K OHM 1/4W 10% FIXED CCMP	E 15690	3.0000	EACH	
3	IN	376-0002	IC 7400N 4 2 IN POS NAND GATE		2.0000	EACH	
3	IN	376-0004	IC 7420N 2 4 IN POS NAND GATE	F12490	2.0000	EACH	
3	IN	376-00(6	IC 7474N 2 D EDGF TRIG FLIP-FLOP		6.9000	EACH	
3 3	I V	376-0J10	IC 7404N HEX INVERTER	18094	3.0000	EACH	
3	IN	376-0012	IC 7451N EXP 2 W 2 IN AND OR INV GT		4 - 2000	EACH	
3	IN	376-0016	IC 74C2N 4 2 IN POS NOR GATE	E 12563	2.0000	EACH	
3 ,	IN	376-0053	IC 74193 SYN 4 BIT UP DOWN CCUNTER		3.0000	EACH	
3 3	IN IN	376-0081 376-0093	IC 7408 4 2 IN POS AND GATE IC 7432 4 2 IN CR GATE	C 1 0 C 4 3	5 • 0 0 0 0	EACH	
3	IN	376-0093 376-0096	IC 9321 2 1 OF 4 DECODER	E 12563	2.0000	EACH	
3	IN	376-0139	IC 7414 HEX SCHMITT TRIGGER	18694	2.0000	EACH	
3	IN	376-0194	IC 7411 3 3 IN POS AND GATE	10074	1.0000 4.0000	EACH EACH	
3	IN	176-023A	IC 7411 3 3 IN POS NAND GATE	E10725	2.0000	EACH	
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MBOOR(-A MULTI-LEVEL BILL OF MATERIAL AS CF

RUN DATE: 0H/26/H1

FAGE 6

ASSEMBLY FART NUMBER 177-2240-HO- -ASSEMBLY DESCRIPTION 22HO DISK PROCESSOR UNIT

LFGFND

1: P=PHANTOM; 2: ITEM MASTER DELY CODE; 3: 4=1	FAGGED OUT OF KITCPROD STRI
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POSITION IN STRUCTURE				F C N	QUANTITY PER ASSY	U/M	L/T
3	1 N	376-0286	IC 74LS374 8 LATCHES W/TR ST OUTP		4.0000	EACH	
3	IN	376-0288	IC 74LS244 OCTUAL RUF/LINE DR 3 OUT		1.0000	EACH	
3	IN	376-0298	1C 745138 3 TO 8 LINE DECODER/MPX		1.0000	EACH	
3	IN	376-0303	IC 74LS299 SCHOTTKY 8 BIT UNIV RES		1.0000	EACH	
3	IN	376-0317	IC 25LS2521 8-BIT EQUAL TO CMPRTR		1.0000	EACH	
3	ΙN	376-9318	IC 74276 QUAD J-K FLIP FLOPS		3.0000	EACH	
3	IN	510-7424	PCB 22005MD I/O CONTROLLER		1.0000	EACH	
3	FS	600-9012	IC 74LS244 OCTUAL RUF/LINE DR 3 OUT IC 74S138 3 TO 8 LINE DECODER/MPX IC 74LS299 SCHOTTKY 8 BIT UMIV RES IC 25LS2521 8-BIT EQUAL TO CMPRTR IC 74276 QUAD J-K FLIP FLOPS PCB 22COSMO 1/O CONTROLLER 24 GA YELLOW SOLID TEFLON WIRE	17991	-1042	FEET	
2	IN	223-0138	12 *EXTENSION CAPLE (35C) B6482-16 LABOR SUB-SYSTEMS LABOR QUALITY CONTROL CONN 18-36 CAPLE TO PANEL PLLG STRAIN RELIEF CVR 36 POS 180DEG GRV STRAIN RELIEF CVR 36 POS 180DEG TNG 4-40X3/8 CAPT SCR FOR SCR MT CONNS.		1.0000	EACH	00010
3	IN	000-0004	LABOR SUB-SYSTEMS		.6980	EACH	00000
1	IN	000-0011	LABOR QUALITY CONTROL		.1400		10000
3	IN	350-2082	CONN 18-36 CARLE TO PANEL PLIG	FC6407	2.0000	EACH	
3	11	350-4228-G	STRAIN RELIEF CVR 36 POS 180CEG GRV	EC8484	2.0000	EACH	
3	IN	350-4228-T	STRAIN RELIEF CVR 36 POS 180DEG TNG	EC8484	2.0000	EACH	
3	IN	350-4234	4-40X3/8 CAPT SCR FOR SCR MT CONNS.	EC6407	4.0000	EACH	
3	FS	420-0054	36 COND 26 GA SHIELDED CABLE	EC6407	13.0000	FEET	
3	IN	458-0361	GROUND STRAP C6815-28	EC6407	2.0000	EACH	
3	IN	605-1011	36 COND 26 GA SHIELDED CABLE GROUND STRAP C6815-28 TY-WRAP IDENT MARKER	E15816	1.0000	EACH	
2	IN	220-3032	30 PAIR FLAT CA ASSY(5.) D6482-188		1.0000	EACH	00010
3	IN	020-0004	LABOR SUB-SYSTEMS		.2370	EACH	00000
3	IN	000-0011	LABOR QUALITY CONTROL		• 0 4 7 0		00000
3	I N	350-0414		EC8371	2.0000	EACH	
3	IN	350-4120	PULL TAR FOR FLAT CABLE	ECP371	2.0000	EACH	
3		420-0070	30 THISTED PAIR/FLAT RIBBON CABLE		5.0000	FEEI	
3	FS	660-0156	30 TWISTED PAIR/FLAT RIBBON CABLE GLUE.HOTMFLT(.750DX1.375LG)			EACH	
?	I N I N	220-3033-5	26 COND.SHIFLDED FLT CRLE C6482-189 LABOR SUB-SYSTEMS LABOR QUALITY CONTROL 13-26 RECEPT CONN .100		1.0000	EACH	00010
3	IN	000-0004	LABOR SUB-SYSTEMS		• 4770	EACH	C C O O C
3	IN	000-0011	LABOR QUALITY CONTROL		• 0 9 5 0		00000
3	IN	350-9413	13-26 RECEPT CONN .100		2.0000	EACH	
3	IN	350-4119	PULL TAR FOR FLAT CARLE		1.0000 5.0000	EACH	
3	FS	420-0069	26 COND FLAT CABLE W/GRND PLANE		5.0000	FEET	
3	FS	669-0195	LABOR SUB-SYSTEMS LABOR QUALITY CONTROL 13-26 RECEPT CONN • 100 PULL TAB FOR FLAT CABLE 26 CCND FLAT CABLE W/GRND PLANE HOT MFLT GLUE	E13142	. 0400	L B	
	IN	270-0575	PHCENIX DR INTTRFC TC VPA/MVPA 22PO		1.0000	EACH	00010
3	IN	000-0004	LABOR SUB-SYSTEMS LABOR PRODUCTION SYSTEMS LABOR QUALITY CONTROL PCA 2200 SMD MOTHERBOARD LABOR SUB-SYSTEMS LABOR QUALITY CONTROL		3.0000	EACH	00000
3	IN	100-0005	LABOR PRODUCTION SYSTEMS		.1210		00000
3	IN	000-0011	LABOR QUALITY CONTROL		.6240		0 0 0 0 0
3	IN	210-7416	PCA 2200 SMD MOTHERBOARD	E15193	1.0000	EACH	00023
*	IN	000-0024	LABOR SUB-SYSTEMS		1.3300	EACH	0 0 0 0 0
4	1 N	000-0011	CAPOR QUALITY CONTROL		•2660		00000
4	I N I N	300-4032					
4	IN FS	303-4041		F15000	1.0000	EACH	
4	IN	331 - 3010 - R	RES 1K CHM 1/2W 10% FIXD COMP TRR	F 1 F 4 7 D	4.0000		
4	I N	334-2010			1.0000		
4	1 \	334-0014	I OUM AN EM EIMED DECICEOD		1.0000		
4	1 1	334-0016 334-0023	TO UTTO THE DESTRICTION OF THE TOTAL STREET OF THE STREET	DATEC	2.0000		
4	IN	357-0011	225-21521-110 PC CONN SOLDER TYPE	CIIOIE	3.0000		
4	IN	380-3002		c:1512	12.0000	EACH	
,	4.4		010 194717 304 SA RECT S (60		4.7000	EACH	

ASSEMBLY PART NUMBER 177-2233-60- - LEGEND
ASSEMBLY PESCRIPTION 7283 DISK PROCESSON UNIT : PEPHANICM; 2: ITEM MASTER DELY CODE: 3: *=14GGED OUT OF KIT(PROD STR)

POSITION IN		COMPONENT PART NUMBER	DESCRIPTION	FCN	GUANTITY PER ASSY	U/M	L/T
4	I۸	517-7416	PCB 2250 SMD MOTHERBOARD		1.0000	EACH	
4	IN	654-1172	12 POS PIN HEADER ASSY AMP 350213-1	F11683			
4	I۸	654-1177	16 DOS DE ETN HEADER ASY AMPRECIALA	F11607	1.0000	EACH	
4	IN	654-1158	6 000 DIN UEADED AGEN AND TEAGED A	F 4 4 4 0 F	1.0001	FACH	
•	• ''	234-117	E 1 ,5 Fig Weaden #301 Am 33e257 1		10.00	LACI	
3	IN	270-0256	2 POS PIN HEADER ASSY AMP 350204-1 2200S HEAT SINK ASSY 6527-8 LAPOR PRODUCTION SYSTEMS LABOR QUALITY CONTROL OTHER DIRECT COST 2200S HEAT SINK HARNESS LAPOR SUB-SYSTEMS LABOR QUALITY CONTROL		1.0076	EACH	
4	IN	000-0005	LAPOR PRODUCTION SYSTEMS		• 375 n		30000
4	IN	000-0011	LABOR QUALITY CONTROL		• 075 0		00000
4	IN	0 -0- 9999	OTHER DIRECT COST		13.1490		00000
4	1 🗸	279-3043	22 COS HEAT SINK HARNESS		1.0070		
£	IN	0 60 - 3004	LAROR SUB-SYSTEMS		• 4960	EACH	0 0 0 0 0
ŗ.	IN	000-0011	- · · · · · · · · · · · · · · · · · · ·		• 1990		00000
Ė	b Eć	677-8860	WIRE 18 G4 BLACK UL		•5800	FEET	
4	FS	600-0009	WIRE 18 GA WHITE UL		1.0000	FEET	
5	P FS	690-0091	WIRE 18 GA PROWN UL		·5800	FEET	
£	FS	600-0005	WIRE 18 GA WHITE UL		1.0000	FFFT	
	•						
÷.	P FS	600-0002	WIRE 18 GA RED UL		1.4200	FEET	
€	FS	610-1009	WIRE 18 GA WHITE UL		1.0000	FEET	
ř.	P FS	600-0003	WIRE 18 GA ORANGE UL		1.1700	FEET	
· +	FS	600-0009	WIRE 18 GA ORANGE UL WIRE 18 GA WHITE UL		1.0000		
		630-6000-	WIN: 18 GA WHITE OL		10000		
5	P FS	600-0014	WIRE 18 GA YELLOW UL		1.1730	FEET	
6	FS	609-09C3	WIRE 18 GA WHITE UL		1.0000	FEET	
E	P FS	600-0005	WIRE 18 GA GREEN UL		.6700	FEET	
5 6	FS	600-0009	WIRE 18 GA WHITE UL		1.0000		
•	rs	670-0304	WINE IN GA WHITE OL		1.0000	r:cr	
5	P FS	. 600-0006	WIRE 18 GA PLUE UL		1.1730	FEET	
f	FS	600-0009	WIRE 18 GA WHITE UL		1.0000	FEET	
f.	P FS	600-0007	WIRE 18 GA VIOLFT UL		6700		
f	FS	600-0009	WIRE 18 GA WHITE UL		1.2900	FEFT	
Ę	FS	600-0009	WIRE 18 GA WHITE UL		•5800	FEET	
į	P FS	600-0050	WIRE 18 GA WH/BLK		1.0000	FEET	
6	FS	600-0009	WIRE 18 GA WHITE UL		1.0000	FEET	
F	P FS	600-0052		EC8399	1.0400	FEET	
F	F S	600-0009	WIRE 18 GA WHITE UL		1.0000	FEET	
E	P FS	600-2005	WIRE 24 GA GREEN UL W/CFF-76		1.0000	FEET	
4	FS	600-2009	WIRE 24 GA WHITE UL		1.0000	FEET	
5	FS	600-2009	WIRE 24 GA WHITE UL		1.1700		
5	P FS	660-56c0	WIRE 24 GA WH/BLK UL		.5800		
F	FS	600-2009	WIRE 24 GA WHITE UL		1.0000	FEET	
5	P FS	600-20°1	WIRE 24 GA WH/BRN UL		.5800	FEET	
	FS	600-2009	WIRE 24 GA WHITE UL		1.0000		
	J						

MRD CRC-A MULTI-LEVEL PILL OF MATERIAL AS CF. RUN DATE: 08/26/81 PAGE &

ASSEMPLY FART NUMPER 177-2200-80- -

LEGEND

ASSEMBLY DESCRIPTION 1240 DISK PROCESSOR UNIT 1: P=PHANTOM; 2: ITEM MASTER DELY CODE; 3: *#TAGGED OUT OF KIT(PROD STR)

		COMPONENT PART NUMBER		E C N	GUANTITY PER ASSY	U/M L/T
t.	p rç FS	601-2052 600-2009	WIRE 24 GA WH/RED UL WIRE 24 GA WHITE UL	EC8399	1.1709	
5	P FS FS	600-2093 600-2009	WIRE 24 GA WHIORN UL WIRE 24 GA WHITE UL		1.1700 1.0000	
£	P FS FS	600-2094 600-2009	WIRE 24 GA WHIYEL UL WIRE 24 GA WHITE UL	RF2285	1.0400	
5 6	P FS FS	6/00-2095 6/00-2009	WIRE 24 GA WHIGEN UL WIRE 24 GA WHITE UL		.6700 1.0000	FEET FEET
5 f	P FS FS	600-2057 600-2009	WIRE 24 GA WH/VIO UL WIRE 24 GA WHITE UL 16 GA RED STRANDED WIRE 16 GA WHITE STRANDED WIRE		.6700 1.0000	FEET FEET
v. F	P FS FS	600-7002 600-7009	16 GA RED STRANDED WIRE 16 GA WHITE STRANDED WIRE		.9200 1.0000	
r F	P FS FS	600-7003 600-7009	16 GA ORANGE STRANDED WIRE 16 GA WHITE STRANDED WIRE		1.17n0 1.0000	
£ .	P FS FS	610-7006 600-7009	16 GA PLUE STRANDED WIRE 16 GA WHITE STRANDED WIRE		1.7500 1.9000	
5 5 5 5 5	IN FS * FS *	605-1004 606-3043 654-1165-R 654-1171 654-1176	3/4" CIA WHT SHRINK BLK NU 270-3043			EACH CCOOL EACH EACH
4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	FS + FS + FS +	375-1008 375-1022 375-1022 375-9014 375-9015 375-9000 380-4003 380-4003 400-0097 600-0097 600-0097 600-0097 6005-0169 605-0169 605-0169 605-0169 605-0169 605-0169 605-0169 605-0169 605-0169 605-0169	40250 / 2N4231A PWR TRANSIST(R 2N5956 TRANSISTCR TRANSISTOR 2N5685 300W S NPN TO-3 INSULATOR XTOR MOUNT WECKESSER TM-1 INSULATOR XTOR MOUNT WECKESSER TM-2 MICA WSHR (SMALL) FOR POWER XISTORS MICA WSHR (LARGE) FOR POWER XISTORS DIO 1N1200A 100V 12A RECT S DA4 M-7110 MDA 970-1 RECTIFIER MICA WSHR .19IDX.630DX.003THK(3000) HFATSINK, (2200S)D6627-11 18 GA WIRE WH/VIOLET WIRE 18 GA TINNED COPPER BUS (UL) WIRE 18 GA TINNED COPPER BUS (UL) TUPING NPR 12 CLEAR TUFING NO 6 CLEAR	FC9826 10280 EC6584 EC6584	6.0000 2.0000 1.0000 8.0000 8.0000 2.0000 1.0000 2.0000 1.0000 2.1000 2.1000 2.1000 1.0000	EACH EACH EACH EACH EACH EACH EACH EACH
4	FŞ	650-2160	4-40 X 1/2 PAN HO PHL MS SS SEMS		1.0000	EACH

ASSEMPLY FART NUMPER 177-2200-80- -ASSEMBLY DESCRIPTION TOAD DISK PROCESSOR UNIT LFGFND

1: PEPHANICM; 2: ITEM MASTER DELY CODE; 7: *=TAGGED OUT OF KIT(PROD STR)

POSITION IN STRUCTURE		CCMFONENT PART NUMPER	DESCRIPTION	f C N	CUANTITY PER ASSY	טיי ניז
4	F S	650-3160	6-32 X 1/2 PAN HD PHL MS SS SEMS	1:0280	22.0000	FACH
4	FS	652-3004	ALL TAR VALLET AND THE FEAT THE	100280	55.0000	
4	FS	652-67(0	NUT 6-32UNC HEX SMALL PAT SS NUT 10-32UNC HEX REG PAT SS	F15082	2.0000	
i.	FS	653-3000	WASH 6 .149ID .3750D .016 FL SS	100280	23.0000	
4	FS	653-3701	WASH 6 .150ID .2880D INT T ST		11.0000	EACH
4	FS	653-6100	WASH 10 -207ID -4380D -032 FL SS	F15082	2.0000	
4	FS	553-6701	WASH 10.20410 .38100 INT T ST	F 15082	2.0000	
4	I٨	654-01-1	SOLDER GROUND LUG DO-4	E 15082	2.0000	
4	IN	654-1006	#6 GROUND LUG	100280	11.0000	
4	IN	654-1319	SHOULCER PUSHING DO-4	515082	5 • 0000	EACH
3	11	311-3310			2.0000	EACH
3	IN	309-3049	77000 UF 15V ELECTROLYTIC CAFACITOR		1.0000	EACH
3	ΙN	300-3068	P. 2K UF 25V FLECTROLYTIC CAP		2.0000	EACH
3	IN	300-5009	CAP CLAMP 1 1/4 INCH 2 LUG CMC-22		2.0000	EACH
3	IN	300-5022	CAP CLAMP 2 1/16 INCH 3 LUG		1.0000	EACH
3	IN	325-0023	DPST TOGGLE SW 15 A 125V CH 7561K4		1.0000	EACH
3	IN	360-0000	FUSE HOLDER 90 DEGREE CONTACT		1.0000	EACH
3	IV	360-9000	RUPBER WSHR FOR 360-1000 / 360-0001		1.0000	EACH
3.	IN	360-9302	HEX NUT FOR 360-1000 / 360-0001		1.0000	EACH
3	IN	360-9003	LOCK WSHR LF#905023(FOR 360-0000/1)		1.0000	EACH
3	IN	380-5000	130 VOLT VARISTOR V130LA10	E 11589	1.0000	EACH
3	IN	400-1009	FAN. WHISPER (SKELETON) ROTRON WR2A2	E 17208	1.0000	
3	IN	410-0098	XFMR LNR MDL 2208/2208E		1.0000	
3	IN	420-1005	XFMR LNP MDL 2205/2205E C5068-99 POWER CORD ROTRON FAN 16415 FOWER CORD+10 FT 18AWG 2200S CPU & PWR SUPPLY CHAS E6627-6 2200S M≏THER BOARD COVER D6627-10		1.0000	
3	IN	429-1096	FOWER CORD 10 FT 18AWG		1.0000	
3	IN	451-1085	2200S CFU R PWR SUPPLY CHAS E6627-6		1.0000	
3	IN	451-2100	22 TUS MATHER BUARD COVER D66 27-10		1.0000	
3 3	I N I N	458-0196	2217 CAPD HOLD DOWN (T-1)C53GC-1060			
3	IN	461-0132 462-0105		E11837		
3	IN	462-0141	SPUR 4-4 TUNC -270 PENT A-250 E FX B	E 1230:	6.0000	
3	IN	462-0141-	SPCR + PHENOLIC CURRENT 4-250	612303		
	P FS	600-0200-	UTDE 10 GA DIACK III		4 • 0 0 0 0 1 • 1 7 0 0	
4	FS	600-0005	WIRE 18 GA WHITE UL		1.0000	
3	FS	611-0019	SPCR. HEX 6-32 X 1 3/4 P6800-110 WIRE 18 GA PLACK UL WIRE 18 GA WHITE UL WIRE 18 GA TINNED COPPER PUS (UL) TUBING PVC #9 CLEAR #3 CLEAR TUBING TUBING 3/8 PLACK TUBING NO 6 CLEAR TEFLON TUBING #20 PENNTUBE#1-5116		1.1700	FELT
3	FS	600-9018	WIRE 18 GA TINNED COPPER PHS (III)		•2100	
3	FS	605-0010	TUBING PVC #9 CLEAR	F 16875	•1600	
3	FS	605-0015	#3 CLEAR TURING		2.3400	
3	FS	635-0103	TURING 3/8 BLACK		.8300	
3	FŞ	6 05 - 01 (9	TURING NO 6 CLEAR		.2100	
3	FS	605-0124	TEFLON TUBING #20 PENNTUBE#1-5116	E 11814	.0830	ROLL
3	FS	675-1004	CABLE TYE . PAN-TY PLTIM-M			EACH
3	FS	650-2121	SCR 4-40 3/R PHIL FLAT H MS SS	E11837	4.0000	
3	F.S	650-2240	4-40 X 3/4 PAN HD PHL MS SS SEMS	E 16875	18.0000	
3	FS	650-3080	6-32 X 1/4 PAN HO PHL MS SS SEMS		7.0000	EACH
3	Fς	650-3160	6-32 X 1/4 PAN HO PHL MS SS SEMS 6-32 X 1/2 PAN HO PHL MS SS SEMS		8.1000	EACH
3	FS		SCR 6-32X5/8 PAN HD PHL SEMS MS SS	E16875	3.0000	EACH
3	FS	650-3560			4.0000	E AC H
3 .	F.S		8-32 X 1/2 PAN HD PHL MS SS SEMS		4.0000	EACH
3	FS	650-5161	SCR 10-24 1/2 PHIL PH MS SS		5.0000	EACH



ASSEMBLY FART NUMBER 177-2200-80- -ASSEMBLY DESCRIPTION 2280 DISK PROCESSOR UNIT

A-1300EM

LEGEND

1: P=PHANICM; 2: ITEM MASTER DELY CODE; 3: *=TAGGED OUT OF KIT(PROD STR)

POSITION IN STRUCTURE				FCN	QUANTITY PER ASSY	U/M	L/T
3	FS	651-6160-	 10-32 X 1/2 PAN HD PHL MS SS SEMS	E16875	10.0000	EACH	
3	FS	652-0029-	 R-32 LOCK-NUT KEPS 511-08180C-50 6-32 LOCK-NUT KEPS 511-061800-00 4-40 SQUARE NUT 4-40 LOCK-NUT KEPS SS		4.0000	EACH	
3	FS	652-0032-	 6-32 LOCK-NUT KEPS 511-061800-00		3.0000	EACH	
3	FŞ	652-2302-	 4-40 SQUARF NUT	E16875	4.0000	EACH	
3		652-2005-	 4-40 LOCK-NUT KEPS SS	E16875	15.0000	EACH	
•	IN	652-3000-	NUT 6-32UNC HEX REG PAT SS		1.0000		
3	FS	653-1003-	WASHER. NO.4 NYLON 1/8 ID X 1/8 OF	E16875	8.0000	EACH	
3	FS	653-2000-	NO. 4 FLAT WASHER		2.0000	EACH	
3 3	F Ş	653-2901-	NO. 4 FLAT WSHR 1/8ID 1/40D	E16875	12.7000	EACH	
3	FS	653-20C2-			2.1010	EACH	
3 3	FS	653-3000-	WASH 6 .14910 .37500 .016 FL SS		4.0000	EACH	
3	F S	653-3701-	WASH 6 .150ID .2880D INT T ST		8.0000		
3	F S	653-3003-	WASH 6 .1411D .2530D SPLIT SS		1.0000	EACH	
3 3	F S	653-4000-	WASH 8 .17410 .37500 .016 FL SS		4 • 0000	EACH	
3 3	FS	653-6061-	WASH 10.2041D .3810D INT T ST		2.0000	EACH	
3	IN	654-1006-	#6 GROUND LUG		1.0000	EACH	
, t	I N I N	654-1010-	HIN GROUND LUG		6.0001	EACH	
·	IN	654-1238- 654-1245-	METCO SIKAIN KELIEF SK5P-4		1.0000	EACH	
3	IN	655-0119-	2200 DC HANDLE DC 00 100		1.0000	EACH	
1	IN	655-0203-	52 00		1.0000	EACH	
3	I۸	655-0208-	ELET HATTE COLEME DA 504P		4.0000	EACH	
-	1"	H 23 - 02 CO-	 WASH 6 .14110 .25400 SPLIT SS WASH 8 .1741C .37500 .016 FL SS WASH 10.2041D .3810D INT T ST #6 GROUND LUG #10 GROUND LUG HEYCO STRAIN RELIEF SR5P-4 SNAP RUSHING, HEYCO SB-1501-21 2200 PS HANDLE B6422-108 FFET PLACK GREENE BH 2096 FFET WHITE GREENE BH-2184		4.0000	EACH	
2	I٨	449-00-6-	 FACE PLATE P. C. PLANK C6422-3C5	F 1 F 0 0 7	3 0000	EACH	
2	IN	449-0273-	 PLANK FACE PLATE B 2290 D6884-1029	E15007	1.0000	FACH	
5	IN	450-0904-	 WANG NAME TAG CA815-97		1.0000		
?	IN	451-2101-	 2200S CFU COVER (6 1/0) P6627-9		1.0000		
2	F٥	650-4165-	 SCR 8-32 1/2 SLOT PH MS SS		8.0000		
	FS	657-4480-					
2	FS	660-n3£8-	 NEOPRENE SPONGE TP GREENE 2218 3/8"		1.0000	FEE	
2	IN	690-0312-	 #F SHIPPING BAG	E:1690	3.0000	EACH	
1	IN	290-0015-	 SHPG PKG BOM:F/R-DISK-DRIVE	18250	1.0000	EACH	00010
2	IN	685-0014-	 TAPE 3" PAPER GUMMED NON-ASPH REINE STRAPPING STEEL .50 WD .015 THK		• 0000		0.010
5	IN	685-0017-	 STRAPPING STEEL .50 WD .015 THK		•0000	ROLL	
2	IN	685-0019-	 PIICKI E MAI METAL SA UTDE	10707	2.0000	EACH	
2	IN	685-0100-	 EDGE PRICIT 21/2x21/2x3 L-BOARD .06 PALLET 40 X 32 CUSHIONED HSC 500# DW 39 1/4 X 32 1/4 X 25 RSC 26 X 20 X 22 DW275		4.0000	EACH	
2	IN	685-0286-	 FALLET 40 X 32 CUSHIONED		1.0000	EACH	
2	IN	685-0287-	HSC 500# DW 39 1/4 X 32 1/4 X 25			EACH	
2	IN	685-0307-	RSC 26 X 20 X 22 DW275	2 - 01 C	•0000	EACH	
2	I٨	685-0332-	FUAR-IN-FLACE "A" CREMICAL .T PCF	20010		EACH	
5	IN	685-0333-	 FOAM-IN-PLACE "R" CHEMICAL .5 PCF	20010	•0000		

END OF REPORT MEDERO-A

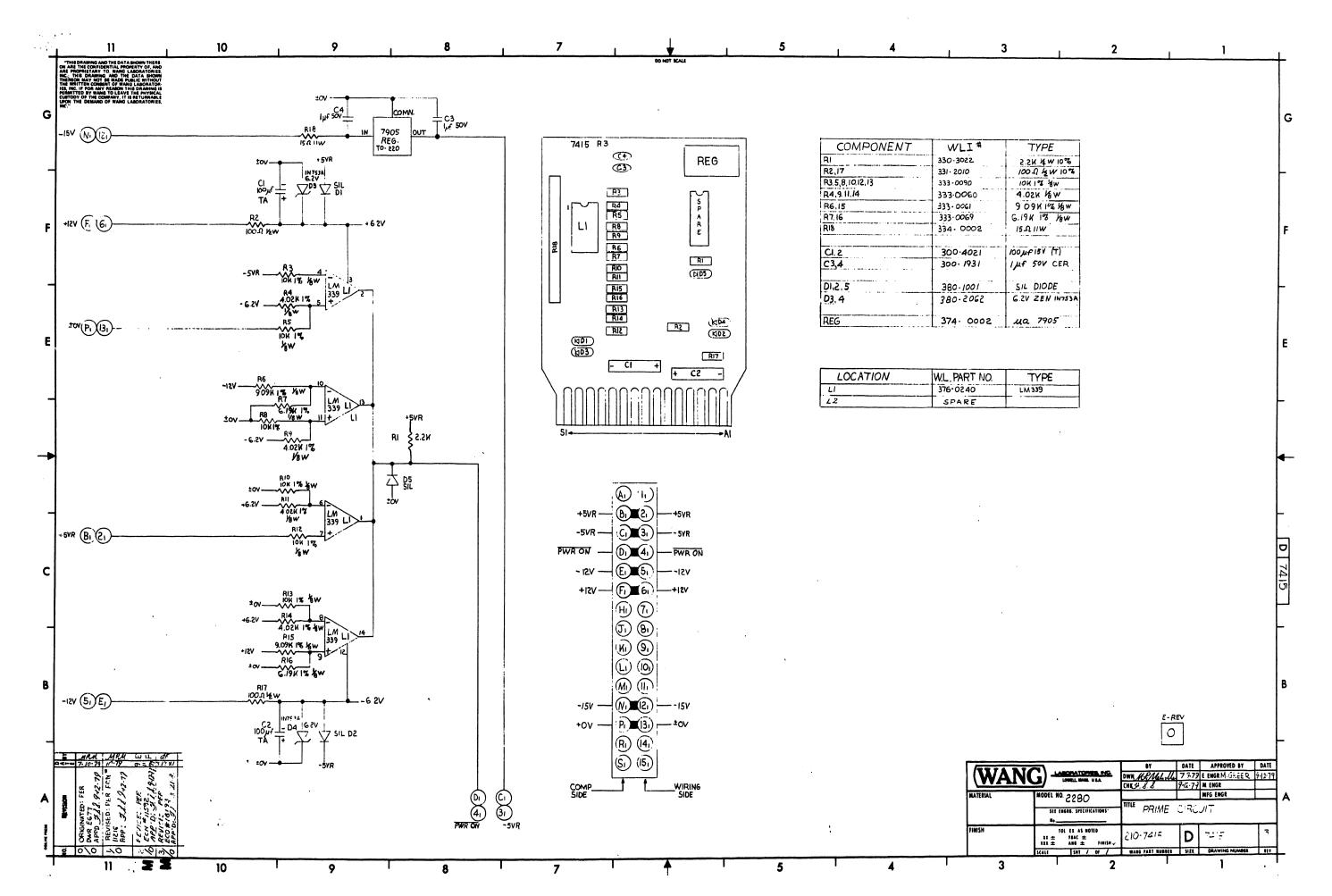
APPENDIX SCHE-MATICS

APPENDIX D

SCHEMATICS

This appendix contains all of the pertinent schematics relating to the 2280 DPU. These are listed below as follows:

WL Number	Nomenclature
210-7415	Prime Circuit Board
210-7416	Motherboard
210-7421	ALU/MUX Interface Board
?10 - 7422	ECC/Device Interface Board
210-7423	RAM/PROM Control Board
210-7424	I/O Controller Board
210-7715	2280 MUX Disk Conntoller
210-7716	Motherboard
210-7717	2280 MUX Master
210-7718	2280 MUX Slave
210-L567	Regulator Board



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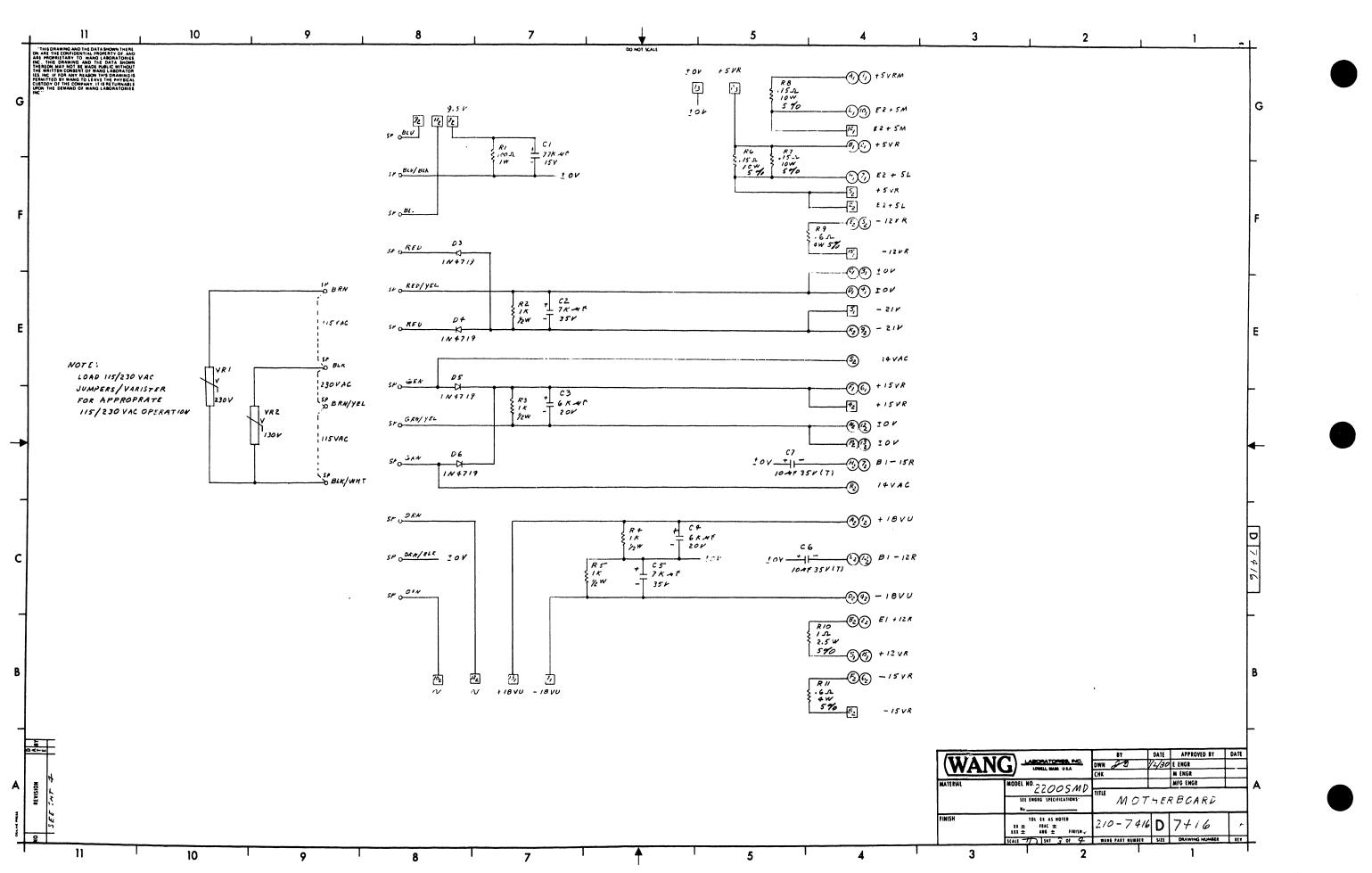
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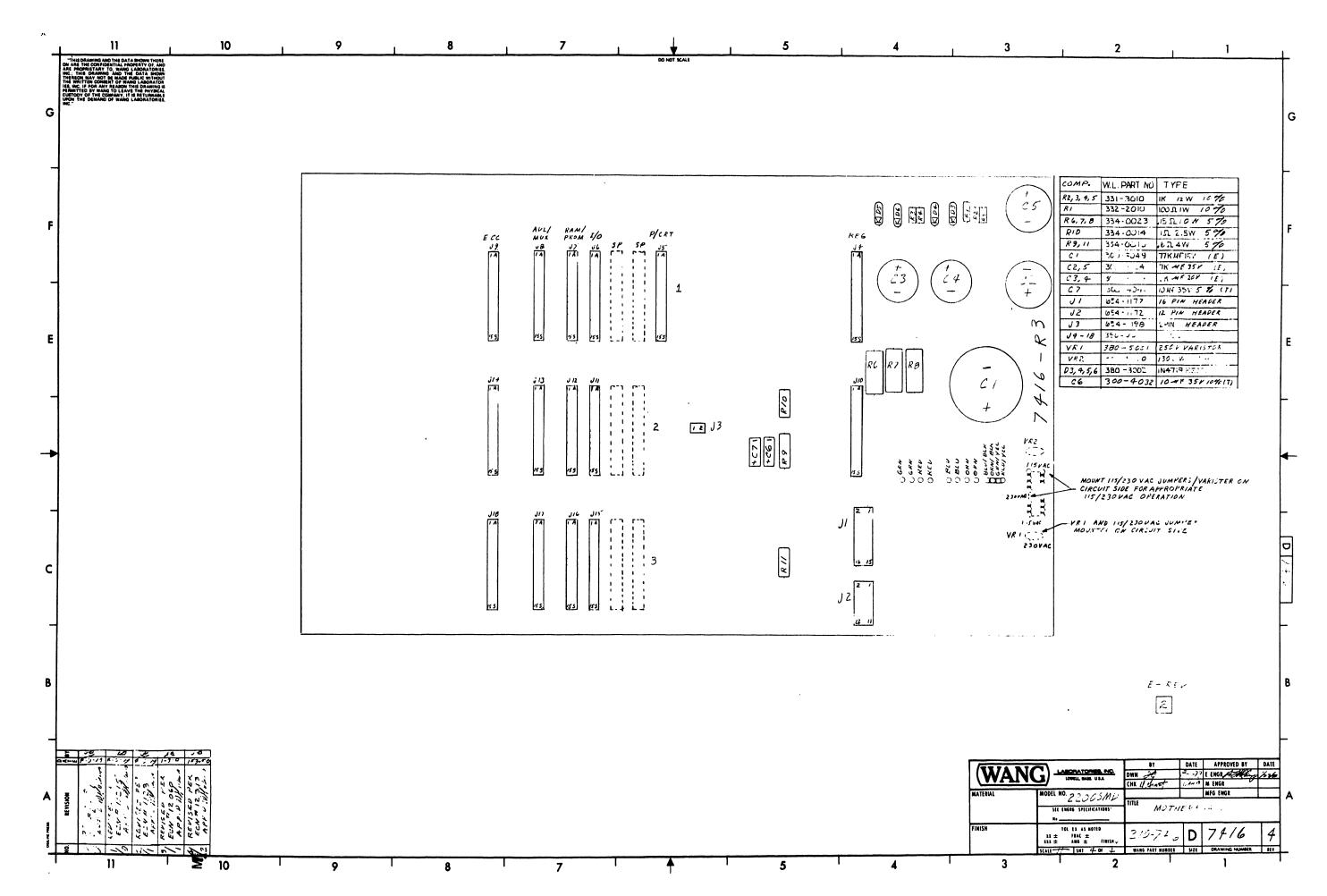
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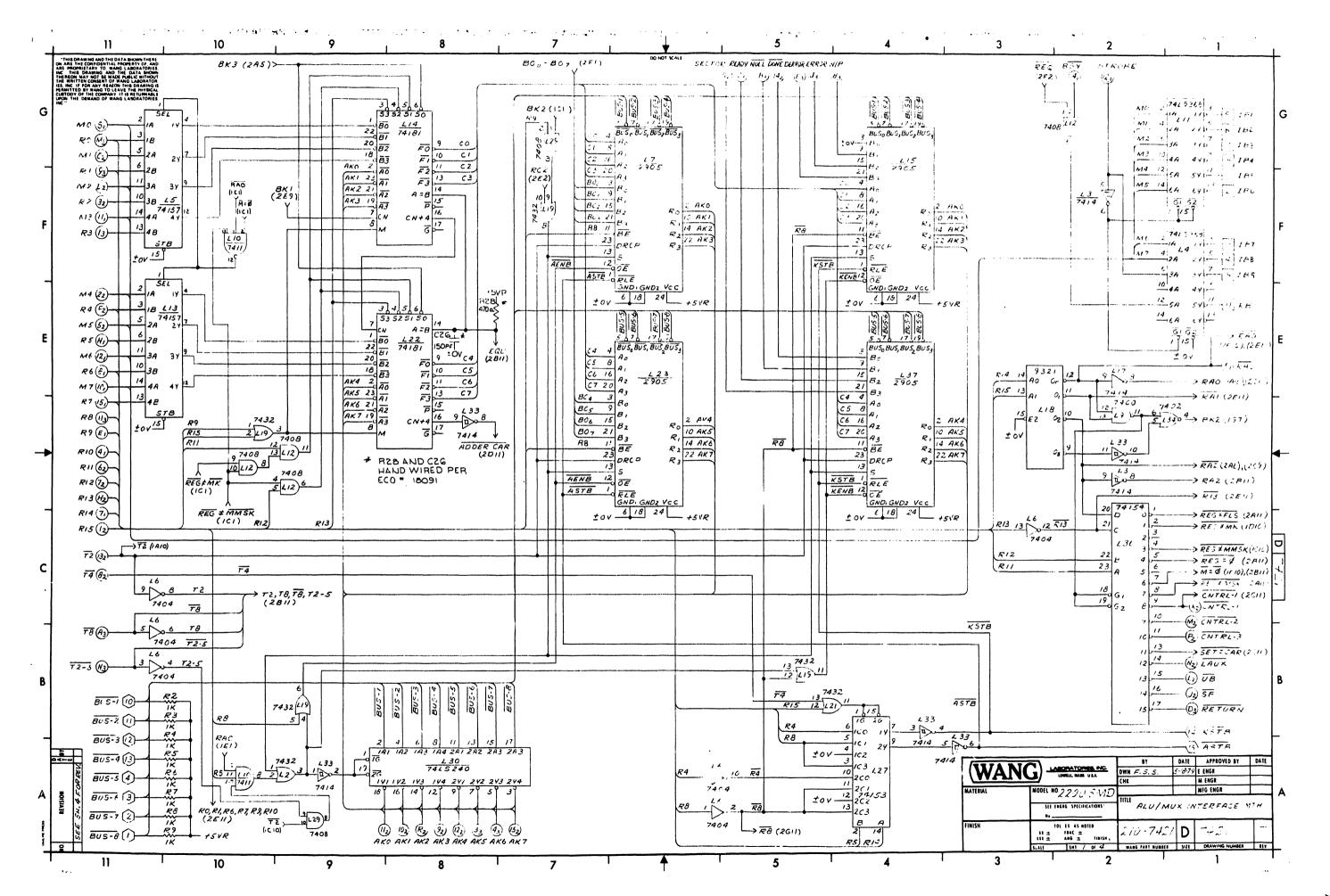
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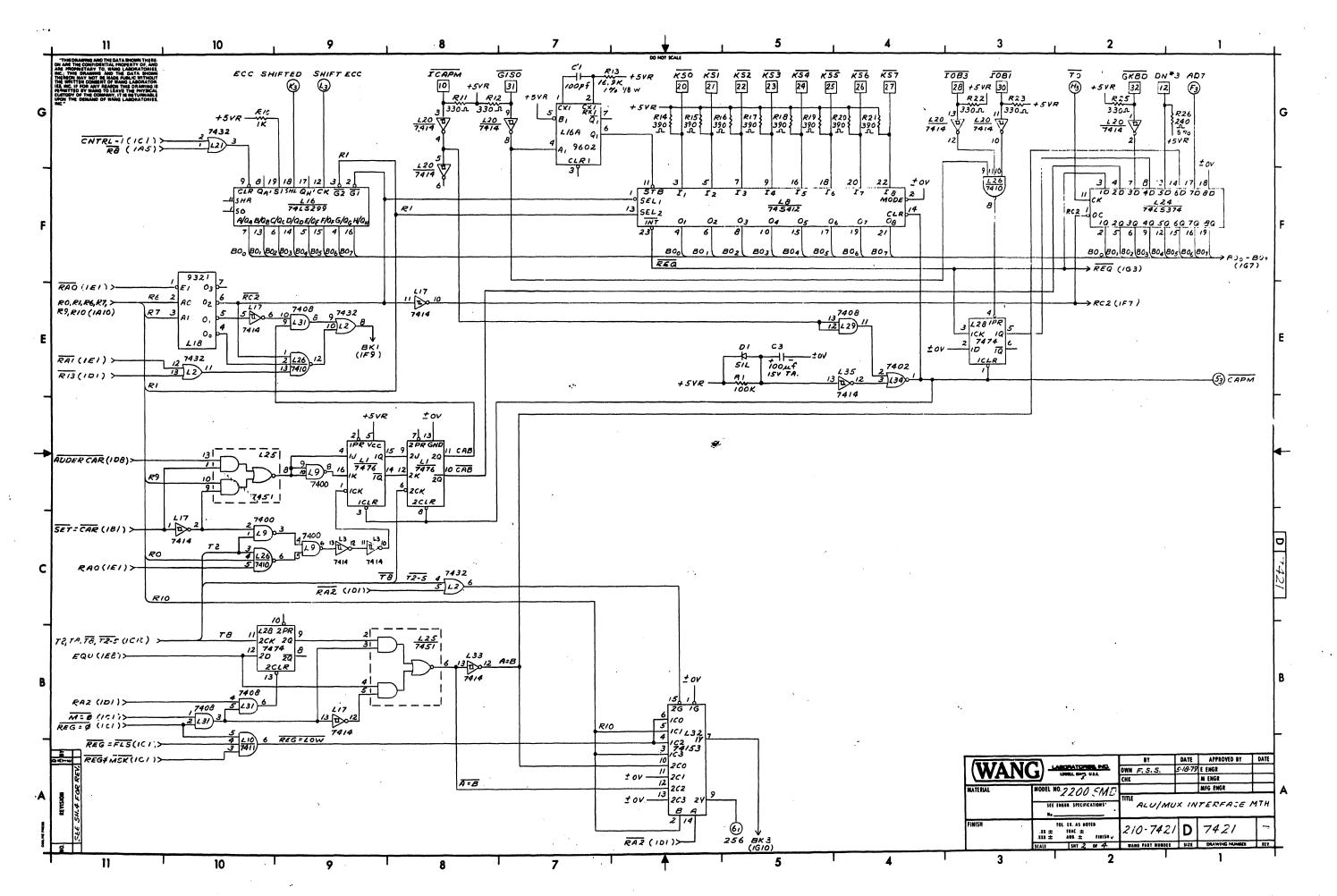
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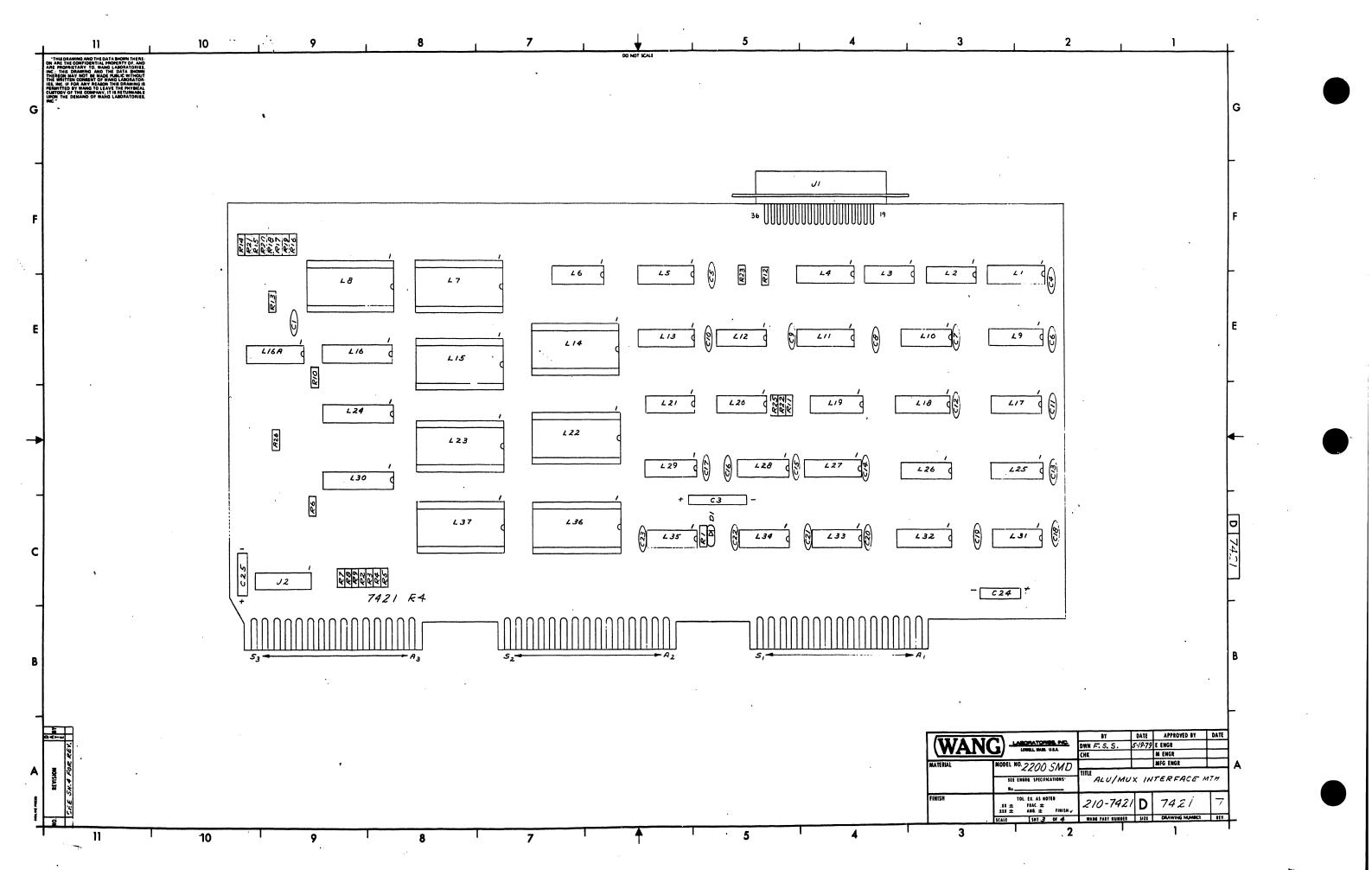
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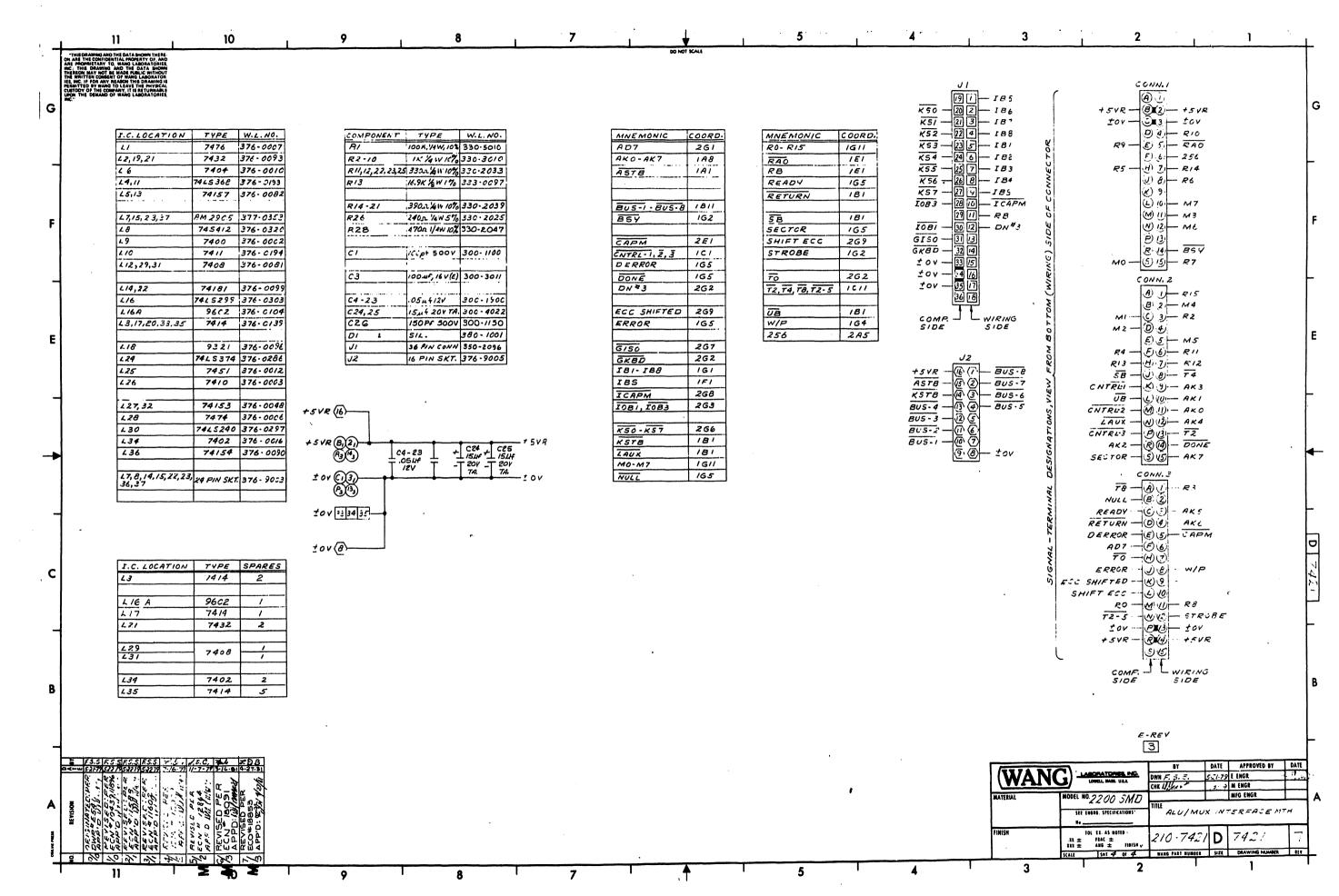


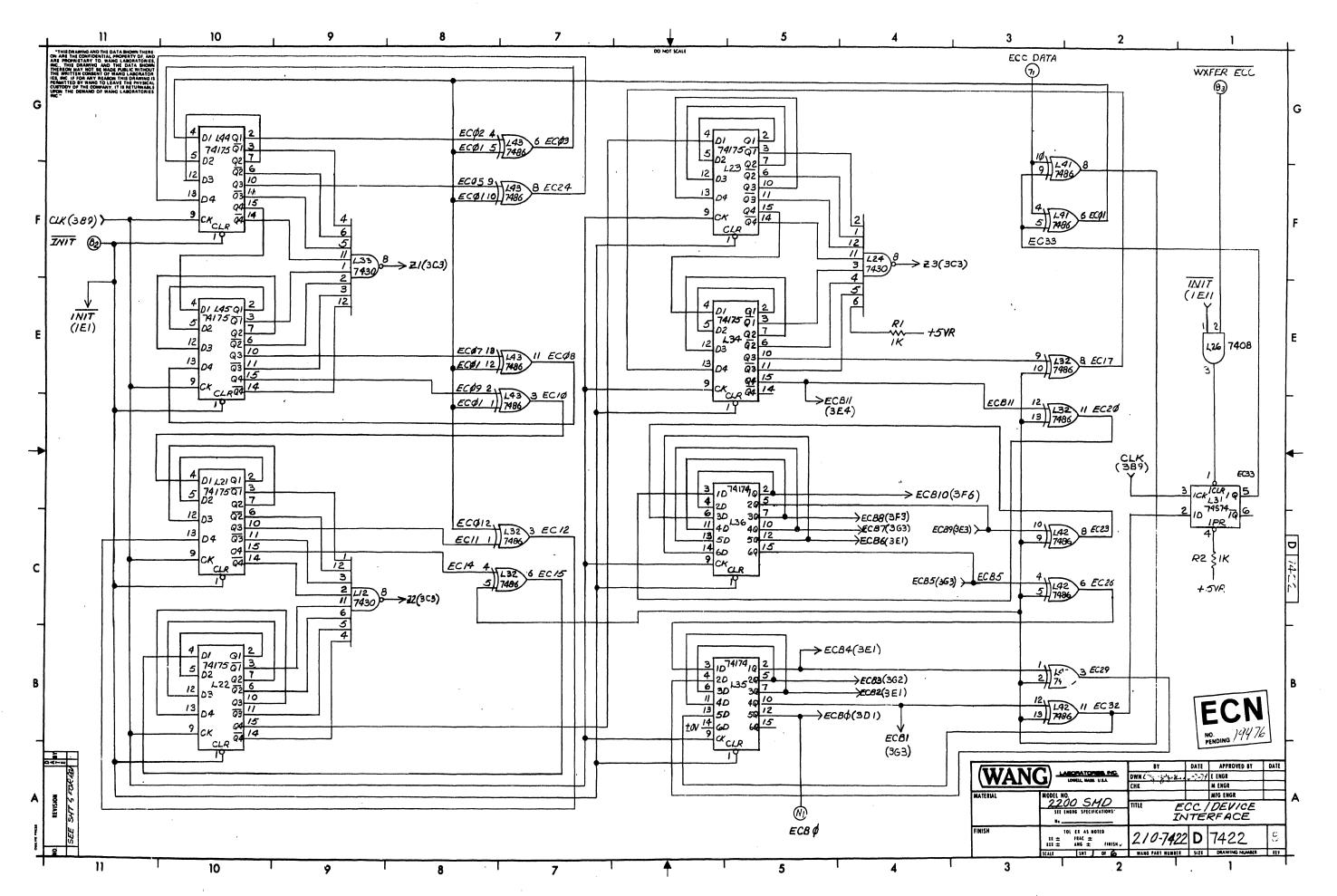


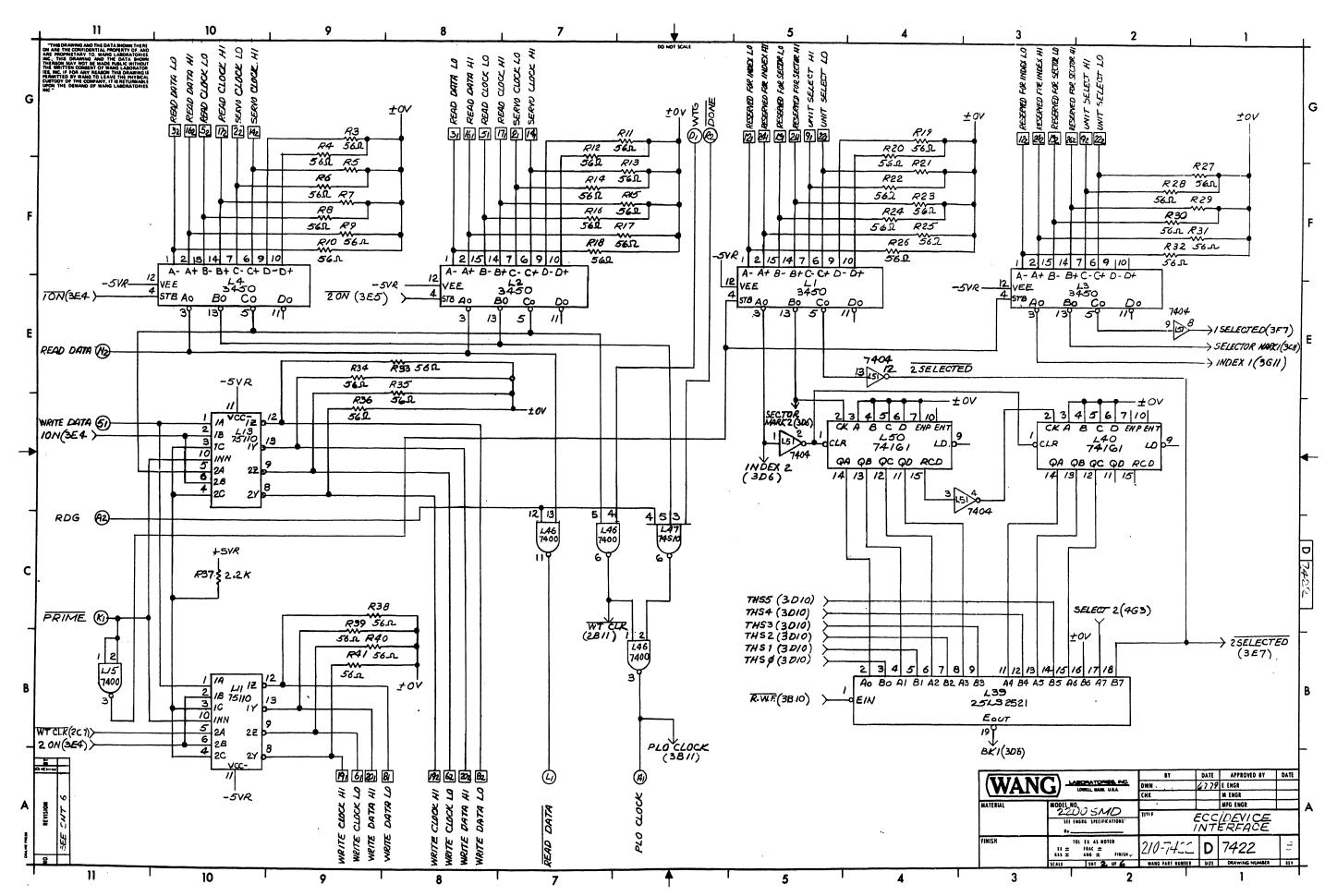


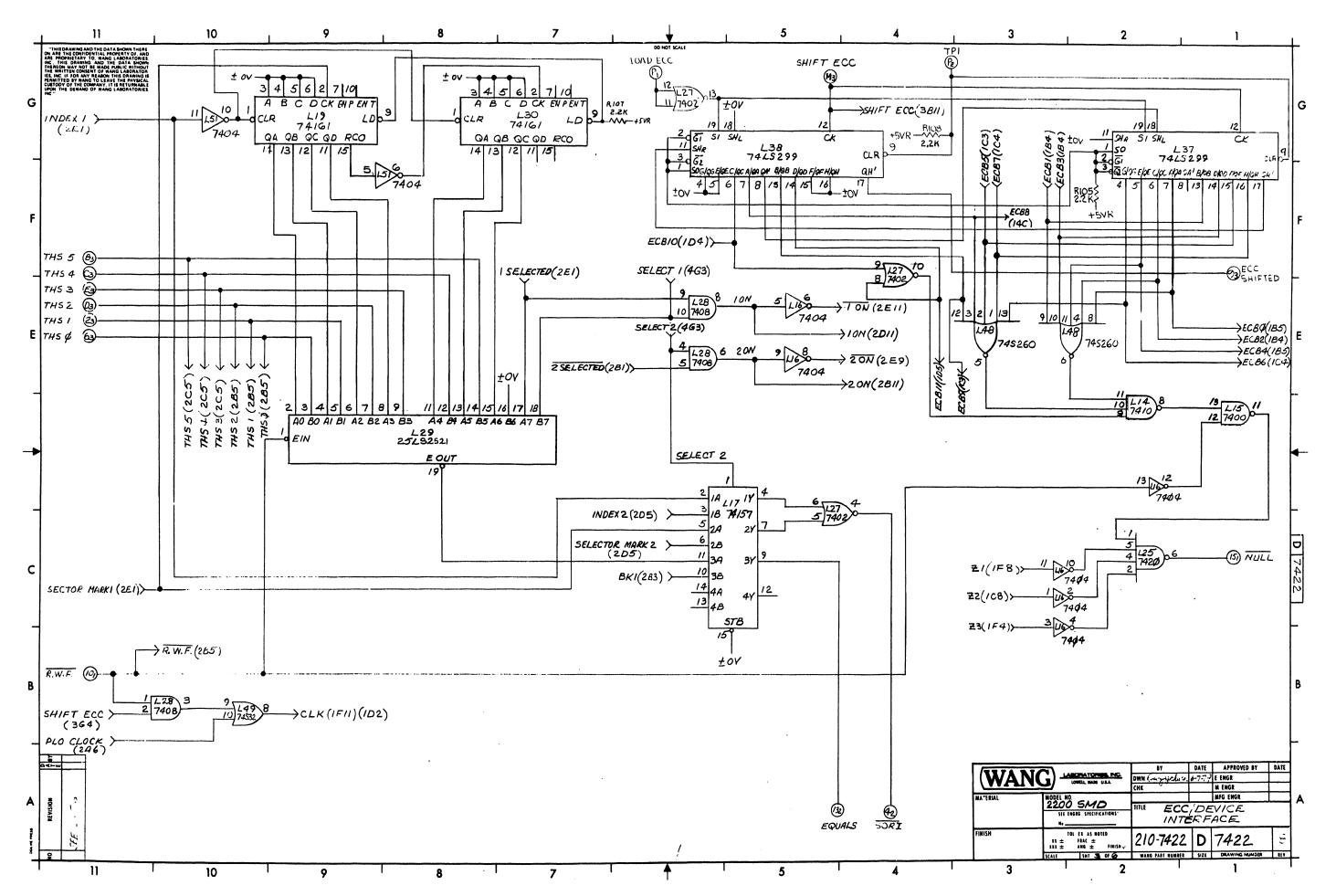


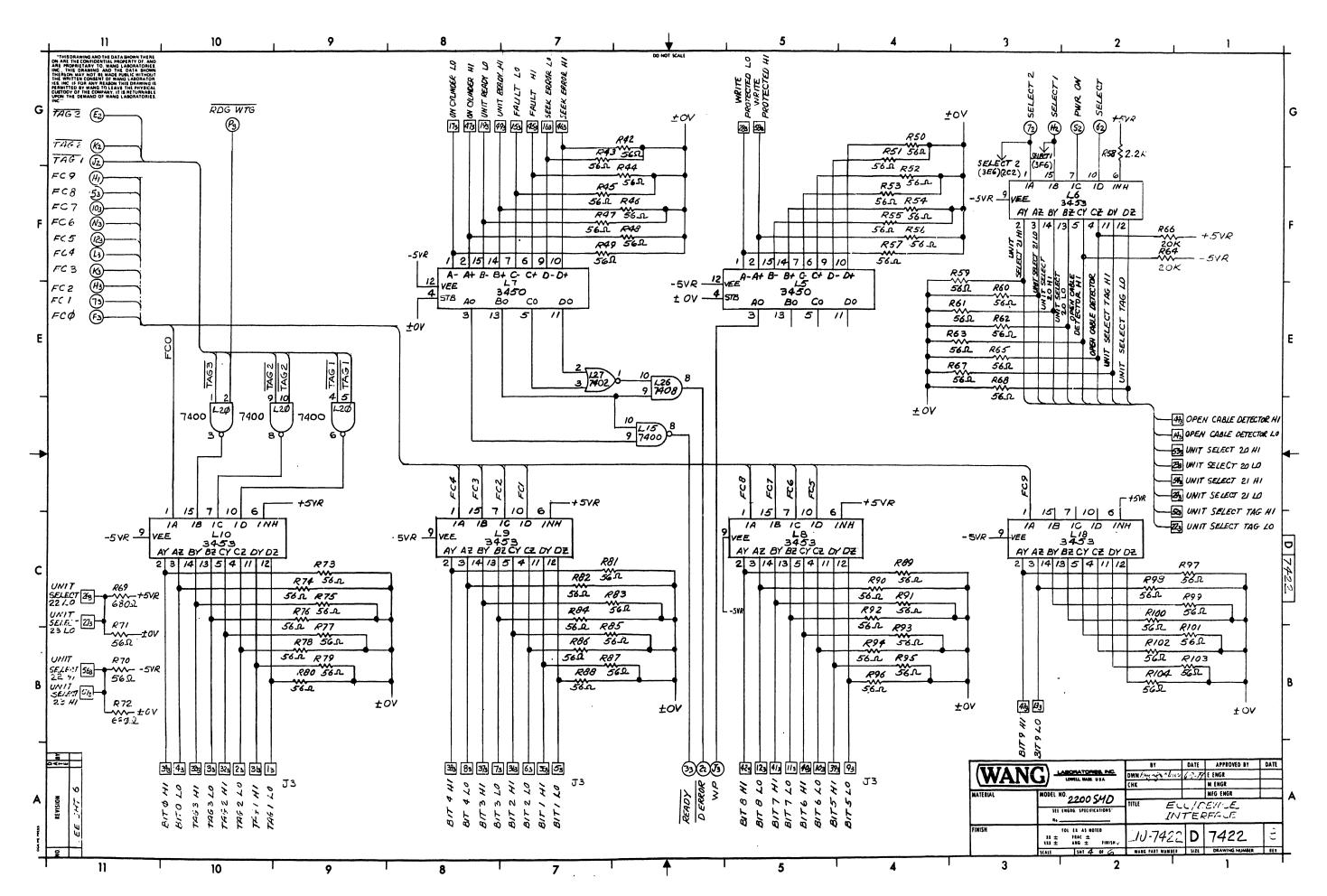
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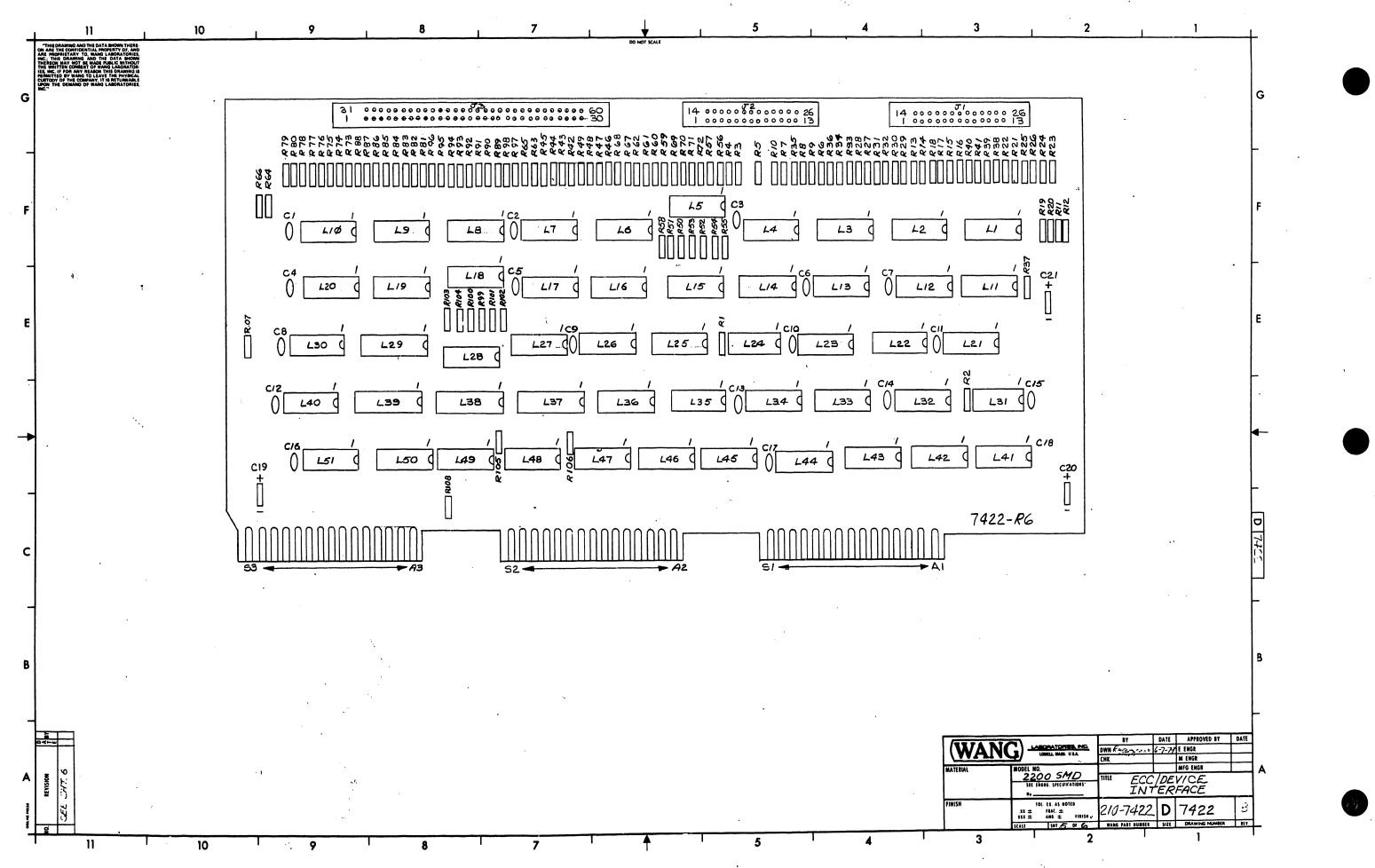


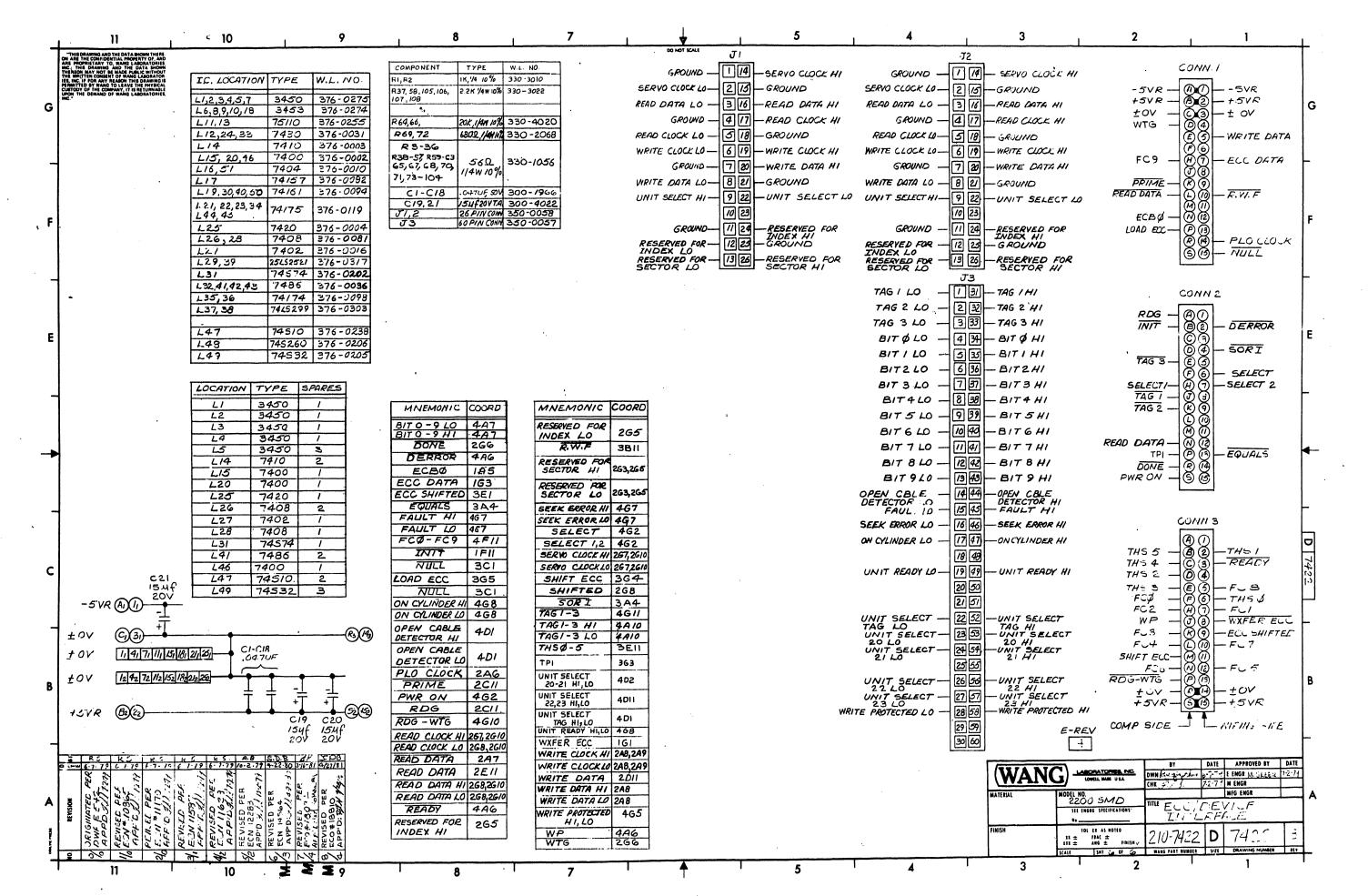


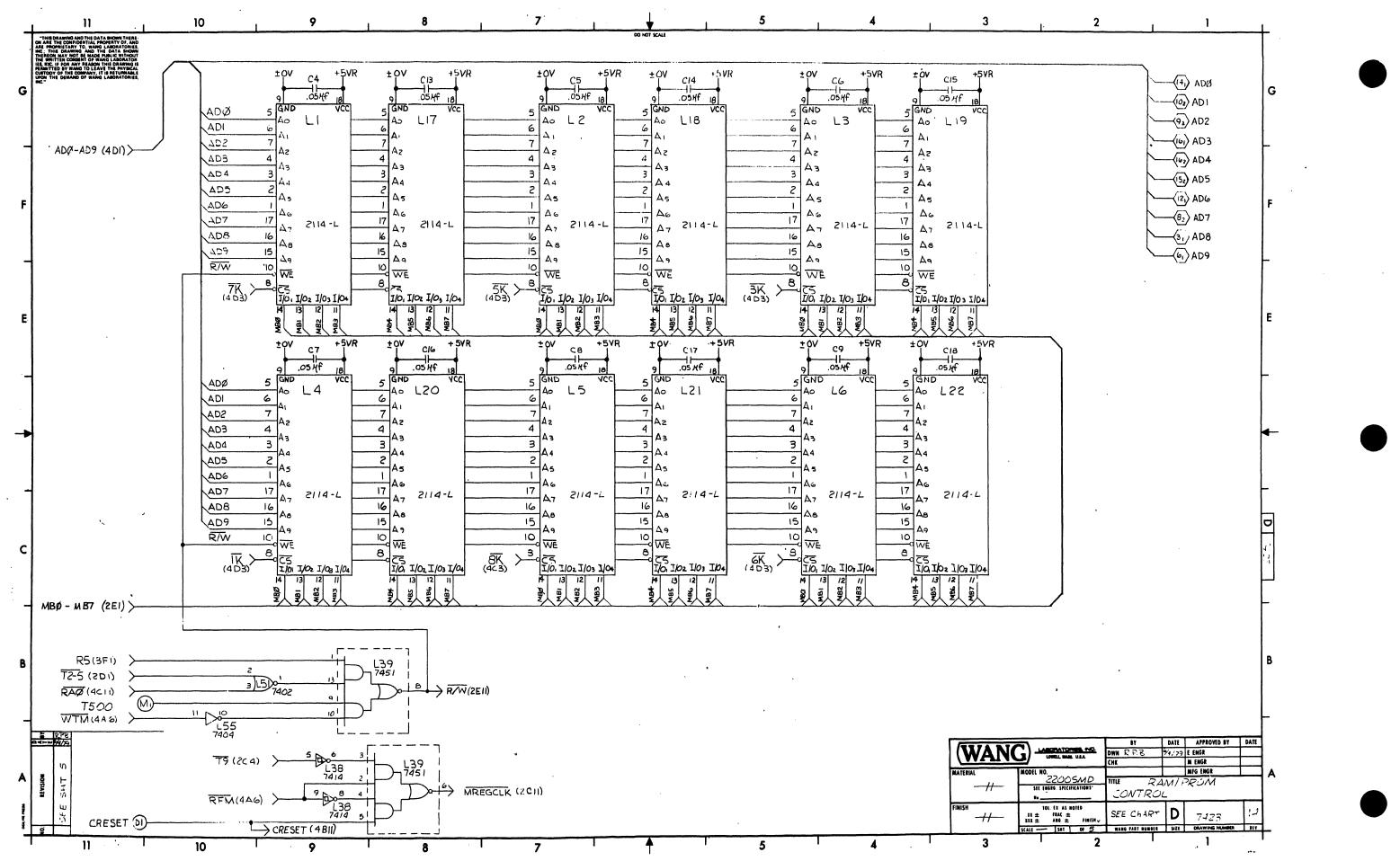


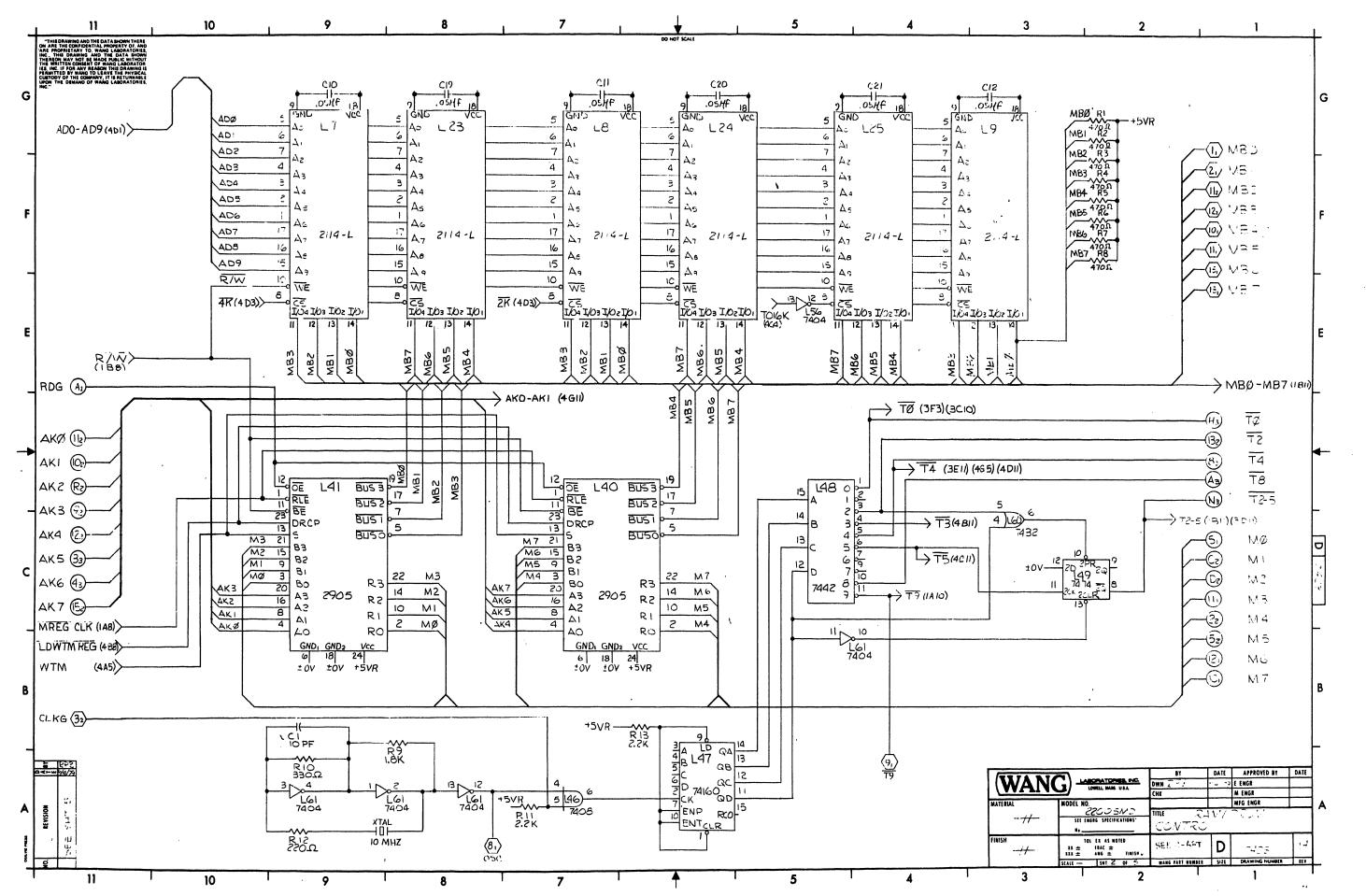


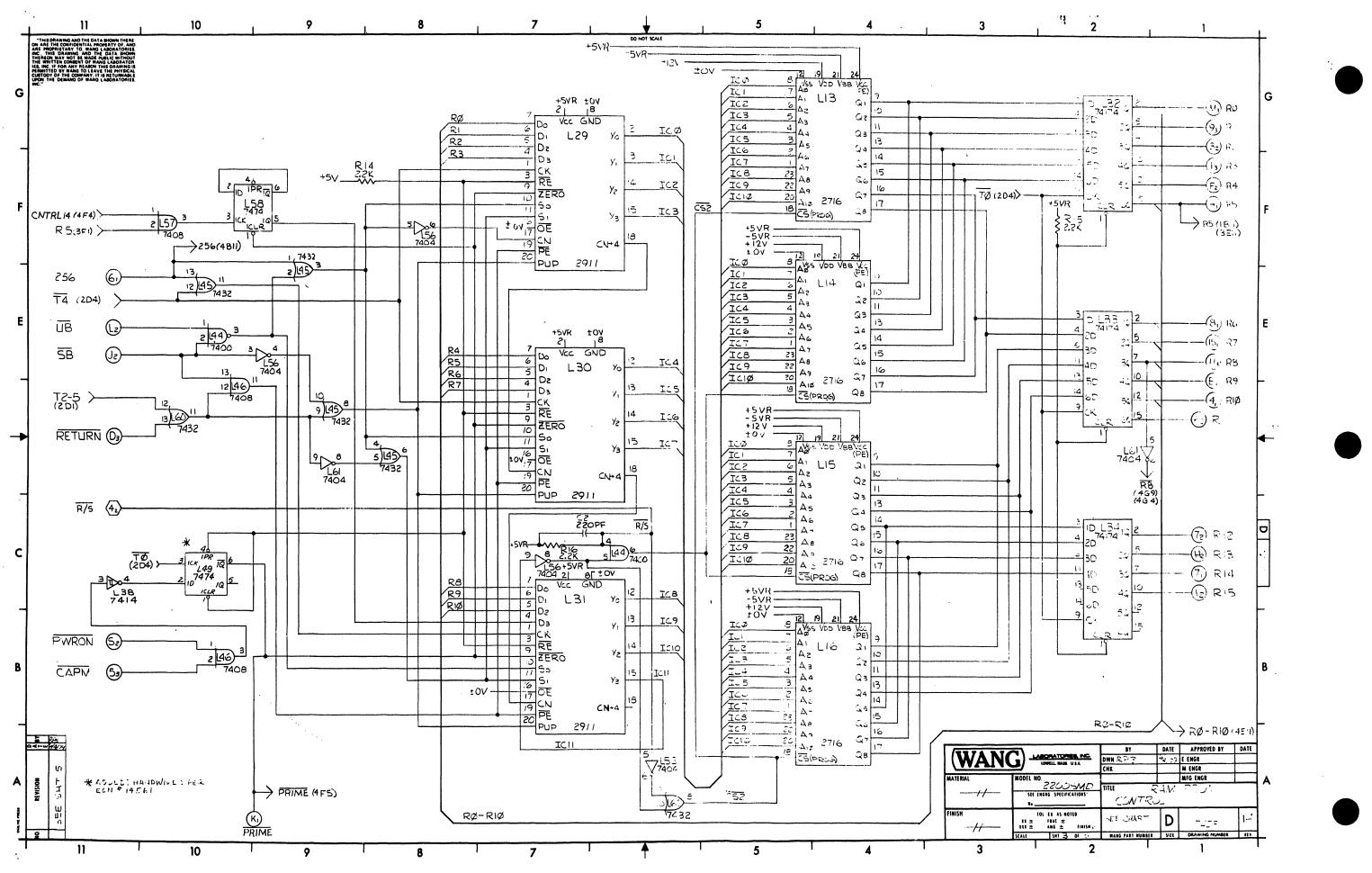


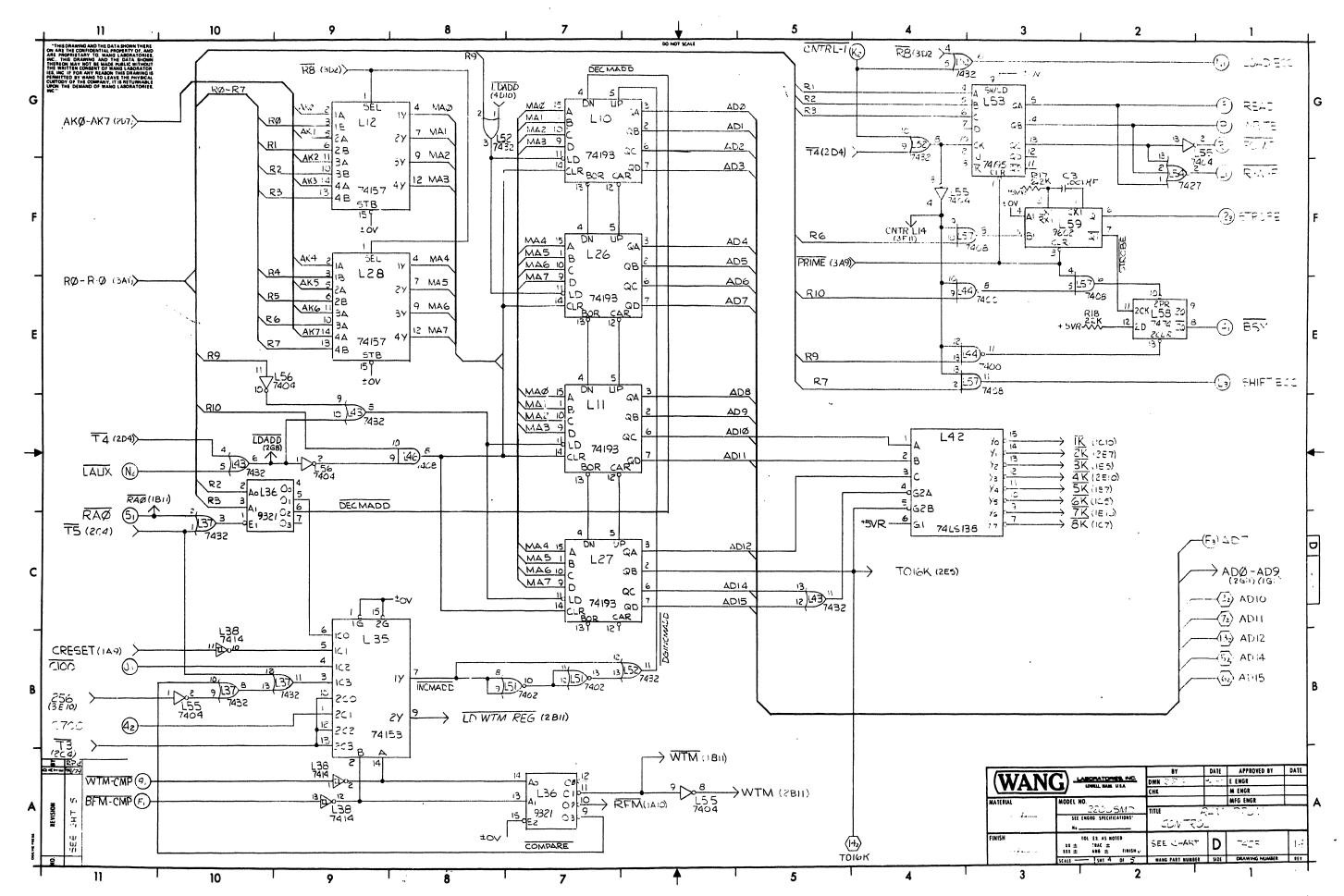


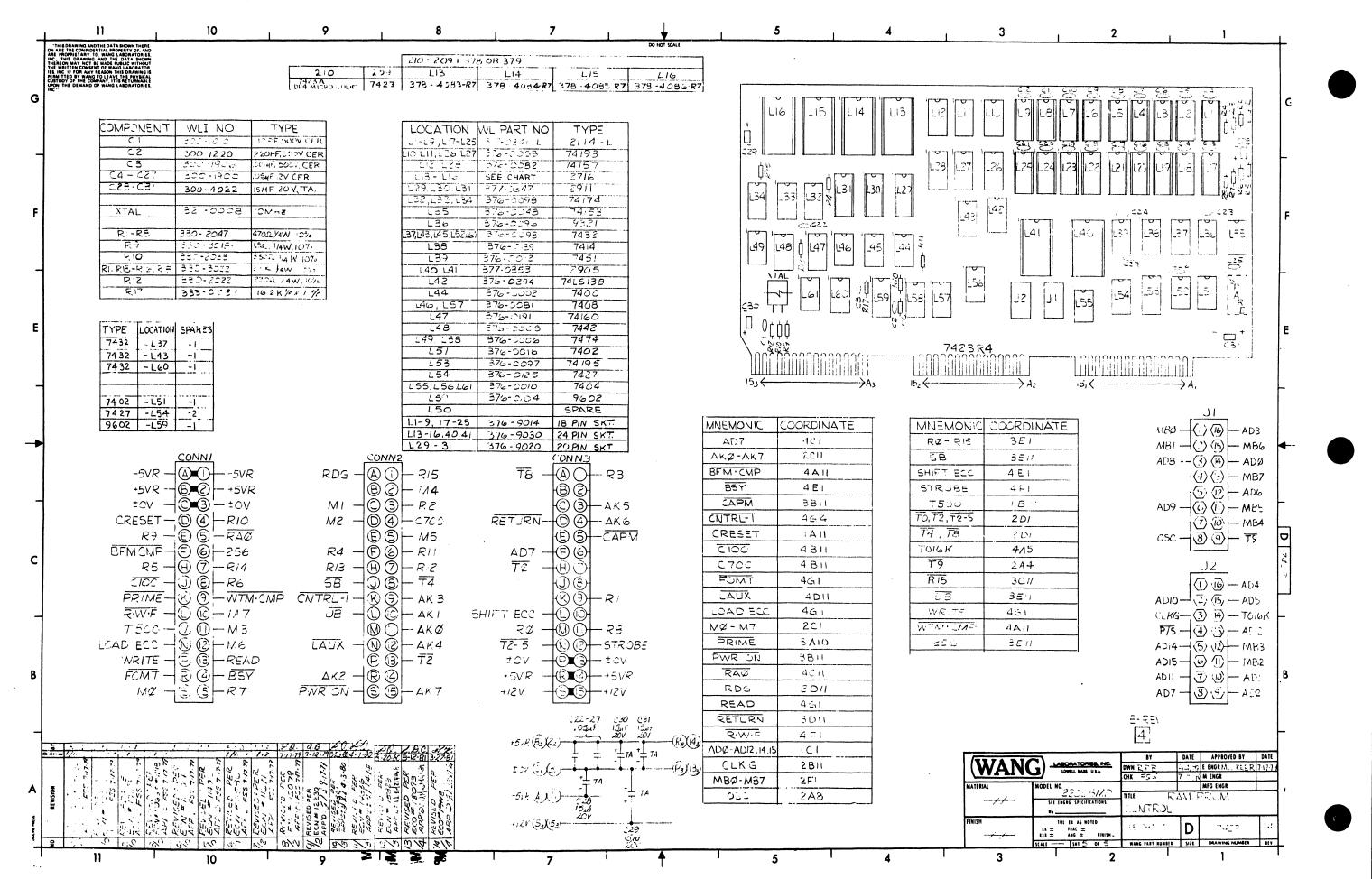


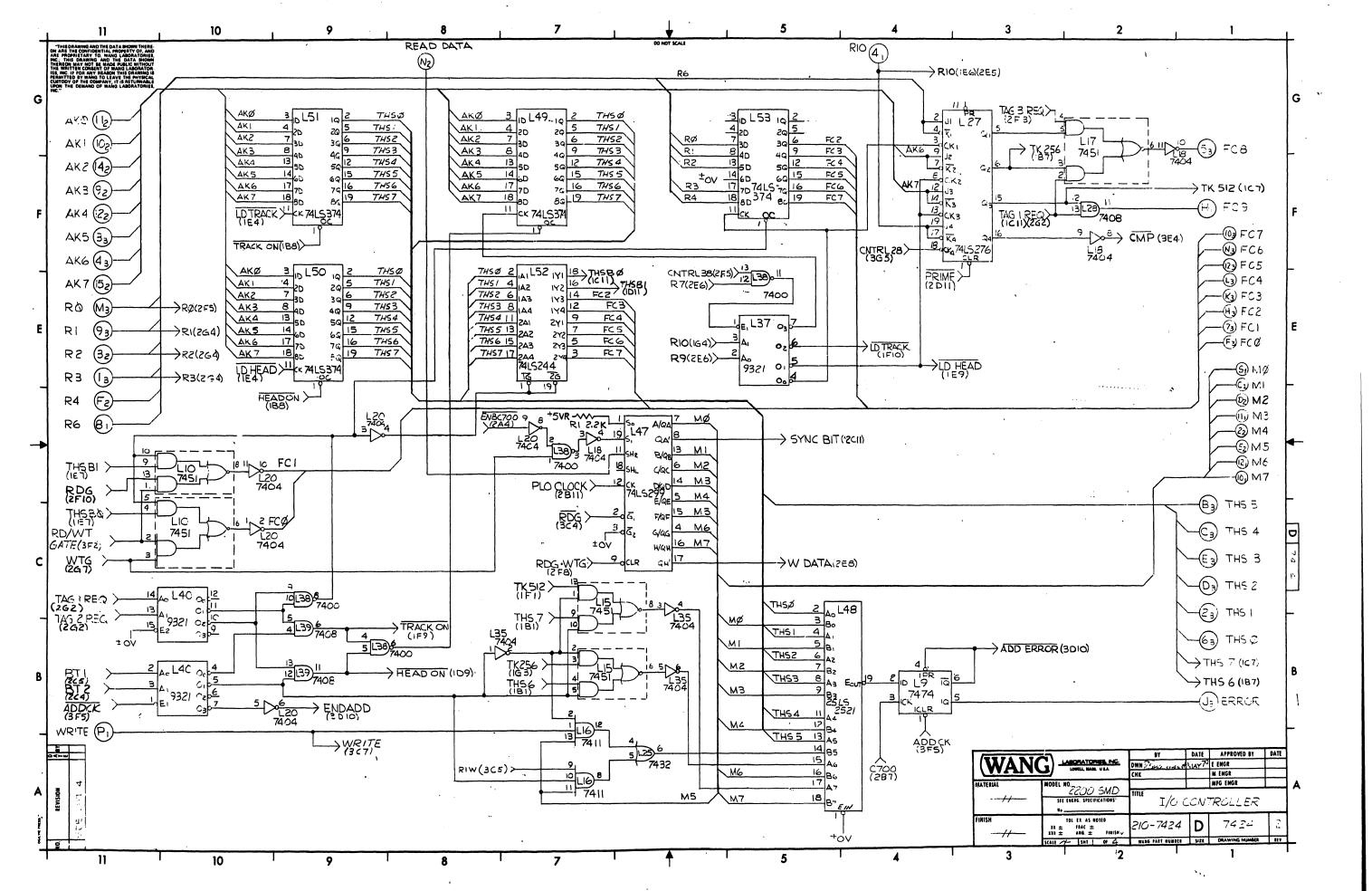


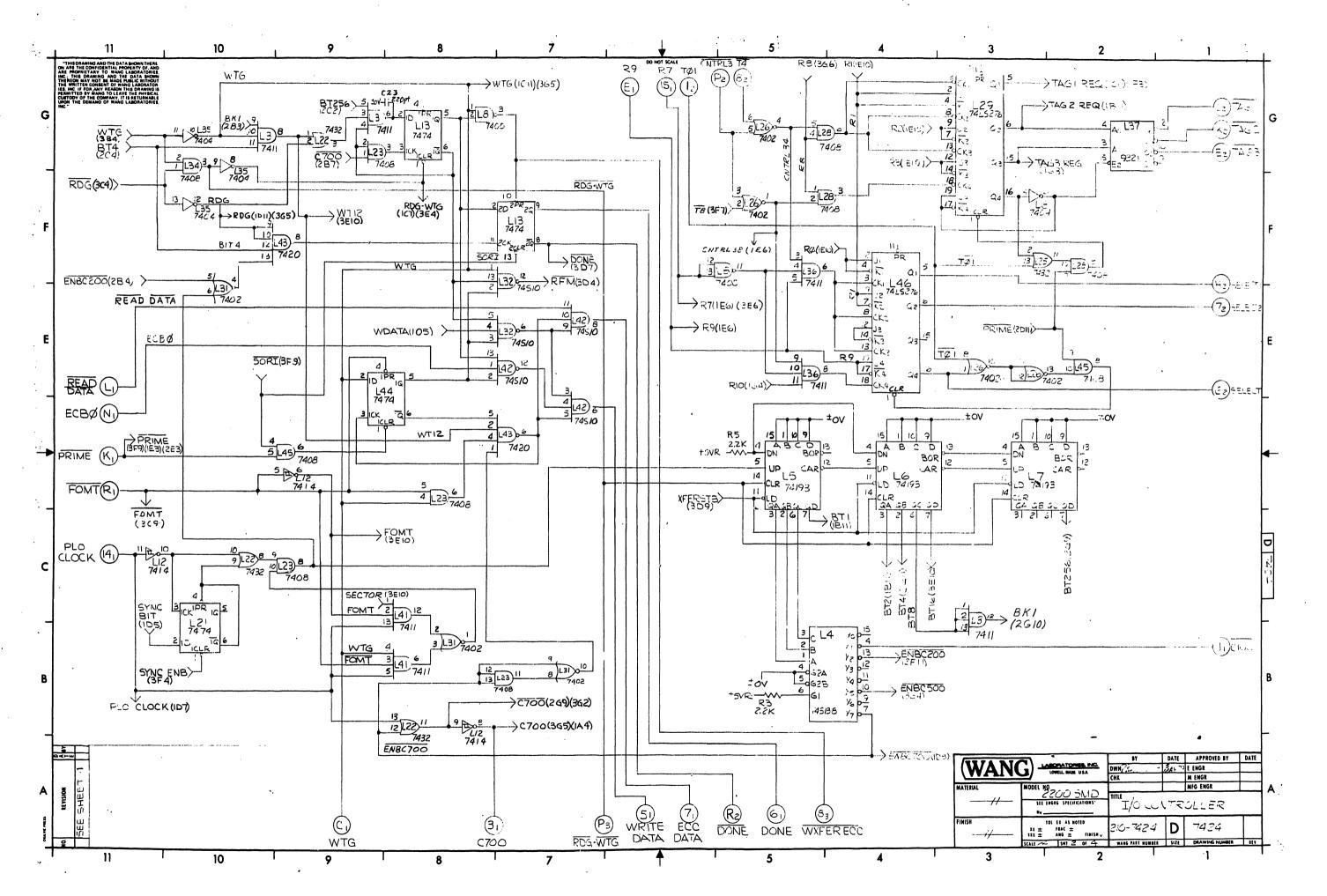


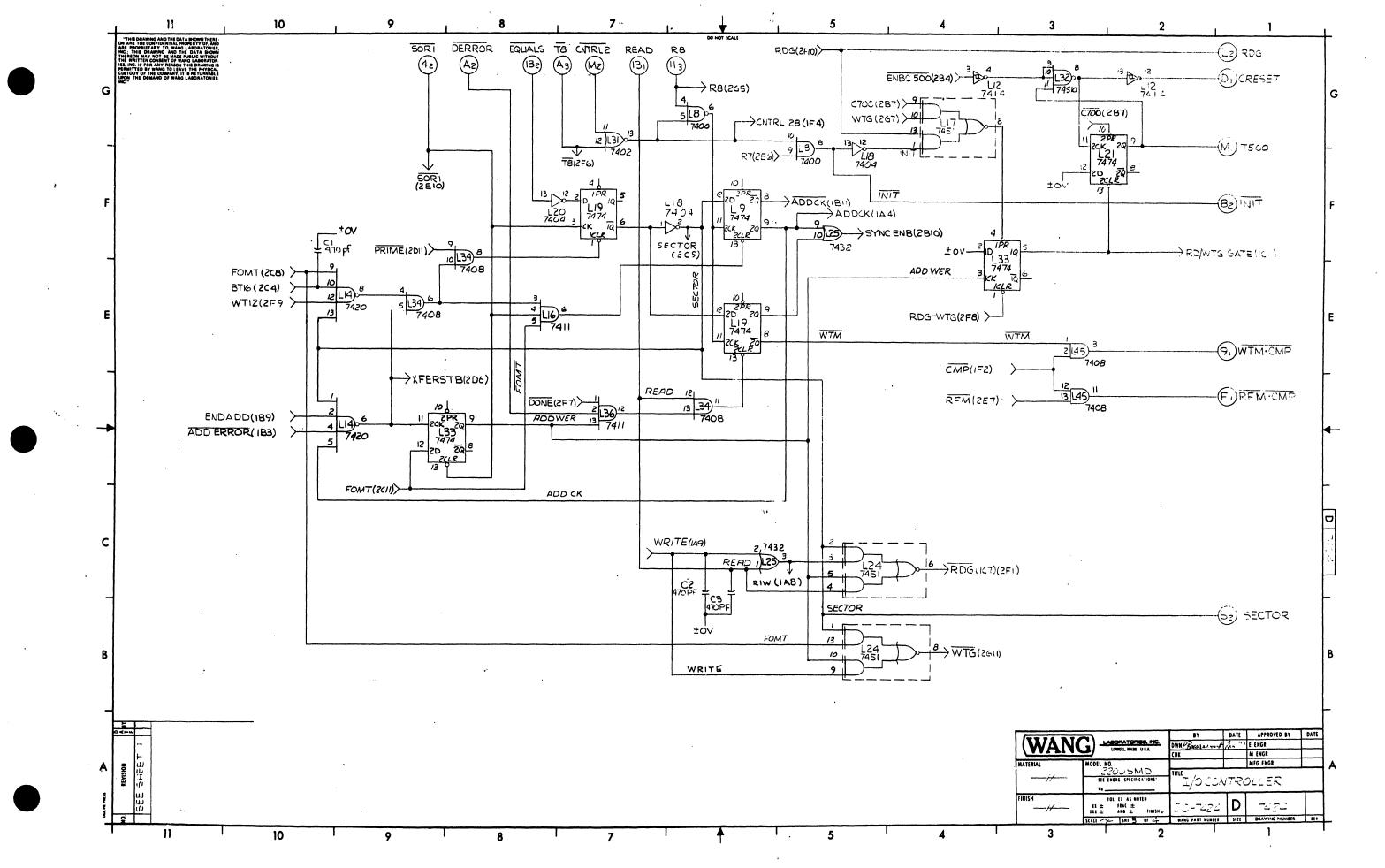


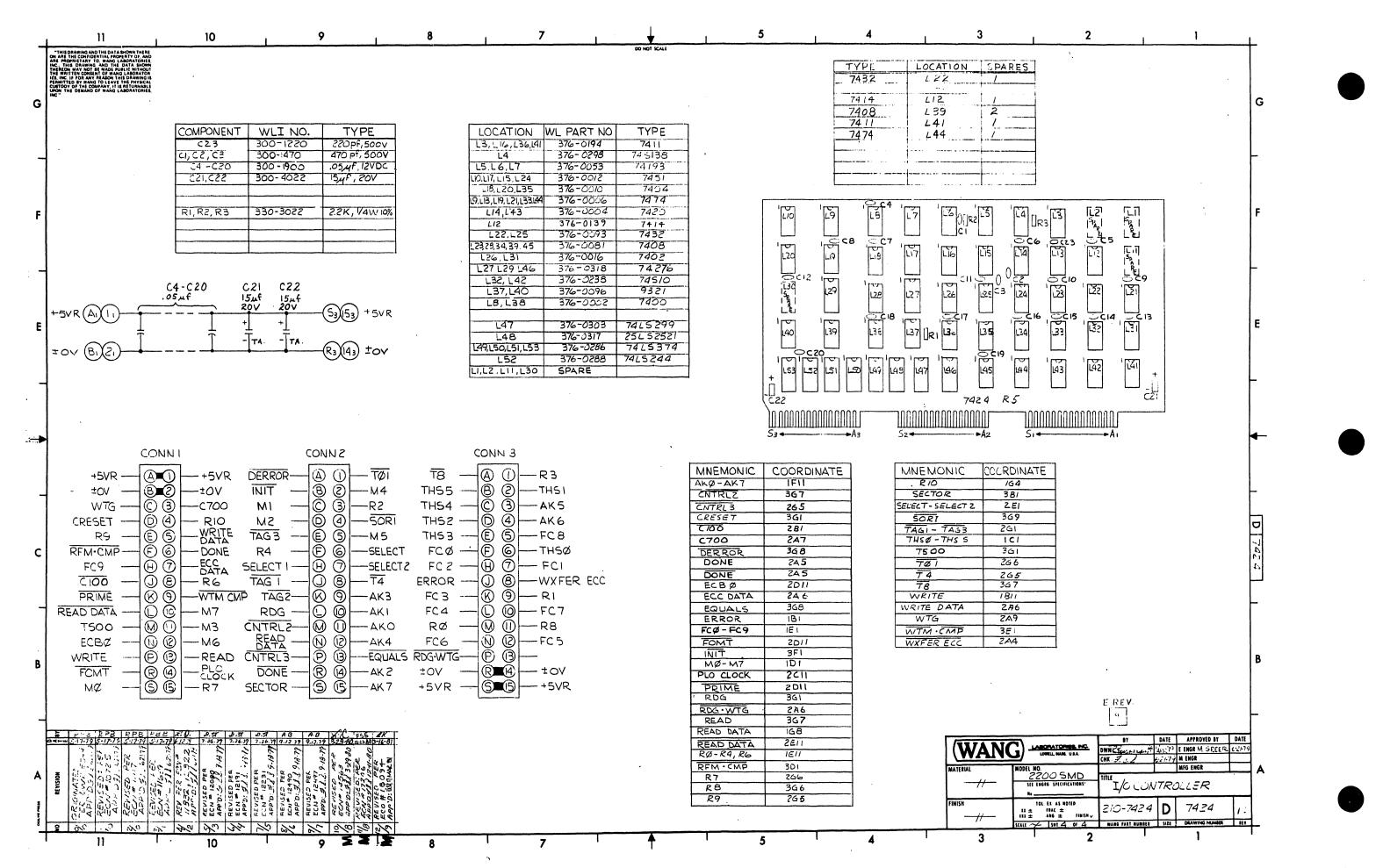


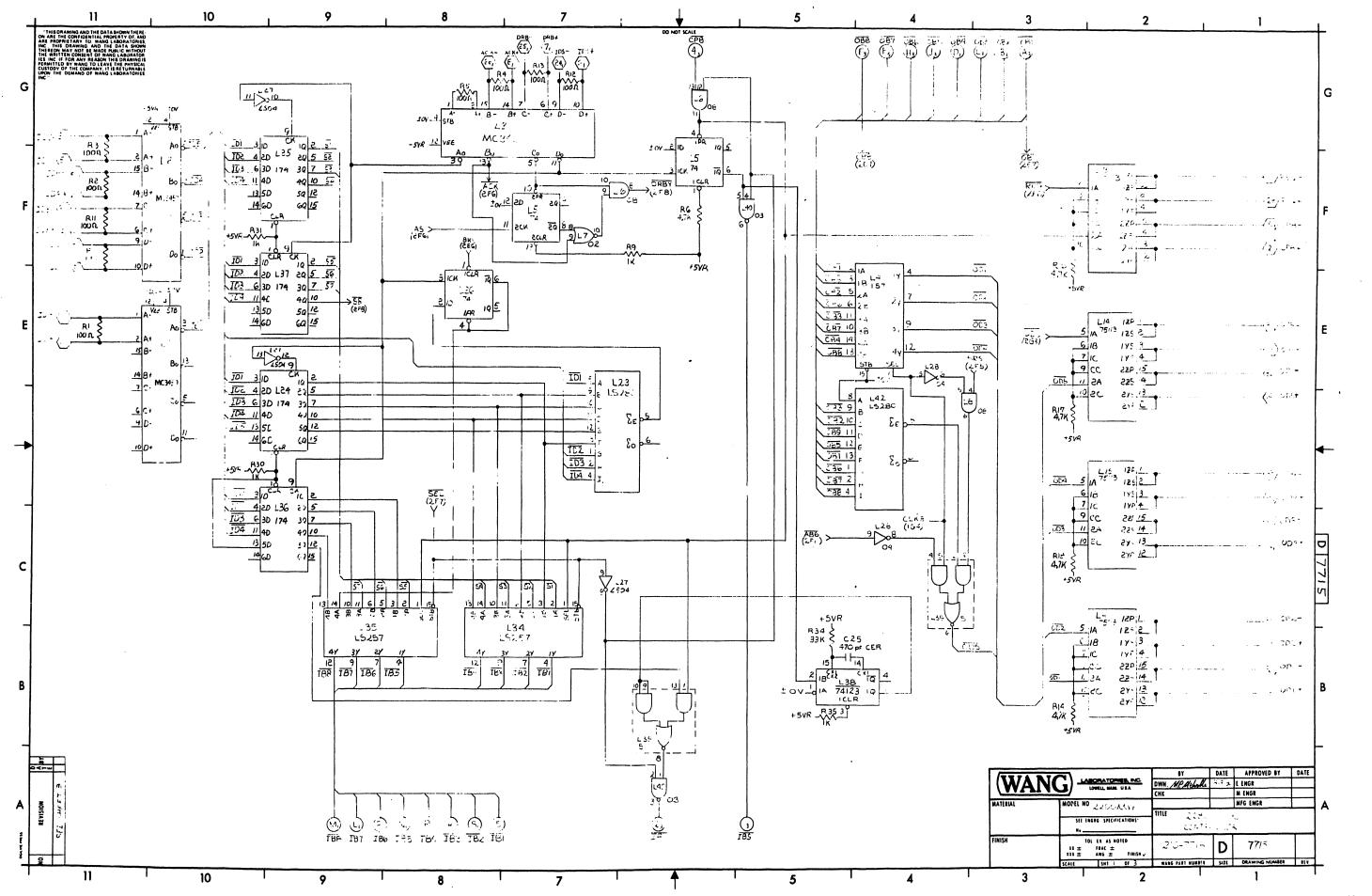


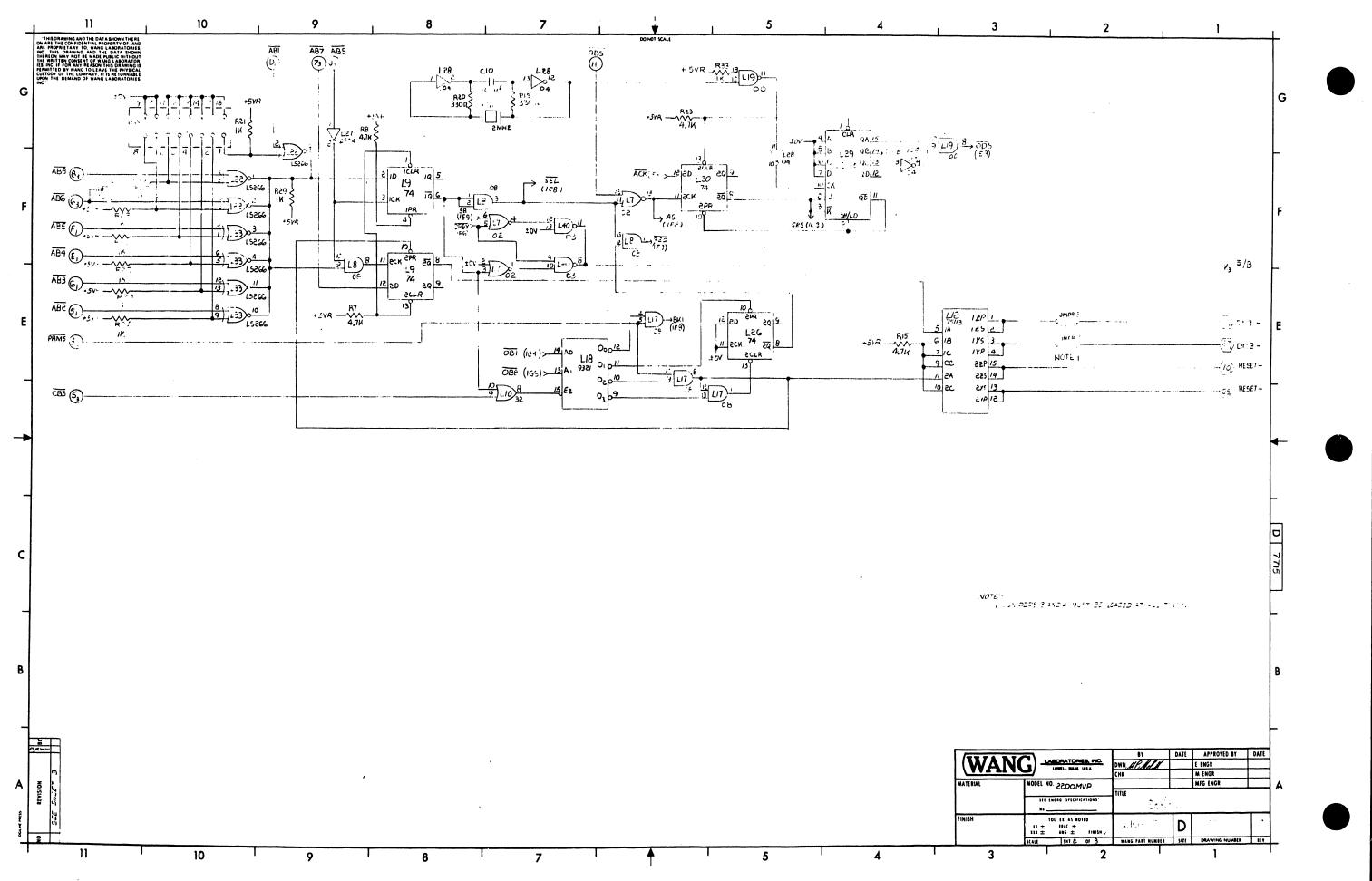


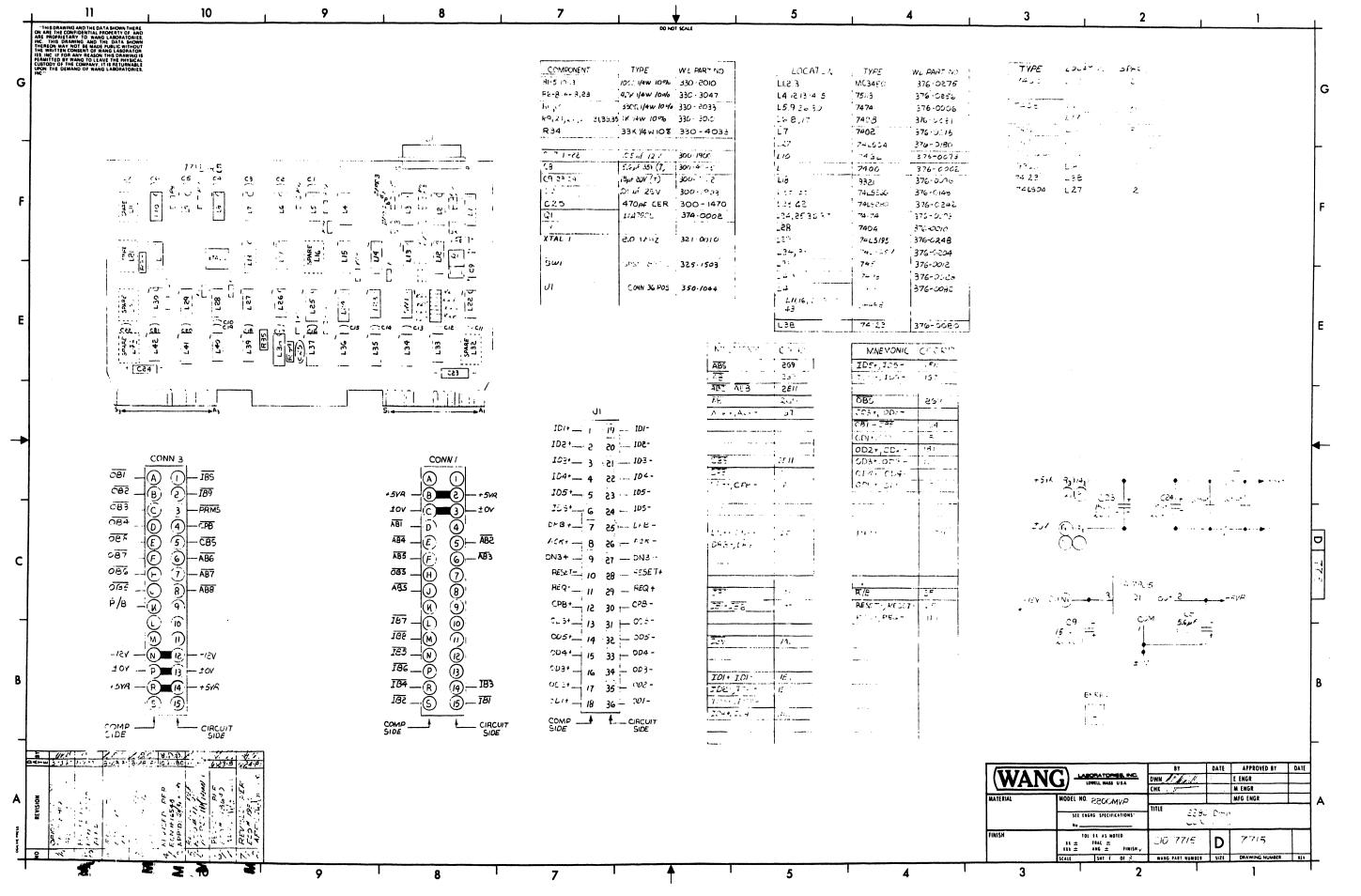












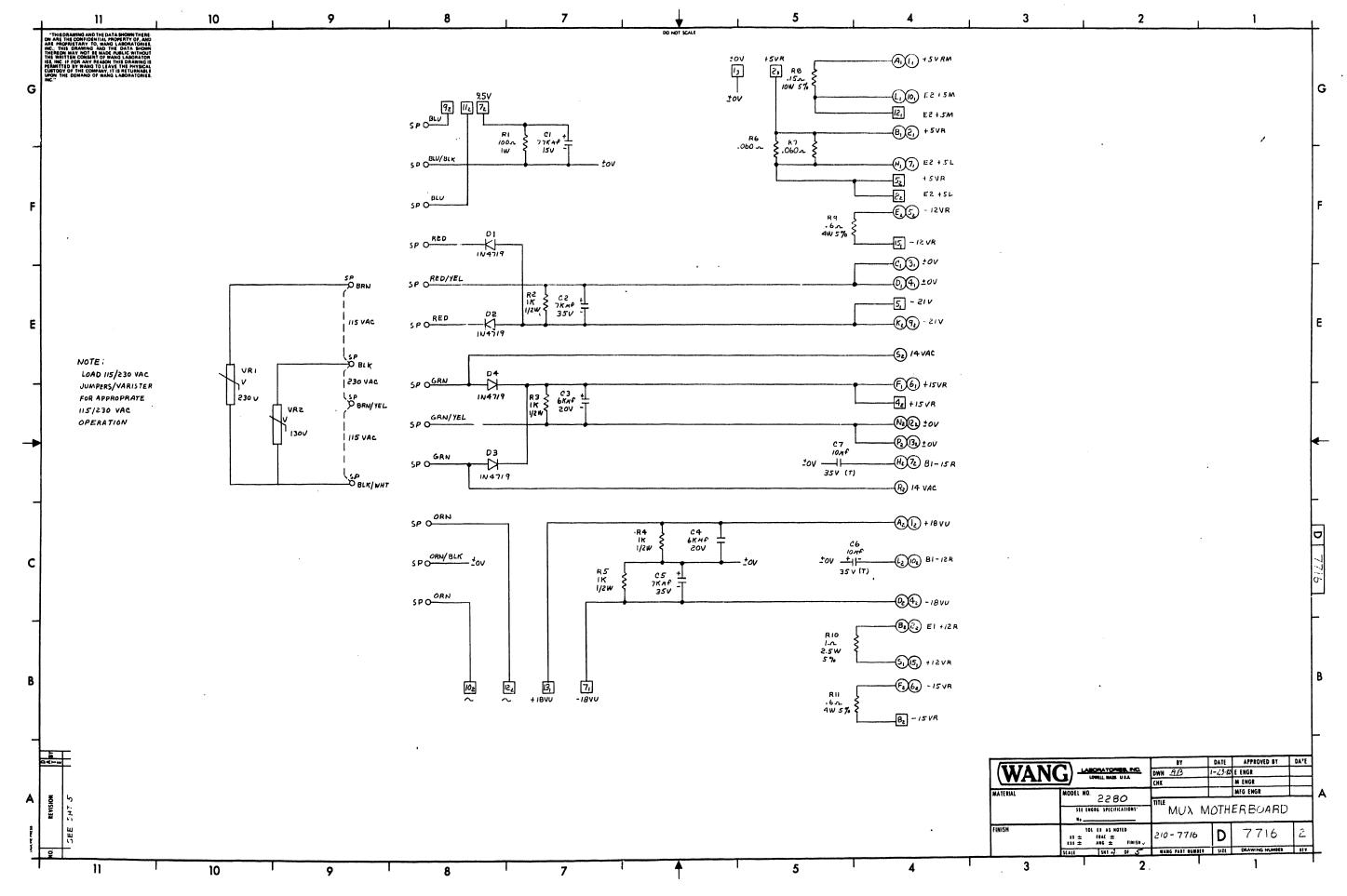
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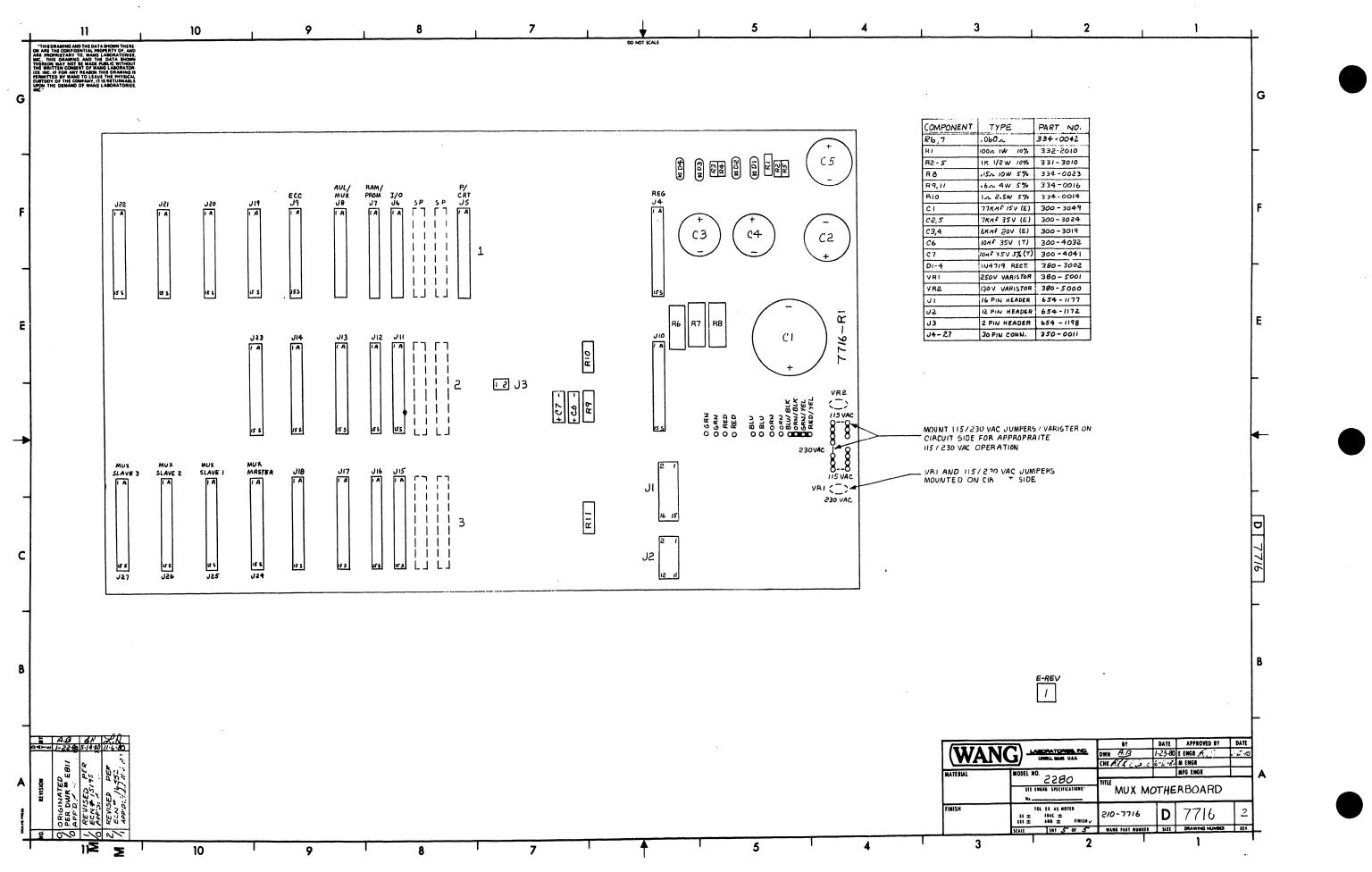
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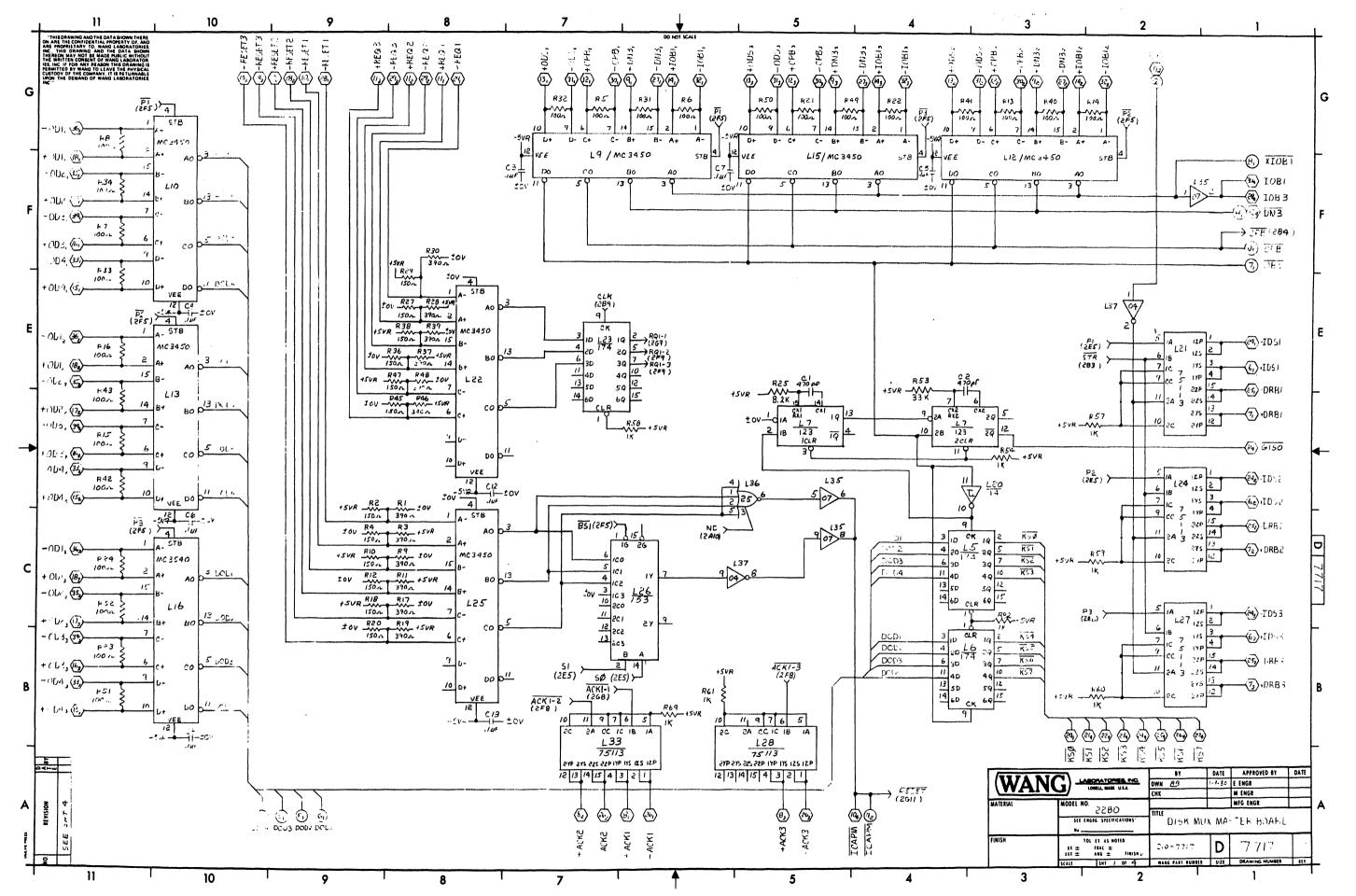
œ G REVISION IN THE AUXIMUS PAMYIRATION ON MEET 1981 1463 1484 1415 MACIER SLAVE EE SHT 5 HEA! ME SPARE AVE LUCE E-SNAL 437 .19.1 SEAD DATA READY <u> و ز</u> - 3 RESET D3 E, Α 35 E, RIM - MP <u>P3</u> RWF L, M₃ 10, M. 20 Mis RI 43 R2 32 R3 ಠ R4 Fz F.2 Fz ō 25 ∄, 86 ٤, **ಆ.** 7 i5. 15, 28 ./3 119 11, R9 Ε, Ł, Ė, RIC 4, 4 4. ₹ ; 62 ÷ε F.IZ 12 12 R13 Hz HZ **KI4** 7, 7, RI5 12 RQ2-1 43 RQZ-2 3, **RQ2-3** 8, 13 RQ - 4 2, J, RQ5-1 73 8, RQ3-2 K3 RQ3-3 5, 8, RQ3-4 RQ4-1 6, J3 93 RQ4-2 103 K3 SQ4-3 9, 23 RQ4-4 113 ول SB ٠)٤ SECTOR 5, SELECT SELECT I Hz Hz SELECT & 72 76 SHIFT ECC Mo Ŀ3 SORI 42 4: P. SØ 102 51 R. <u>L2</u>. TROBE 123 123 Нз A3 /32 132 T2-5 Na Nz T4 T8 TAG-1 8, 8, A, A3 Aз Jz Kz TAG-3 E2 Ē٤ THS Ø 63 63 THS 1 2, رخ D, THS 2 Dj Ε, THS 3 Es C 3 Ċ, TH54 THS 5 8, ÜΒ WP 8, . وز. The detailed and the properties of the control of t WRITE DATA 5, <u>5,</u> NTG D, CI WTM-CMP 9, 4, Pı WRITE **F**₁ WXFER ECC 8, ų, WTR 112 256 6, T500 Μ, M, A. Δ, -5UR C, 3. Pi ± CV c. Ci C, 8, WITHW W 3. P3 3, 3, 13. Rs 143 R3 Р, 133 В, s, Bı g, SBOLEDICIDIAL SESES SIS s, R, 143 ر4 ب 152 153 280 ۶, 15, + IZVR F, 210-7716 MUX MOTHER BOARD B1- -5 R 32-15R -21V O -*i*8vu B1+12K 916 E1+12 R +18 10 B2-12R B1-12R E1+8R 9/6/ Q æ ➤ Q

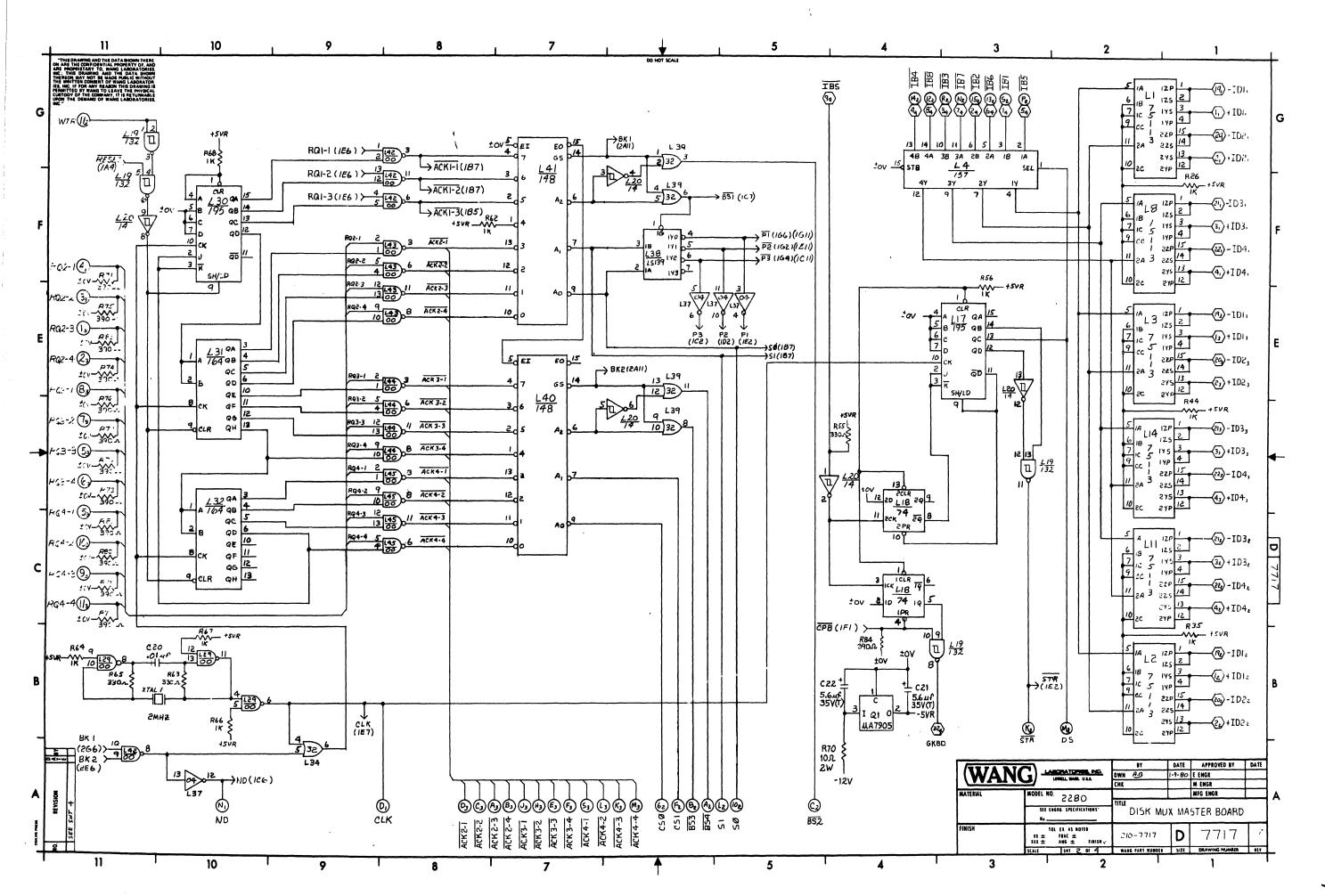
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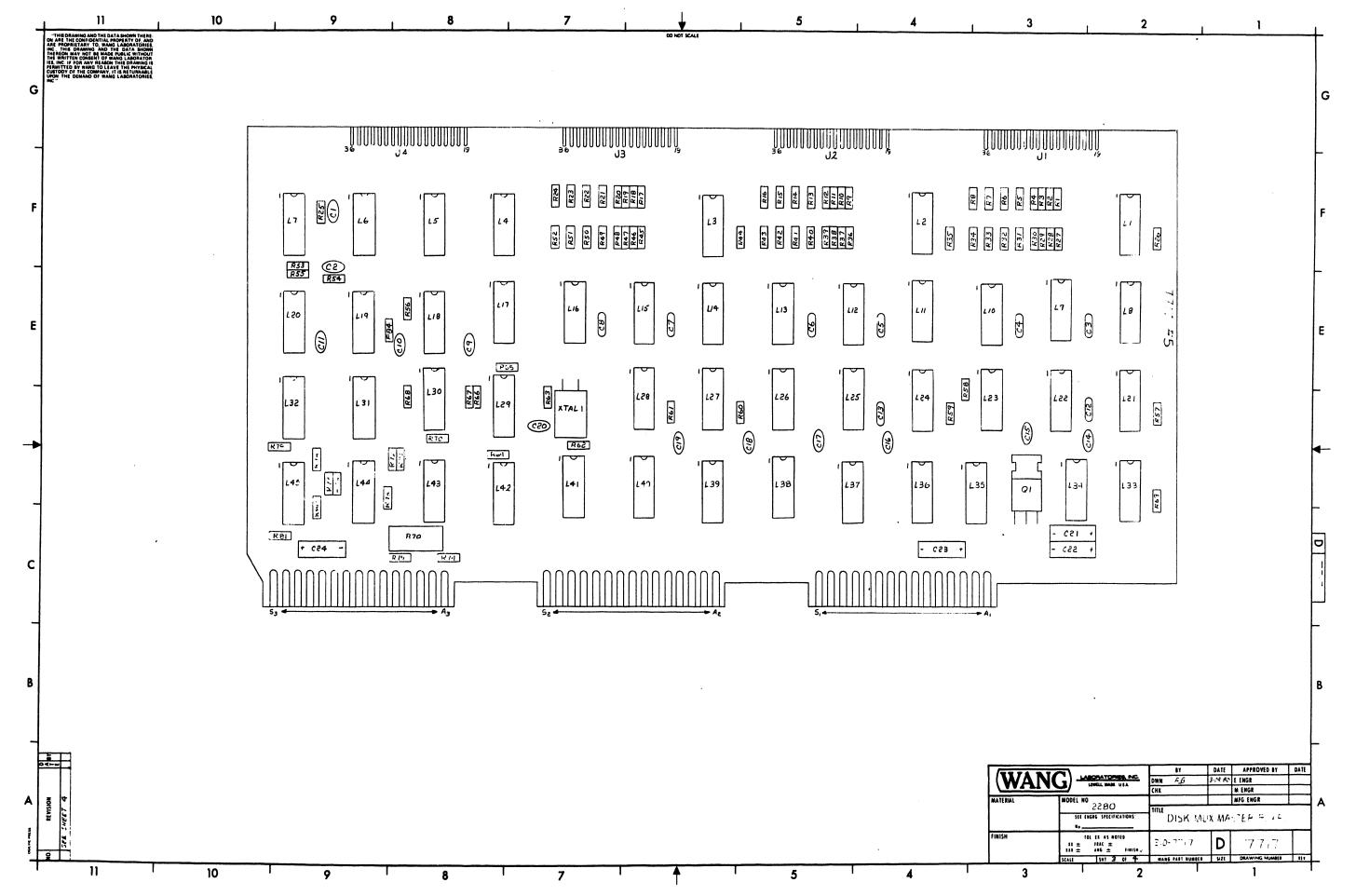
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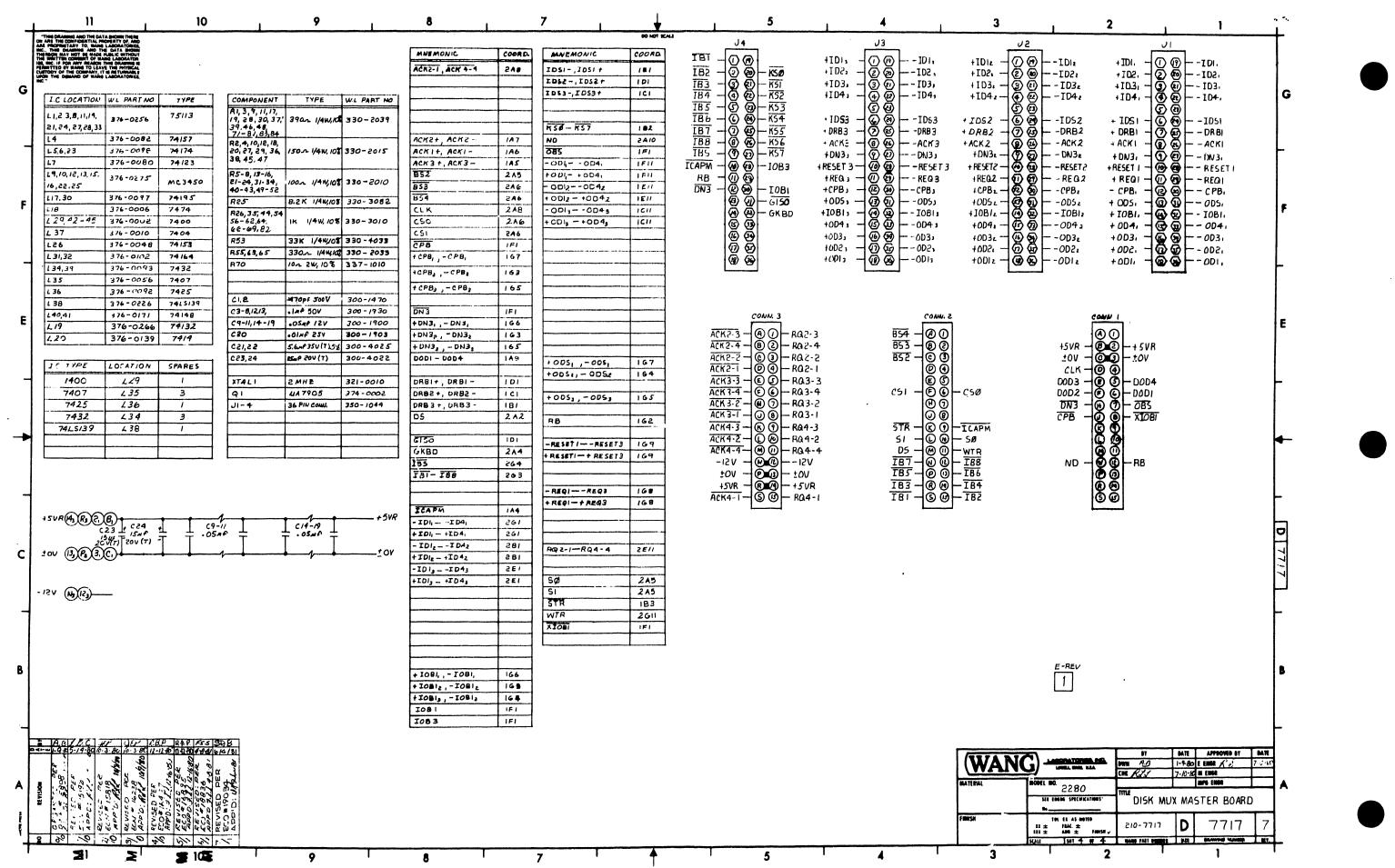


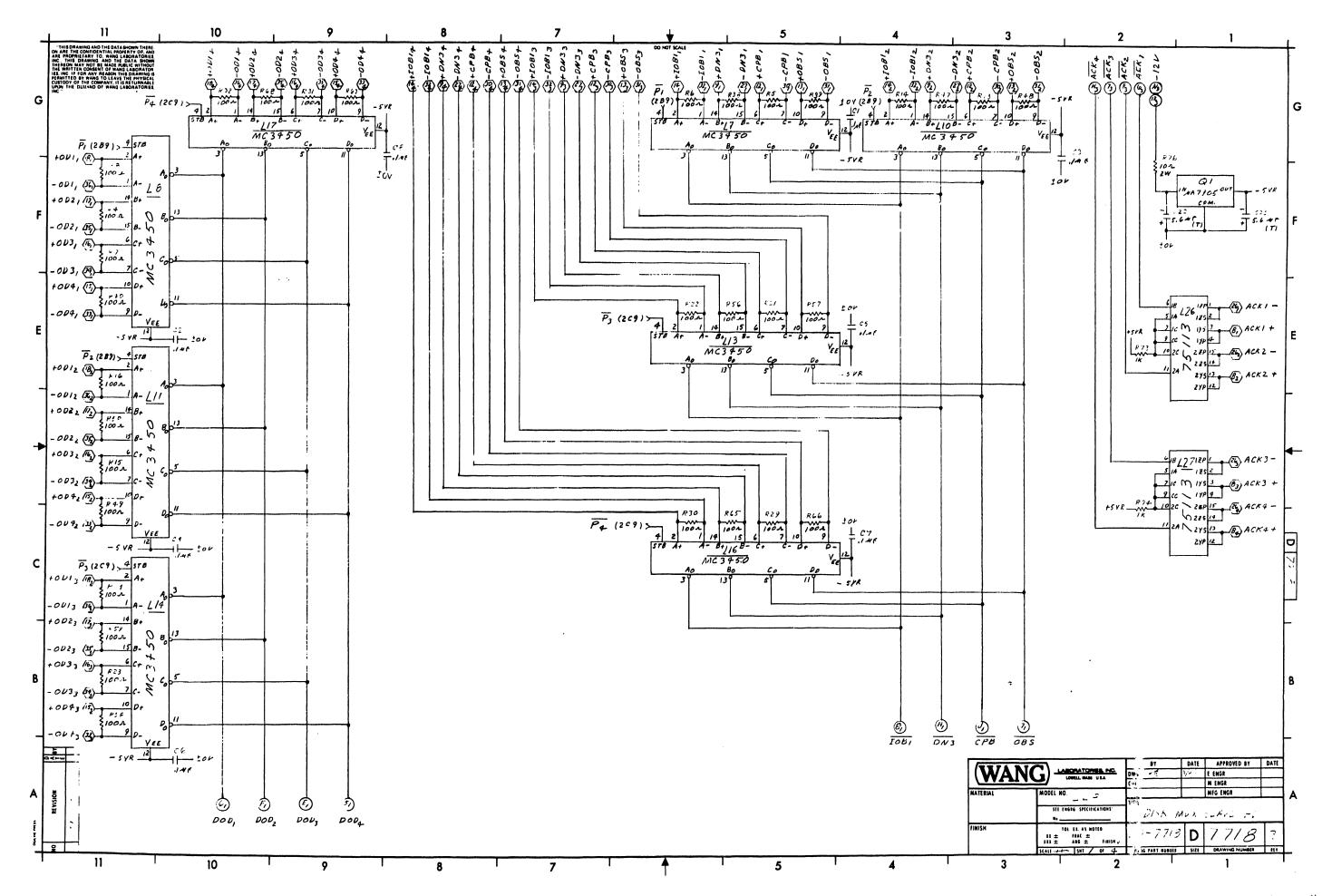


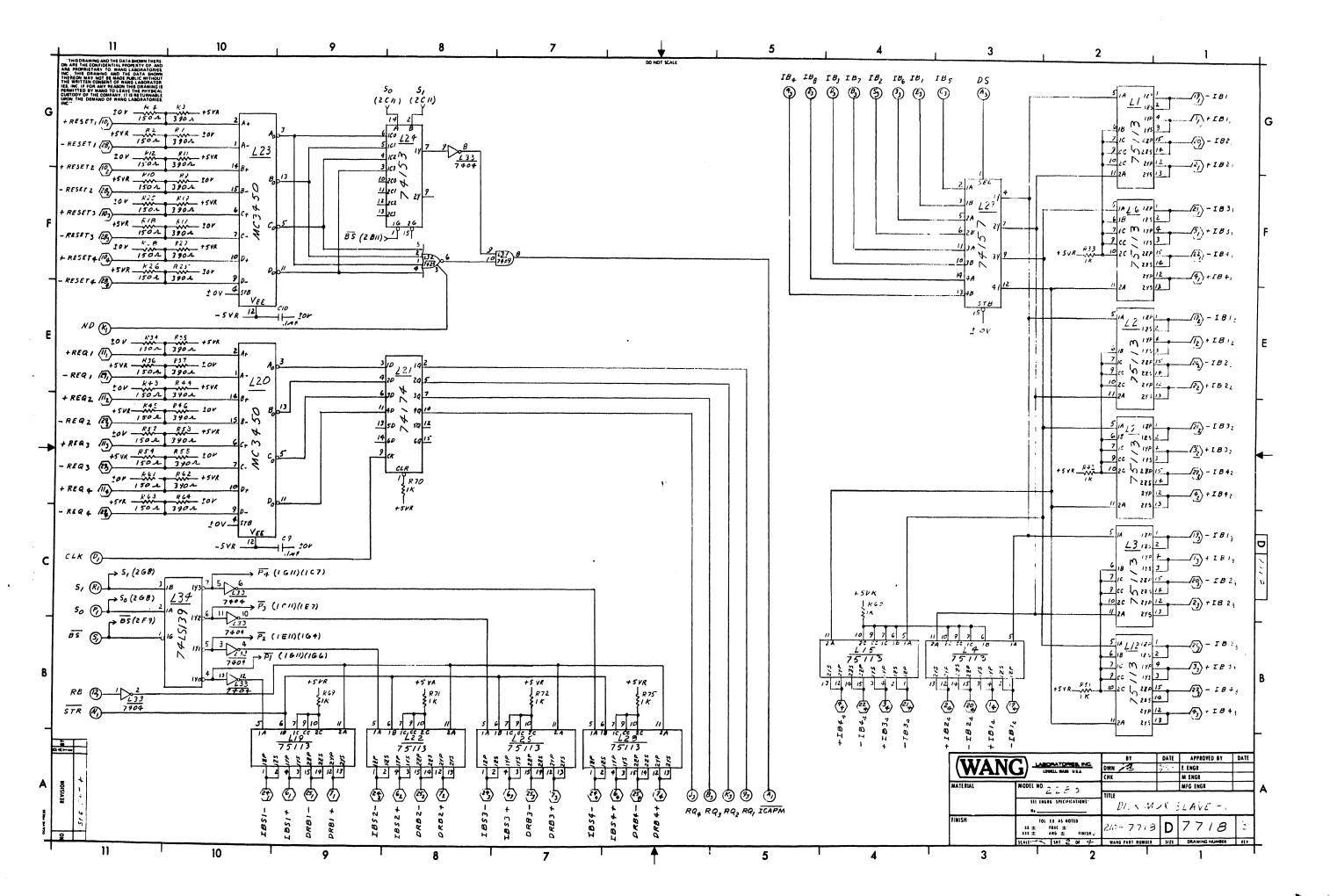


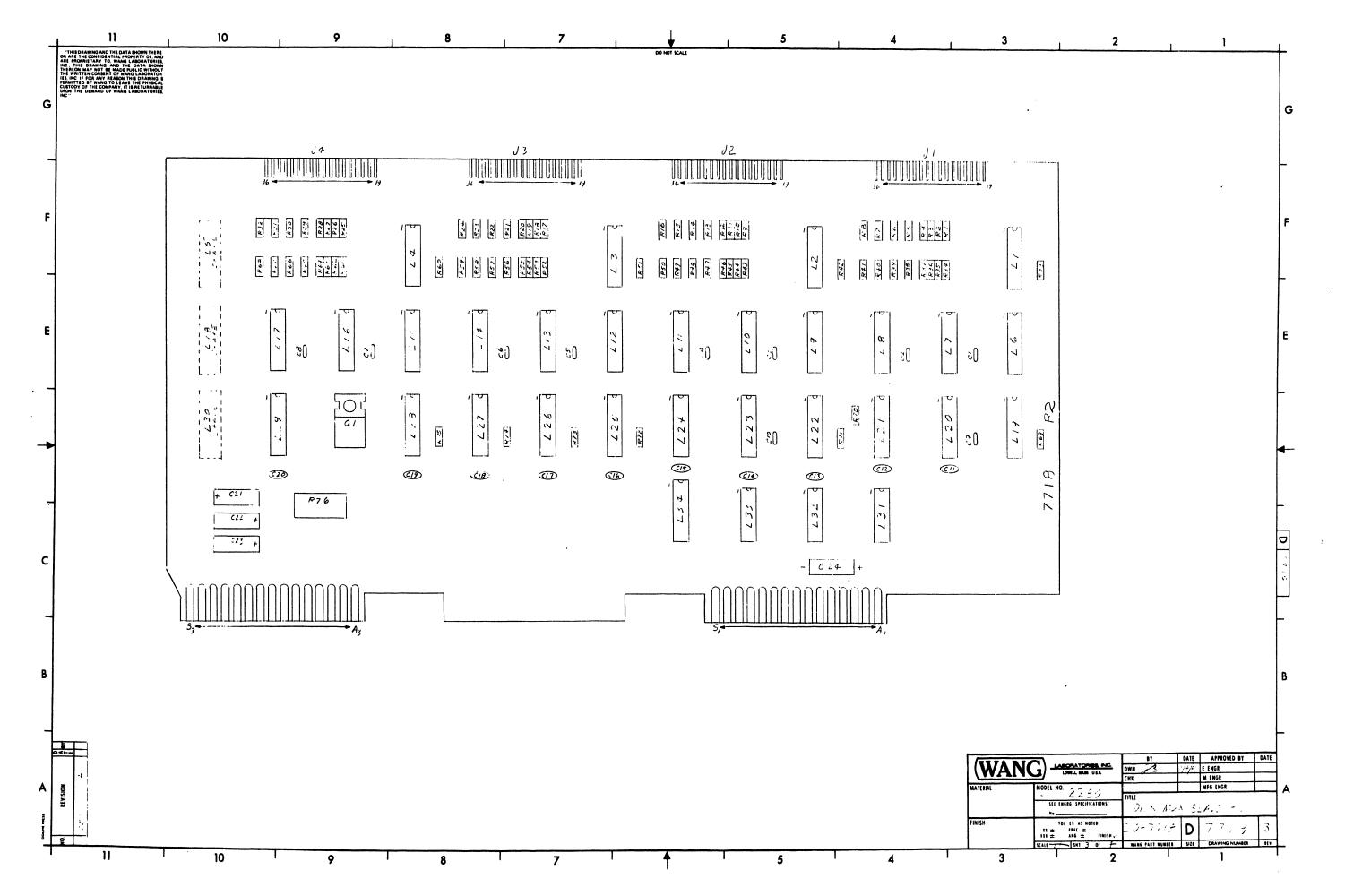


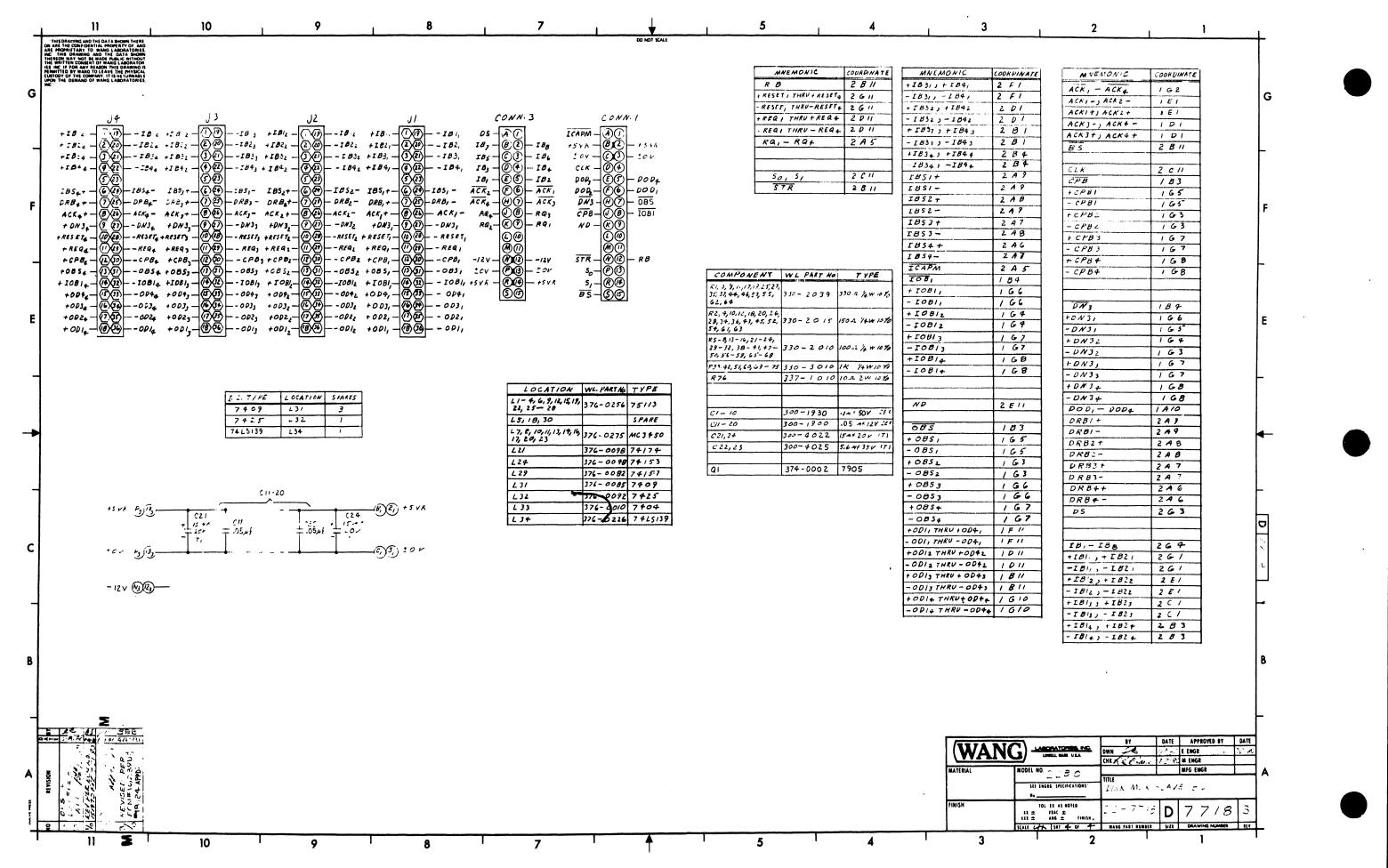


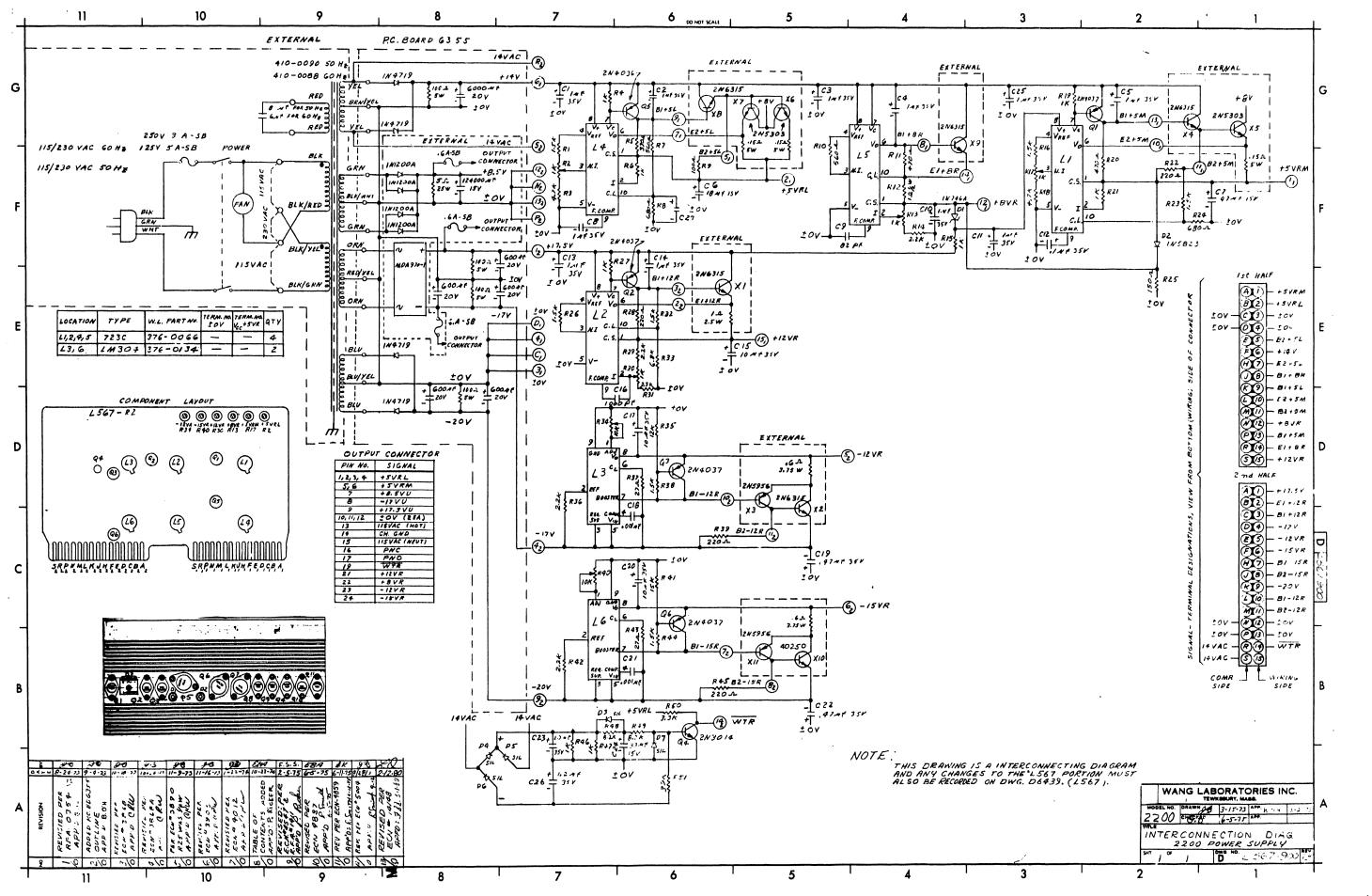












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